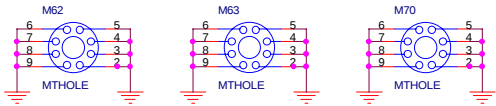
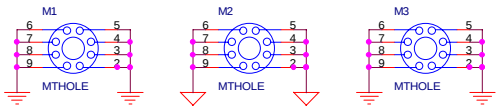
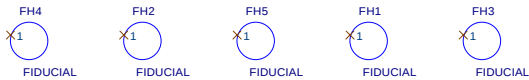
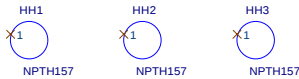


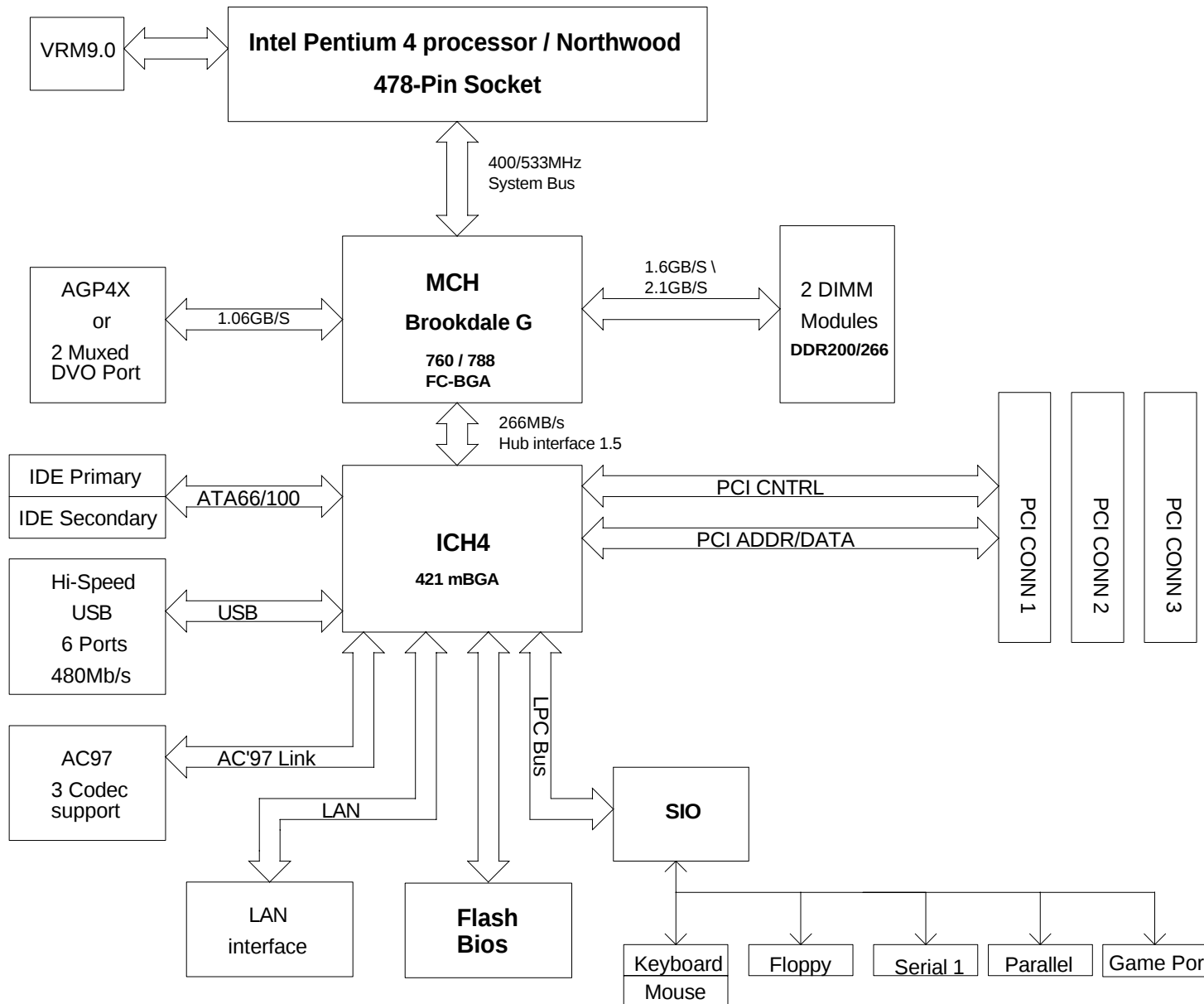
CH845G

REV:2.0

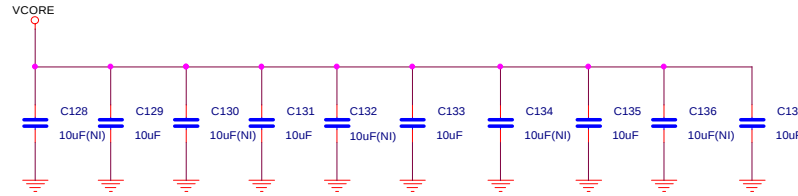
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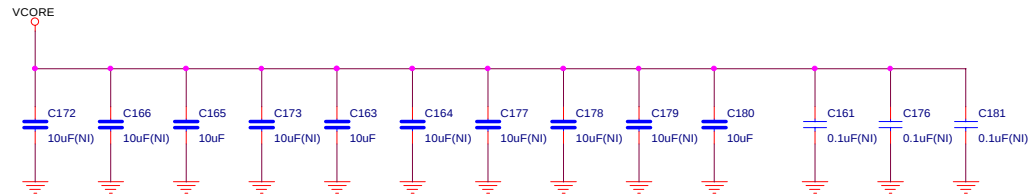
Block Diagram



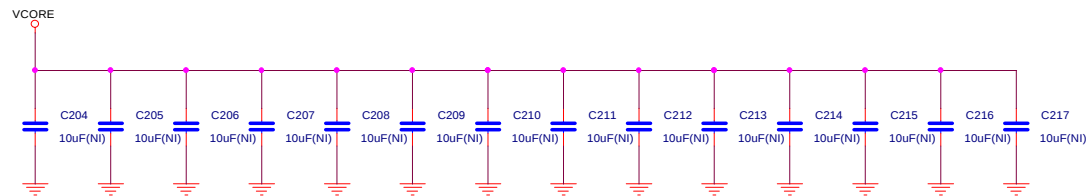
Put on the North side of the processor



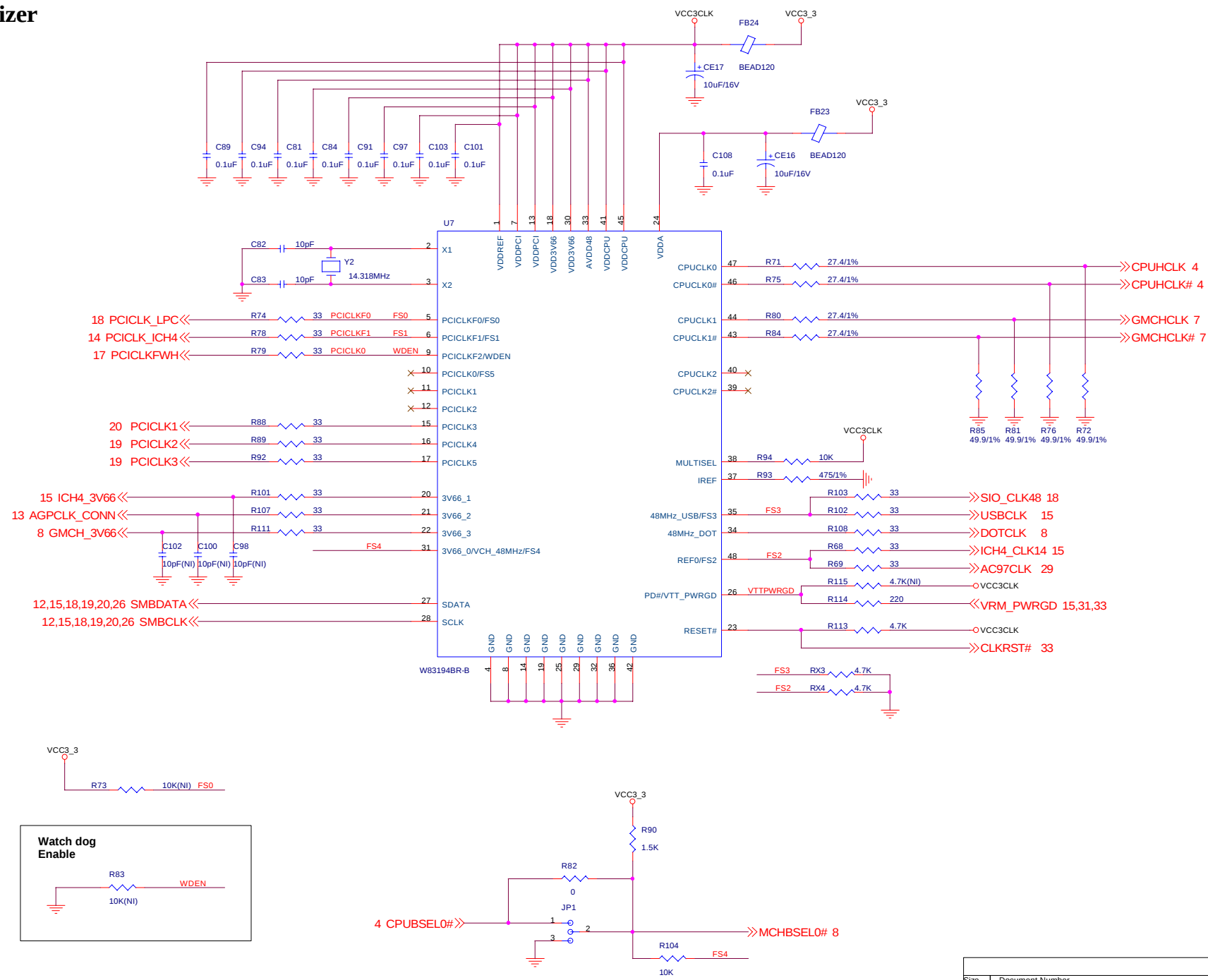
Put in the processor cavity

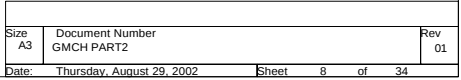


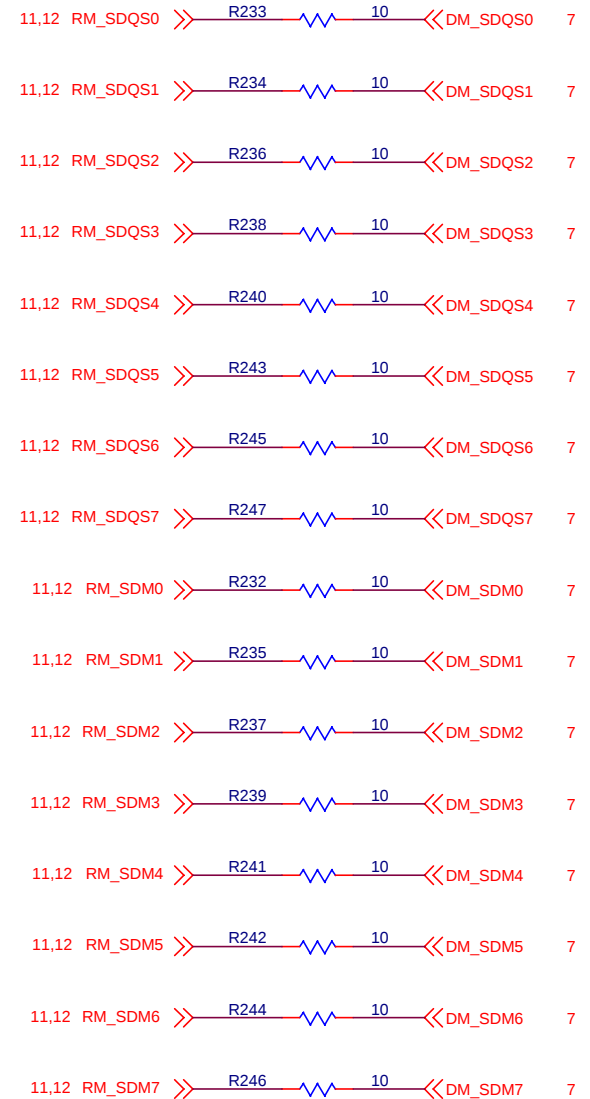
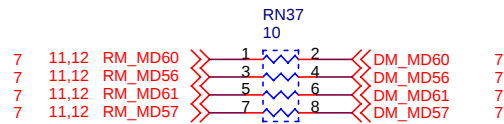
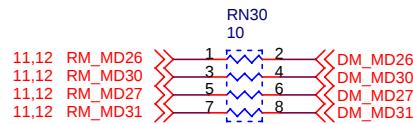
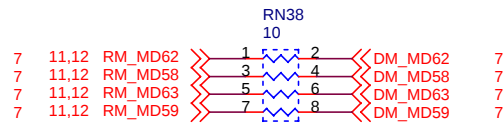
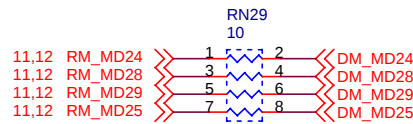
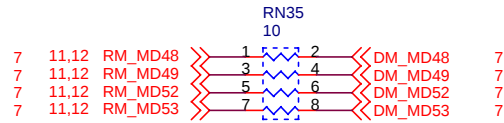
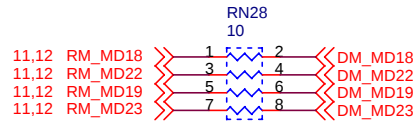
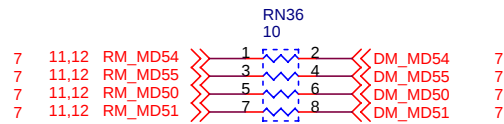
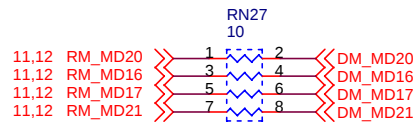
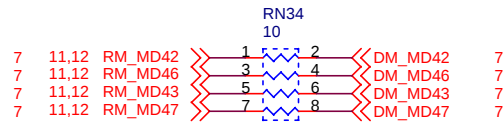
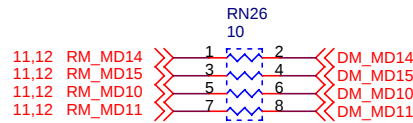
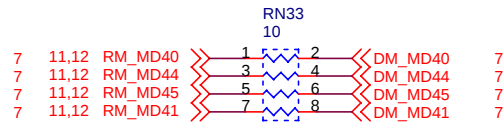
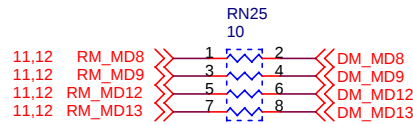
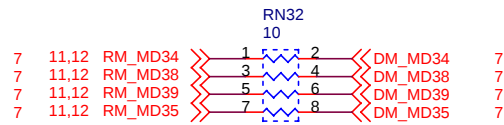
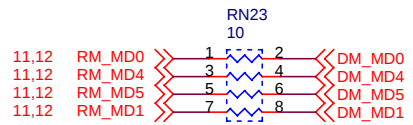
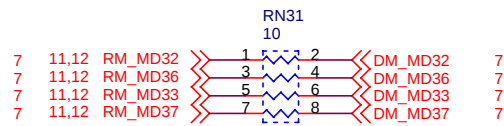
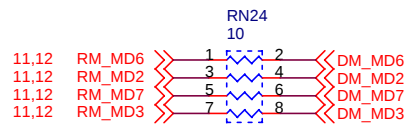
Put on the South side of the processor

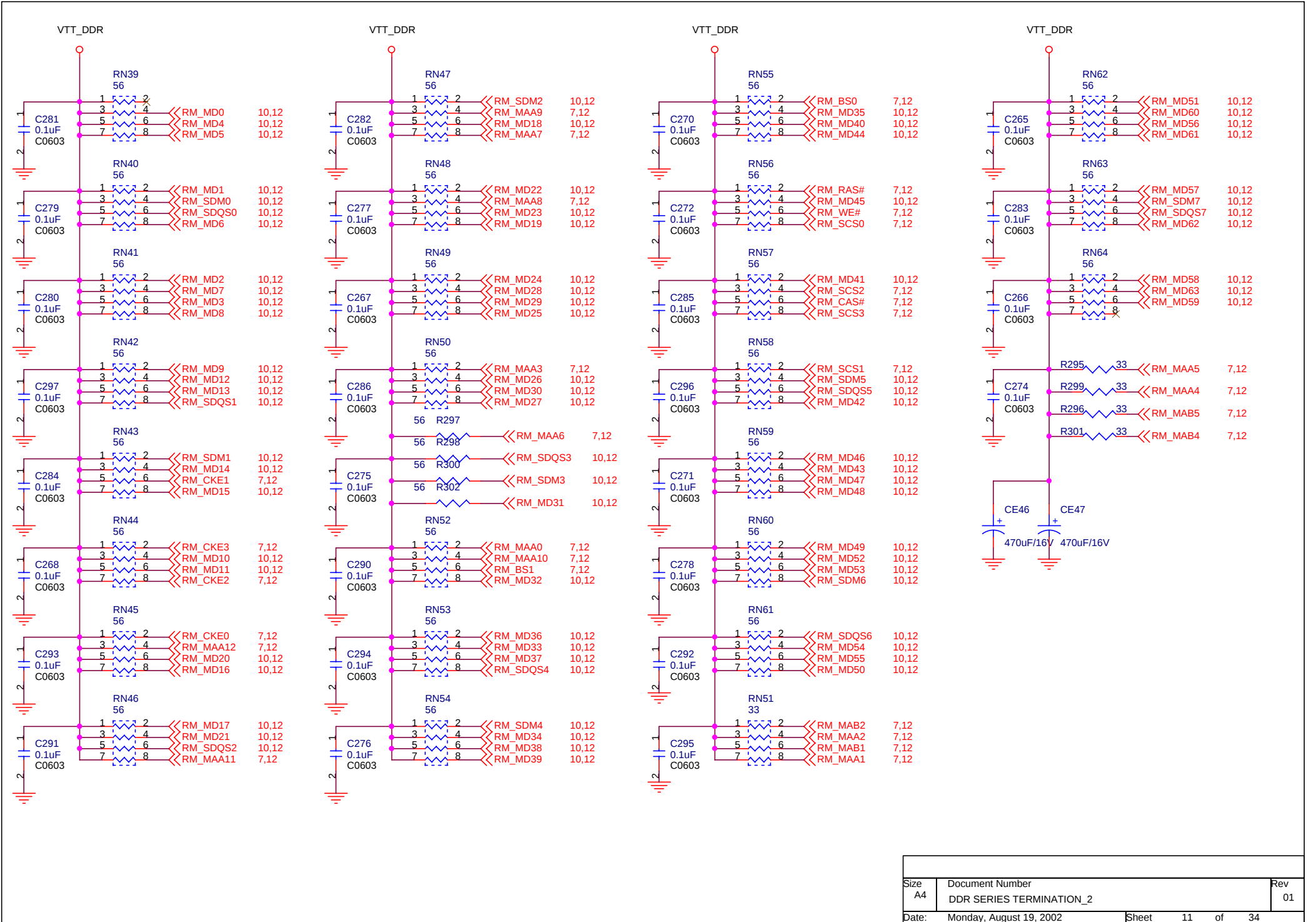


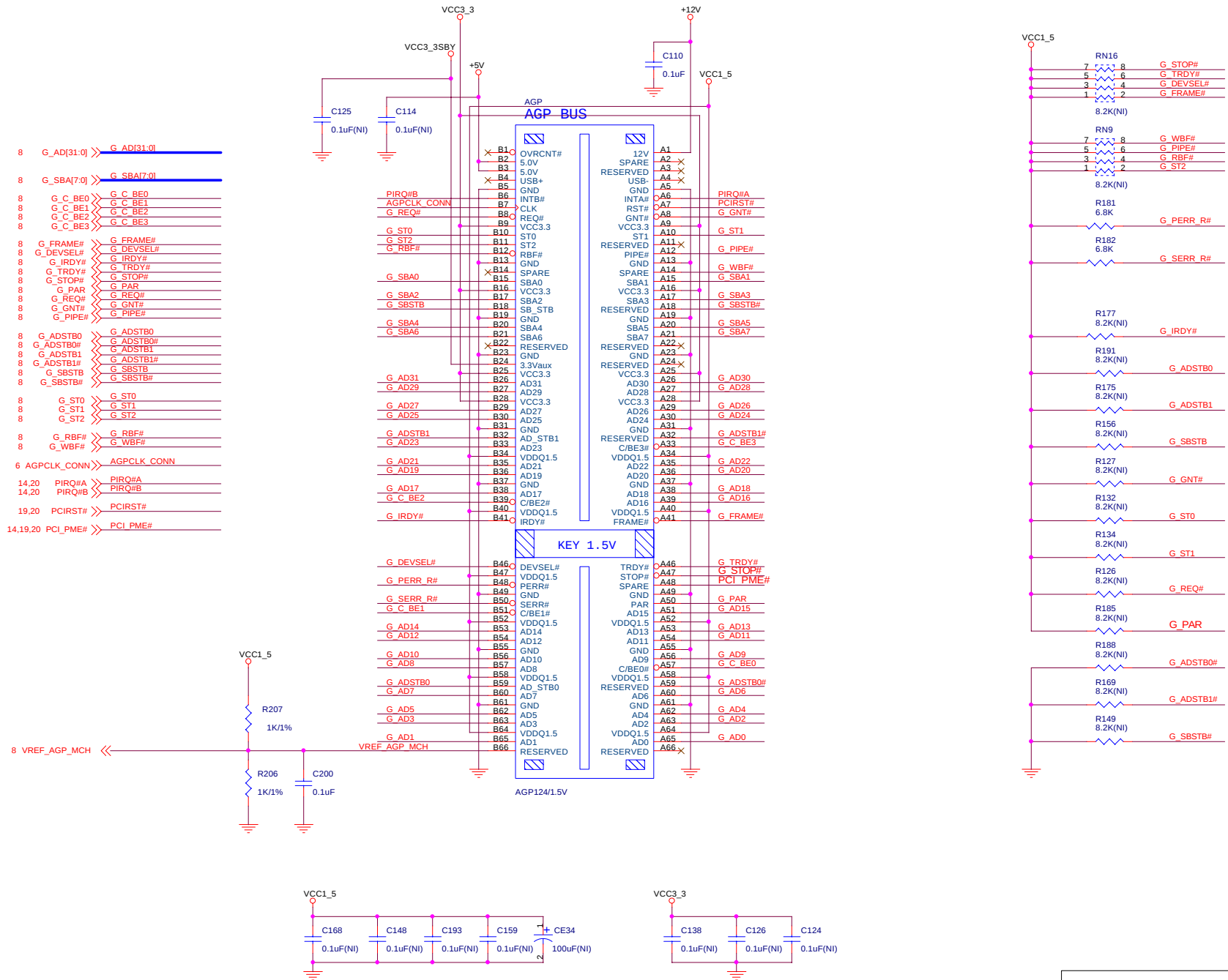
Clock Synthesizer



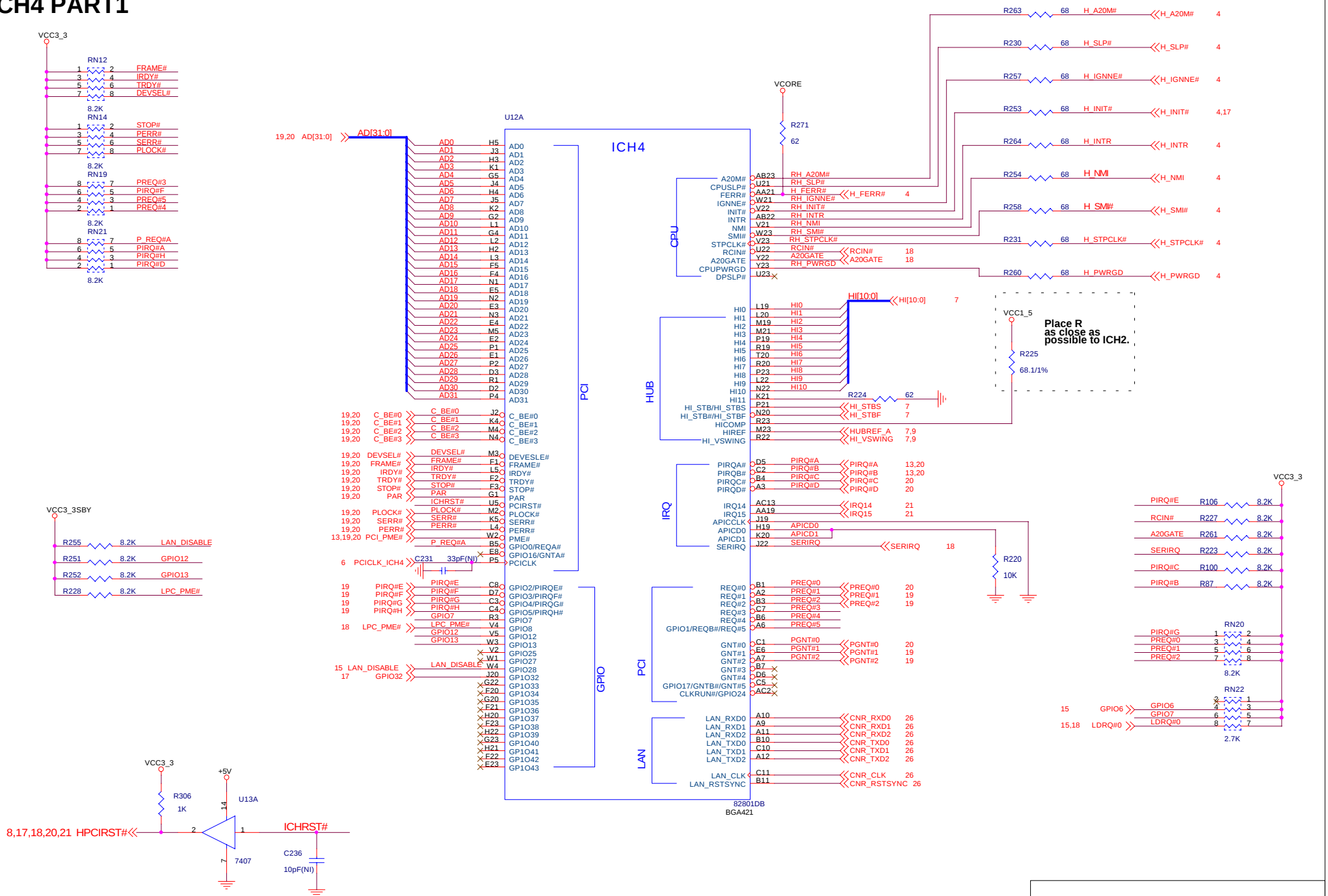




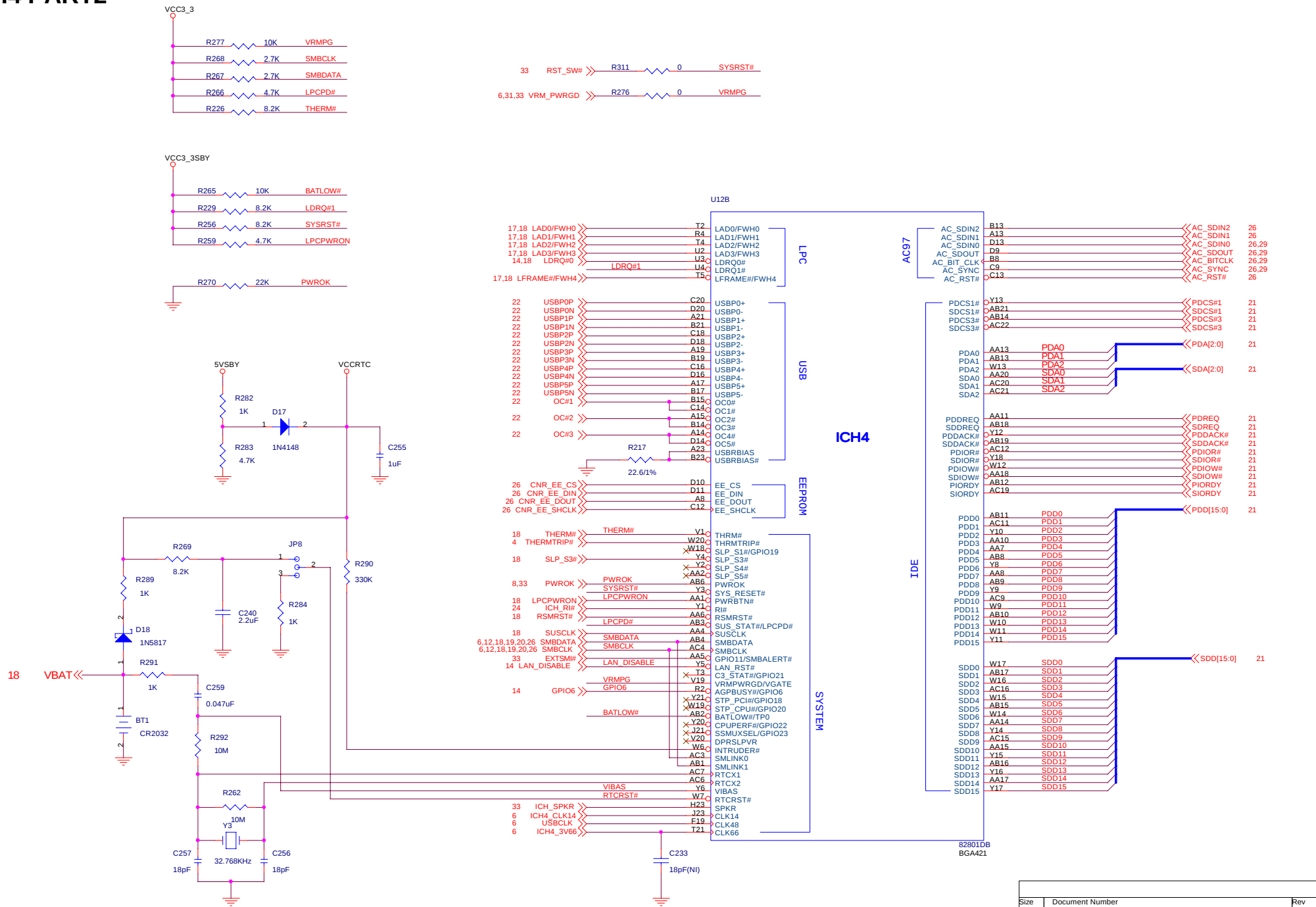




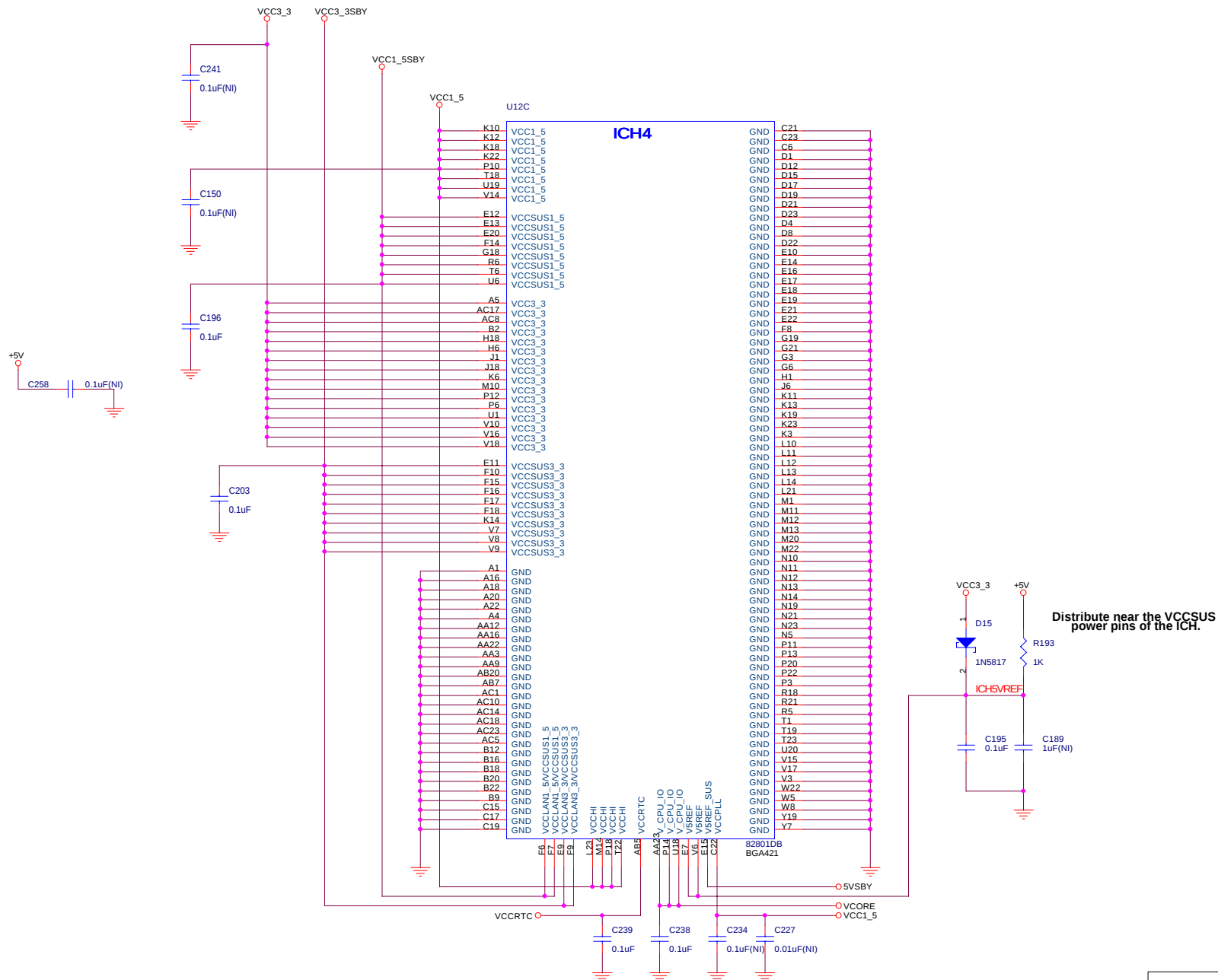
ICH4 PART1



ICH4 PART2



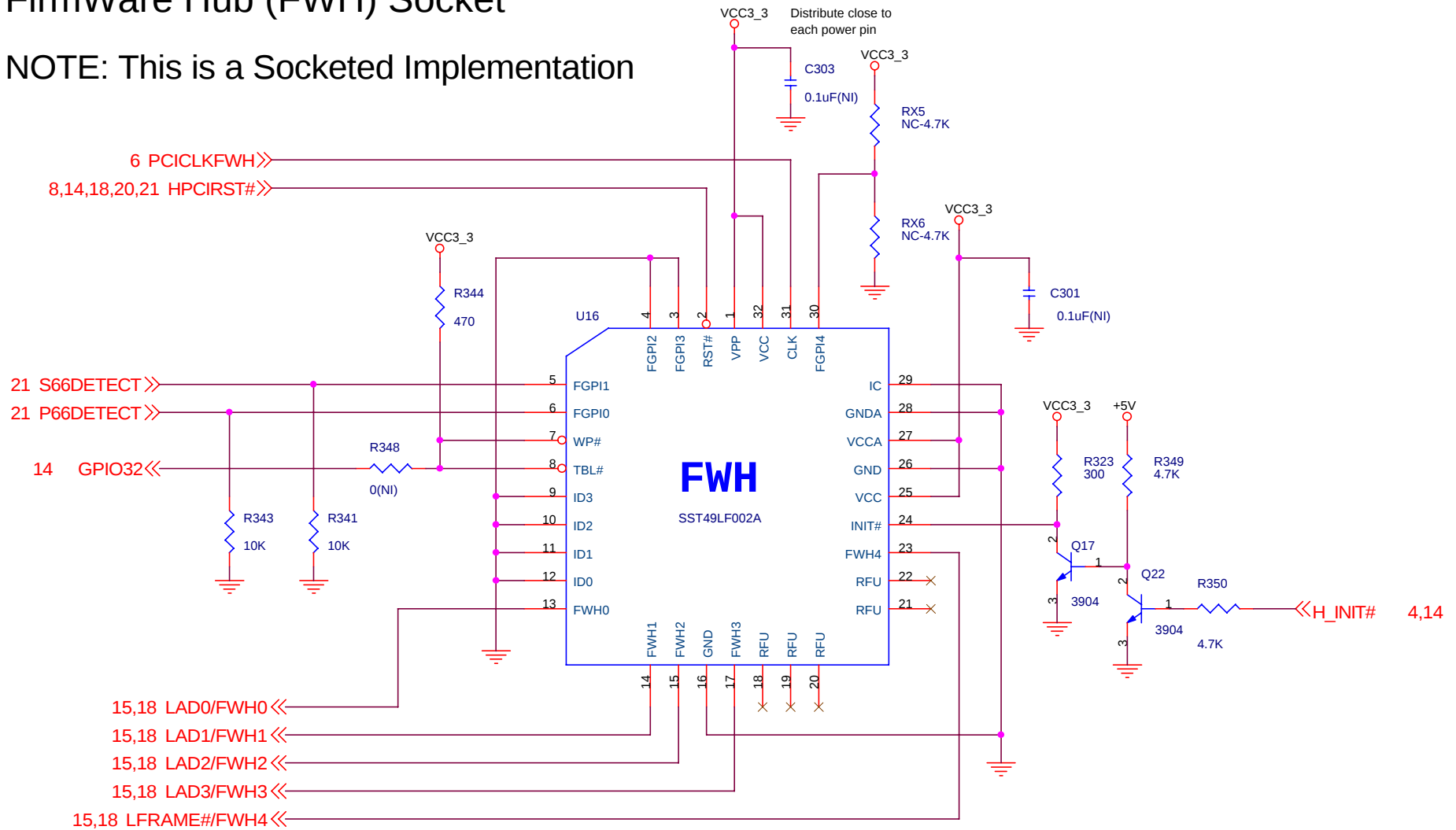
ICH4 PART3

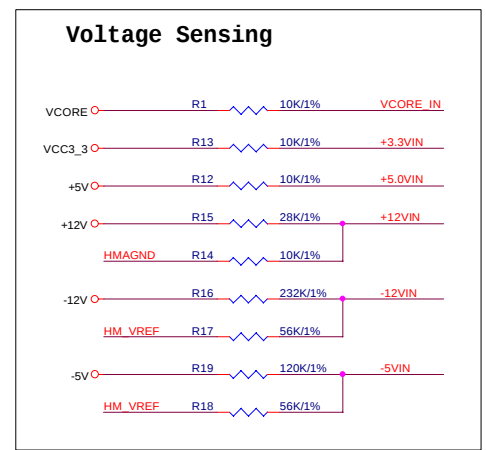
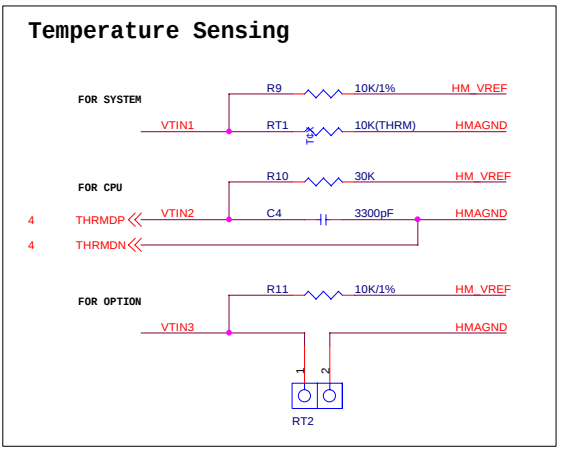
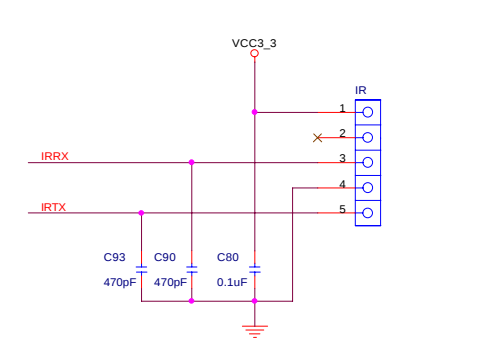
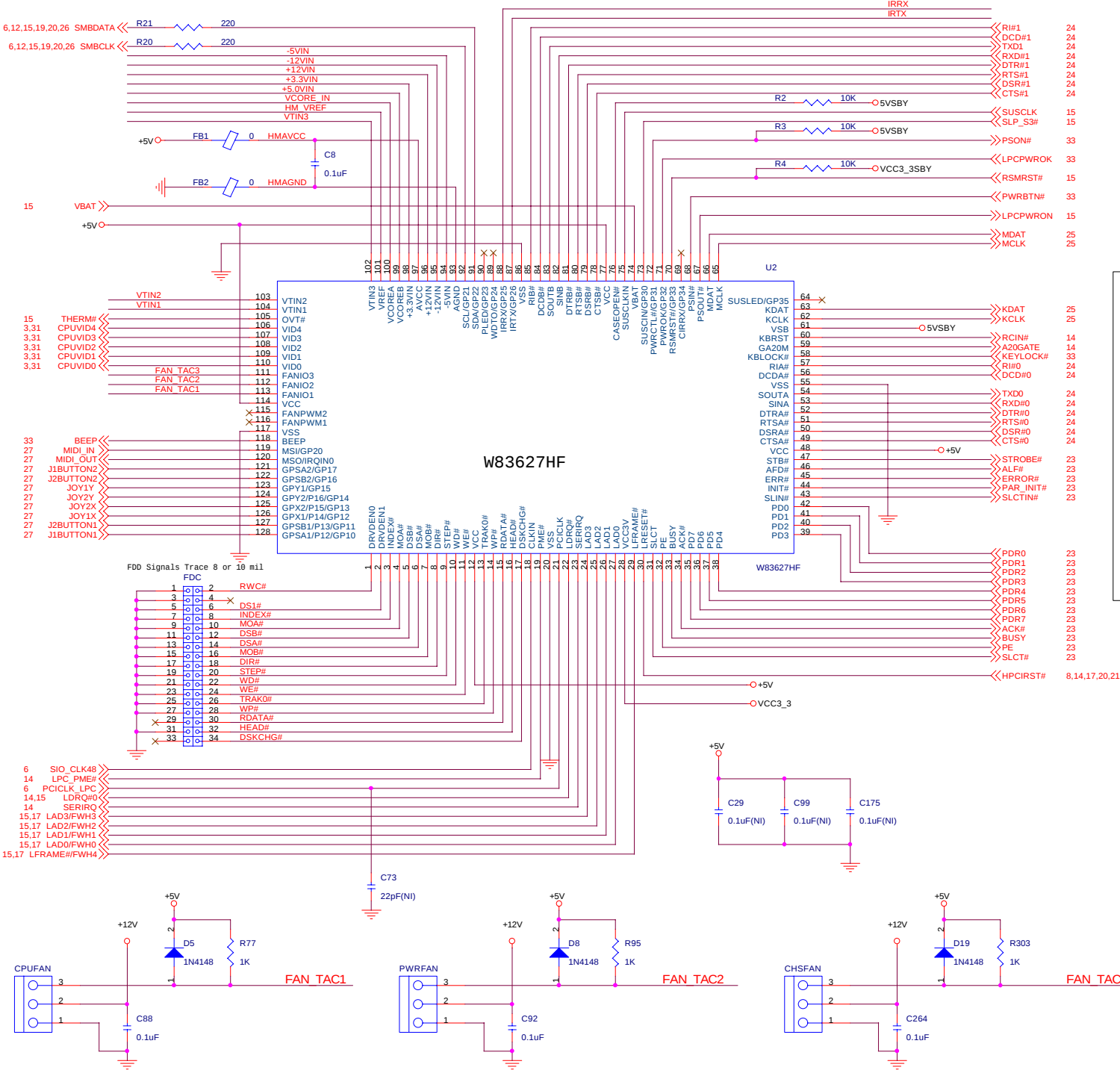


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FirmWare Hub (FWH) Socket

NOTE: This is a Socketed Implementation

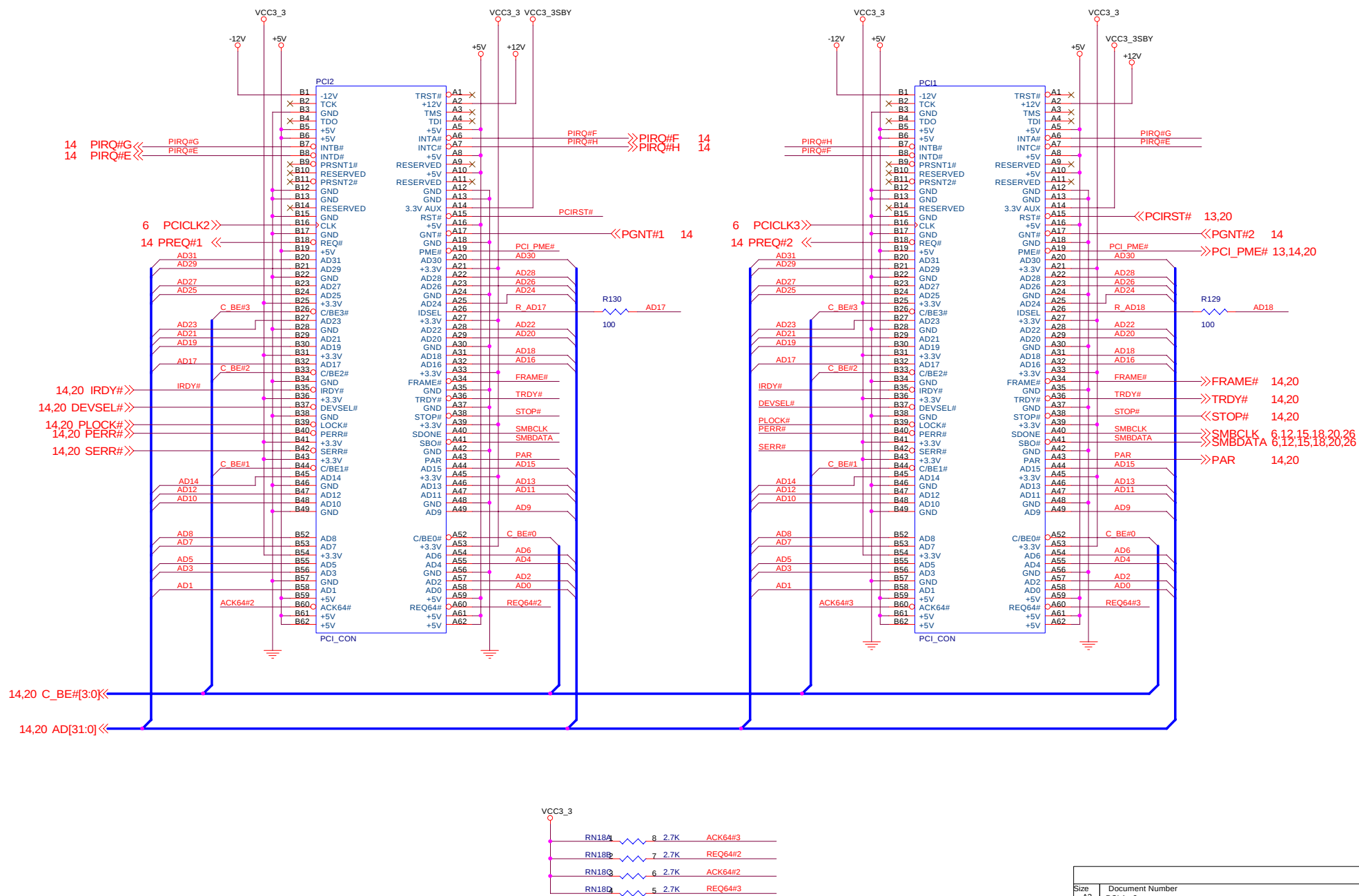




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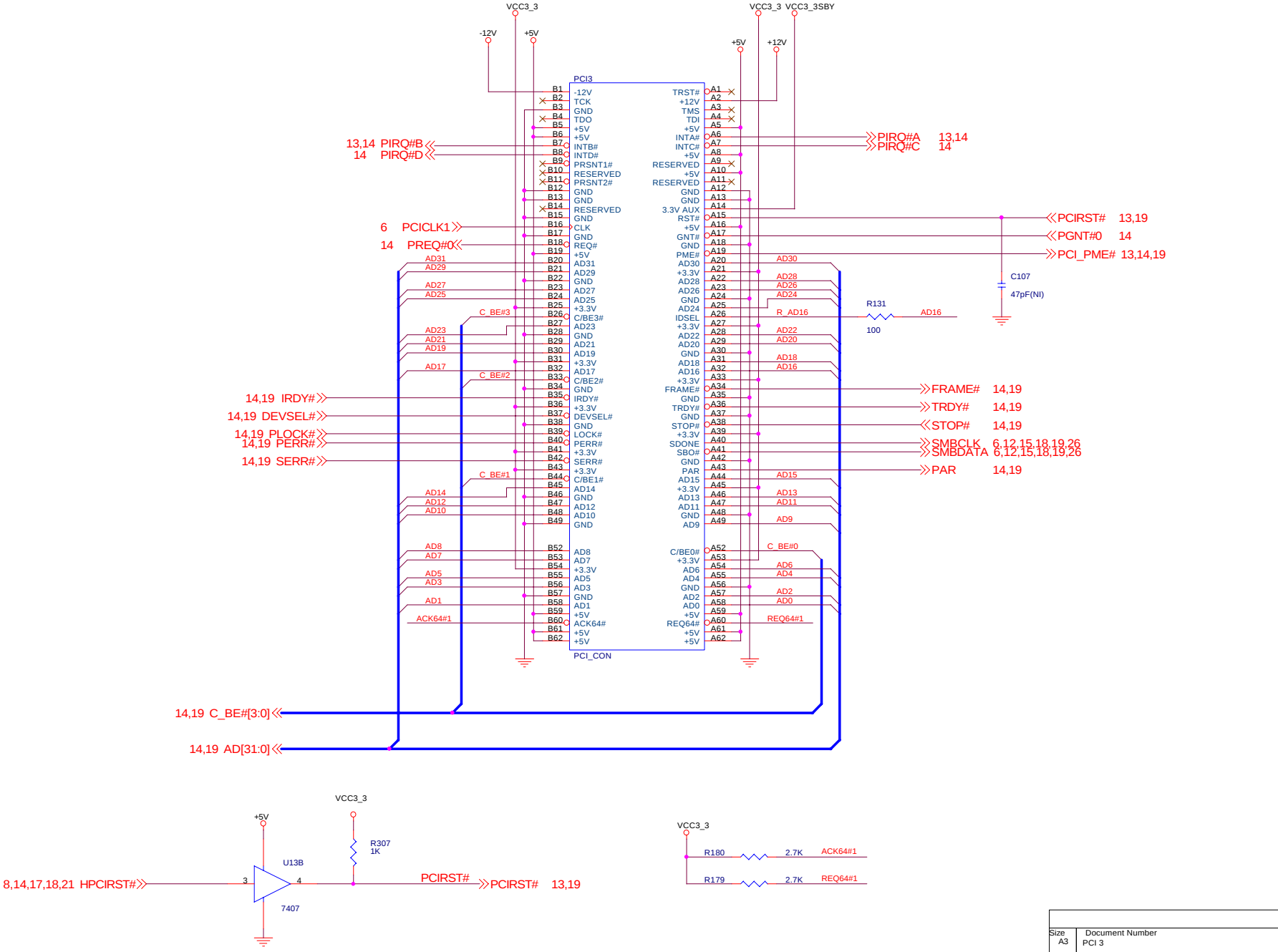
PCI CONNECTORS

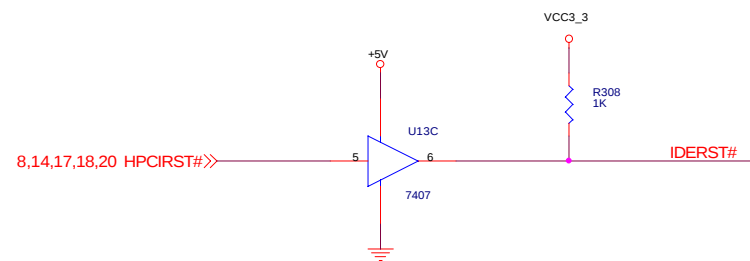
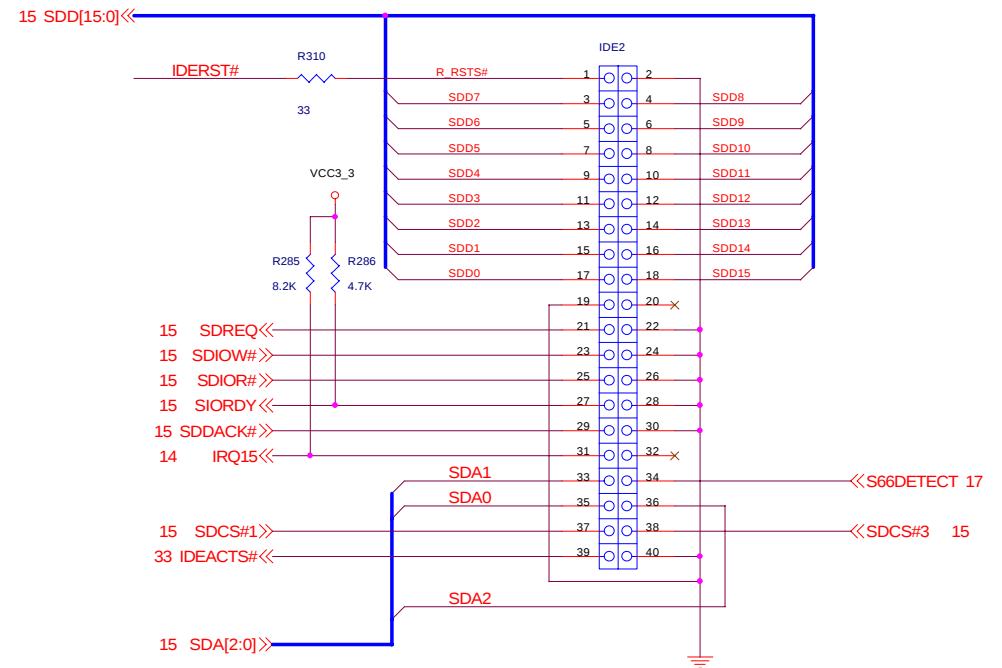
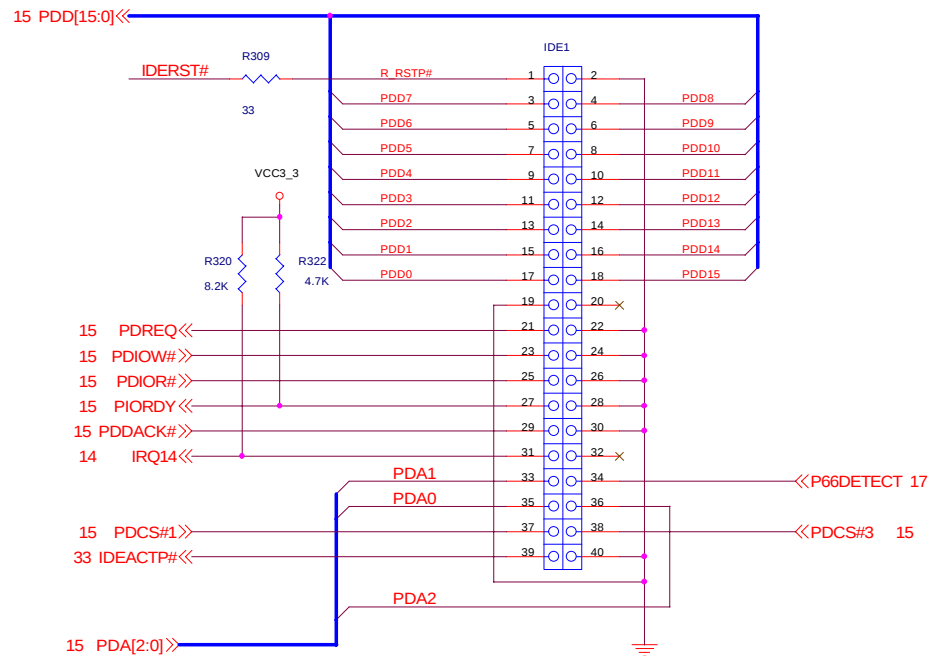
1 AND 2

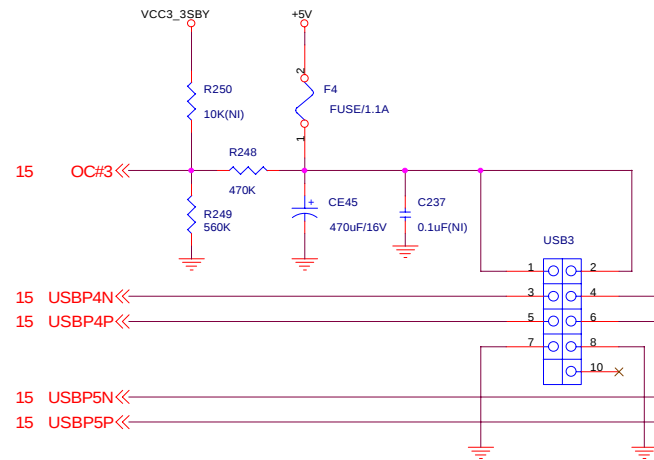
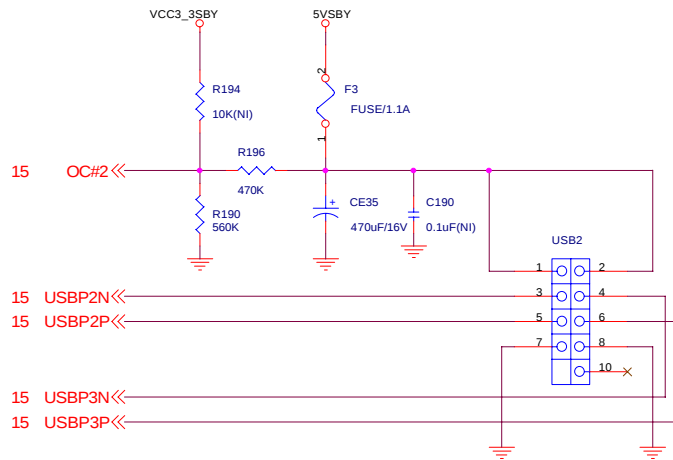
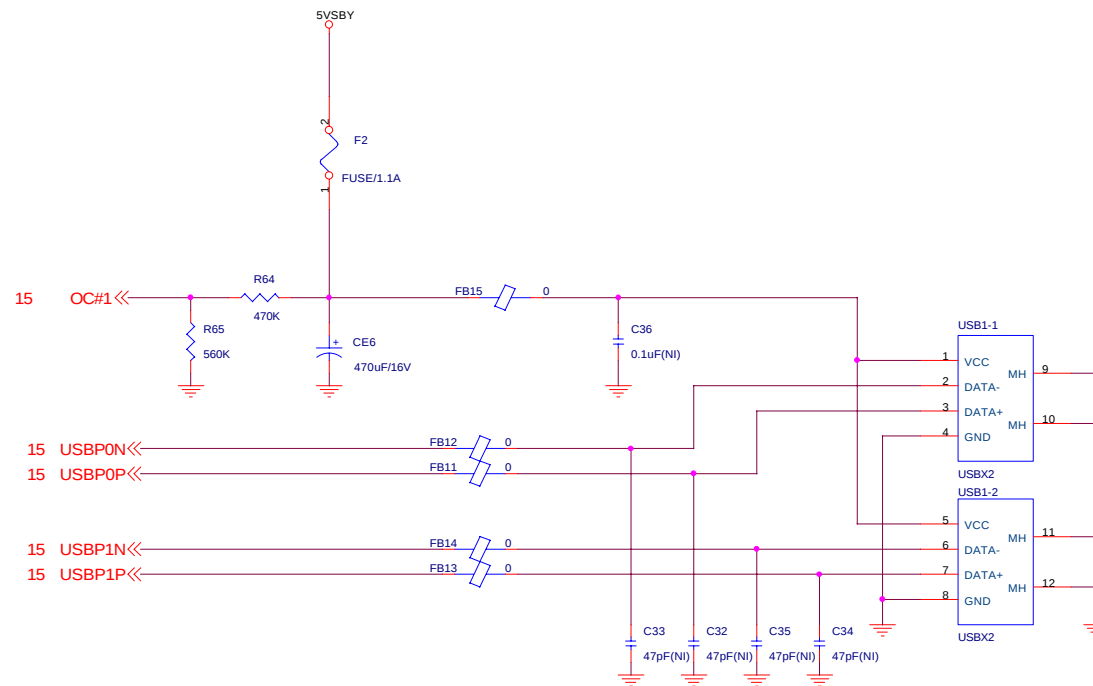


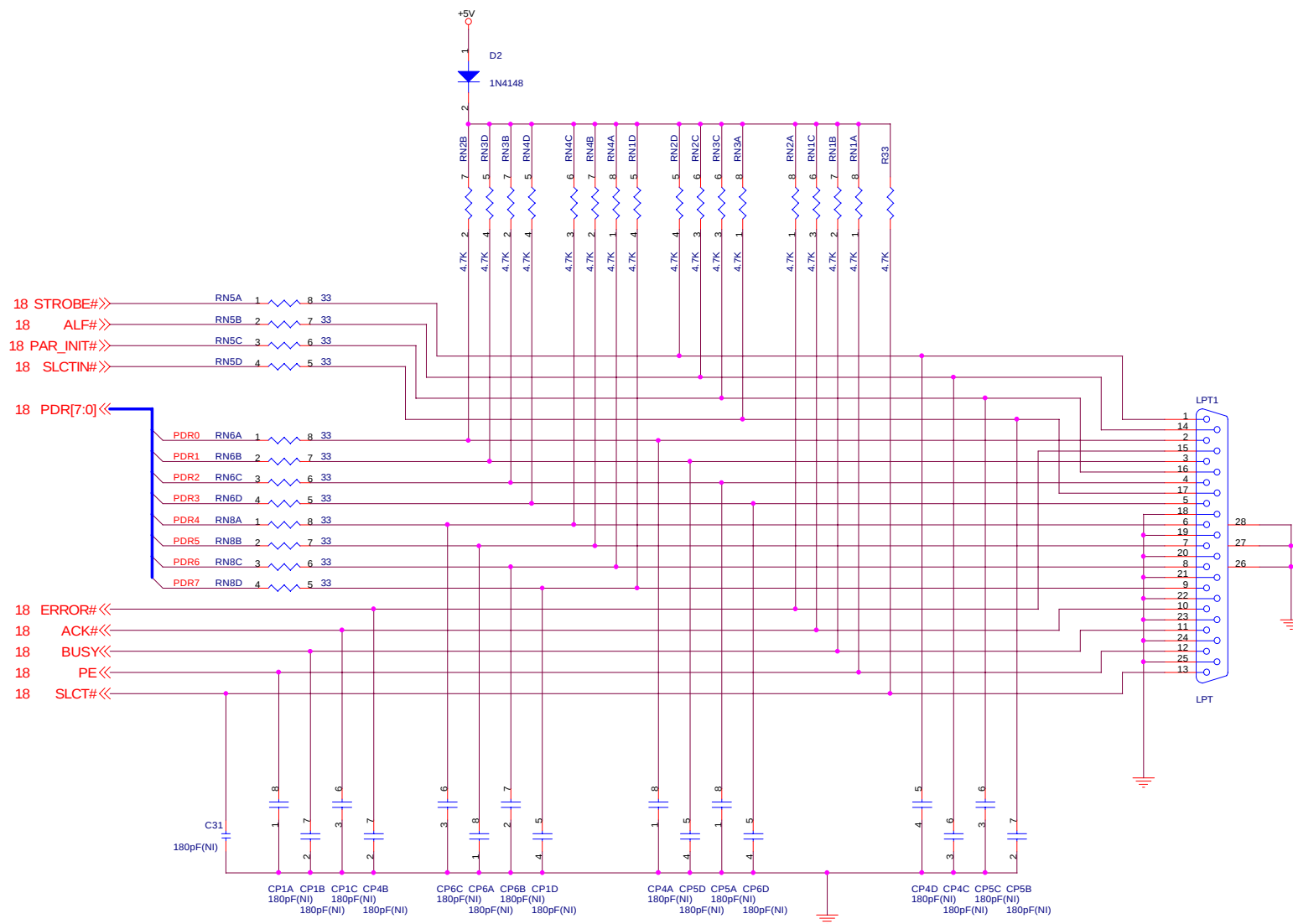
Size	Document Number	Rev
A3	PCI 1, 2	01
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PCI CONNECTORS 3

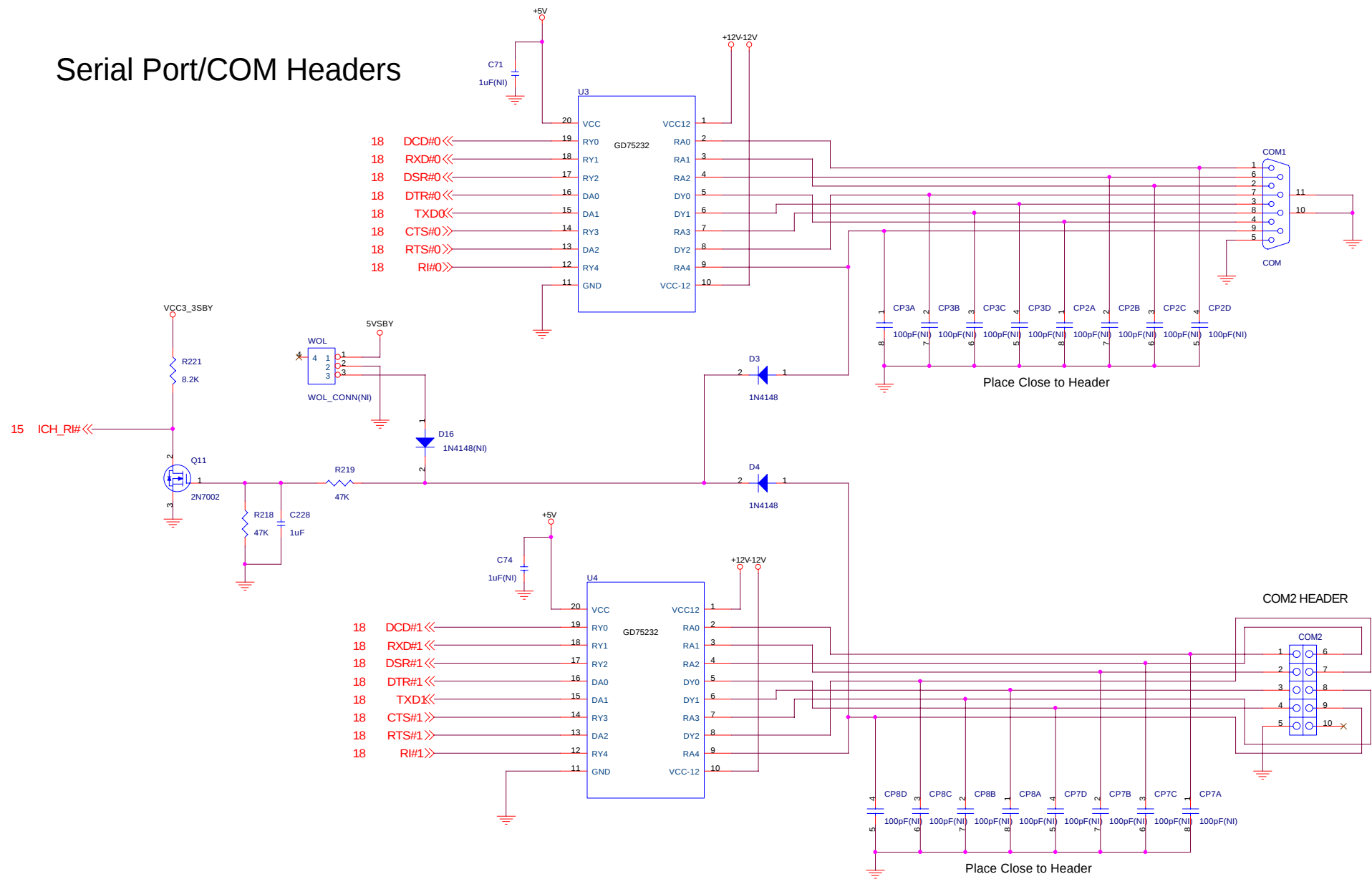


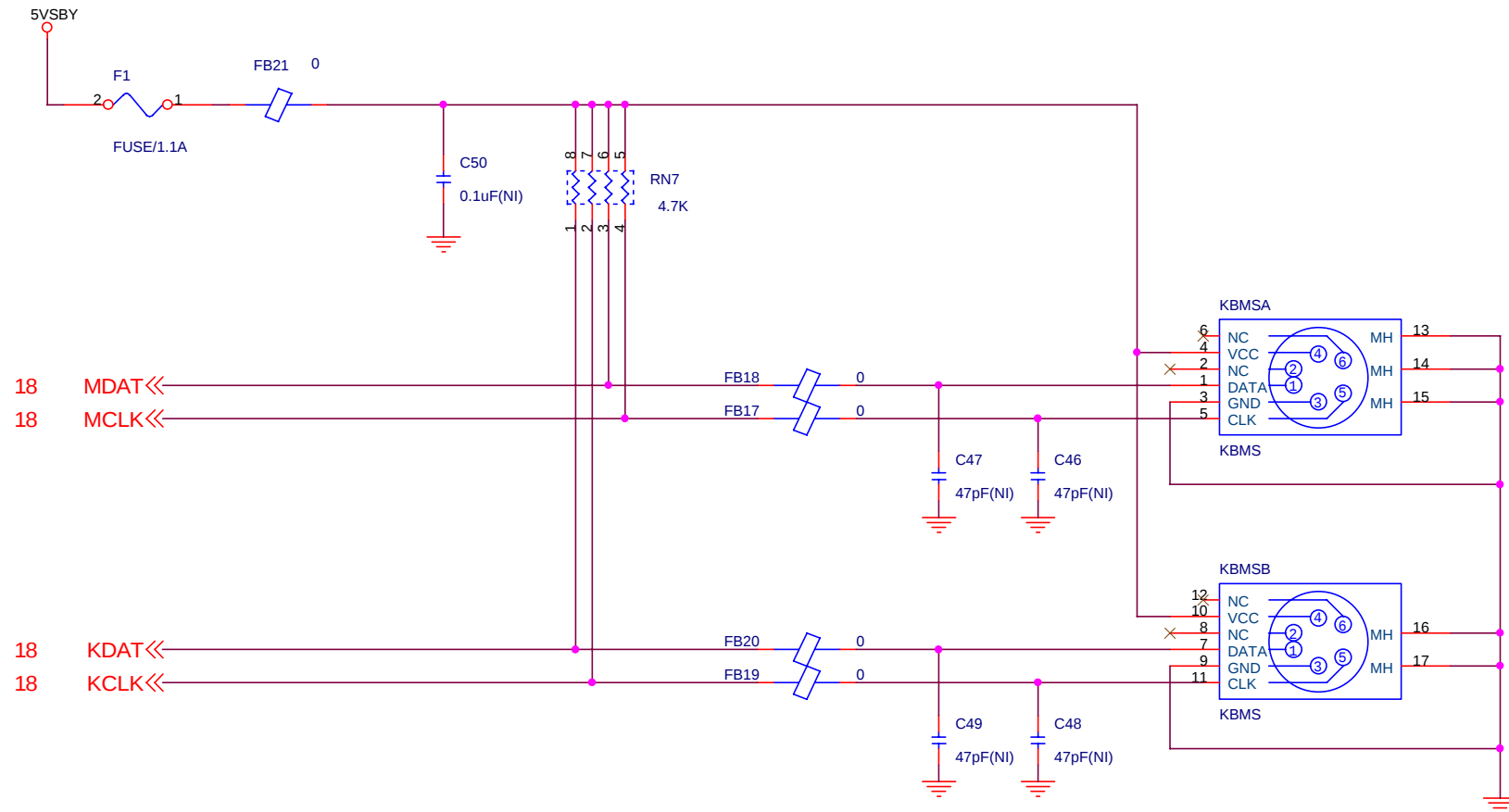


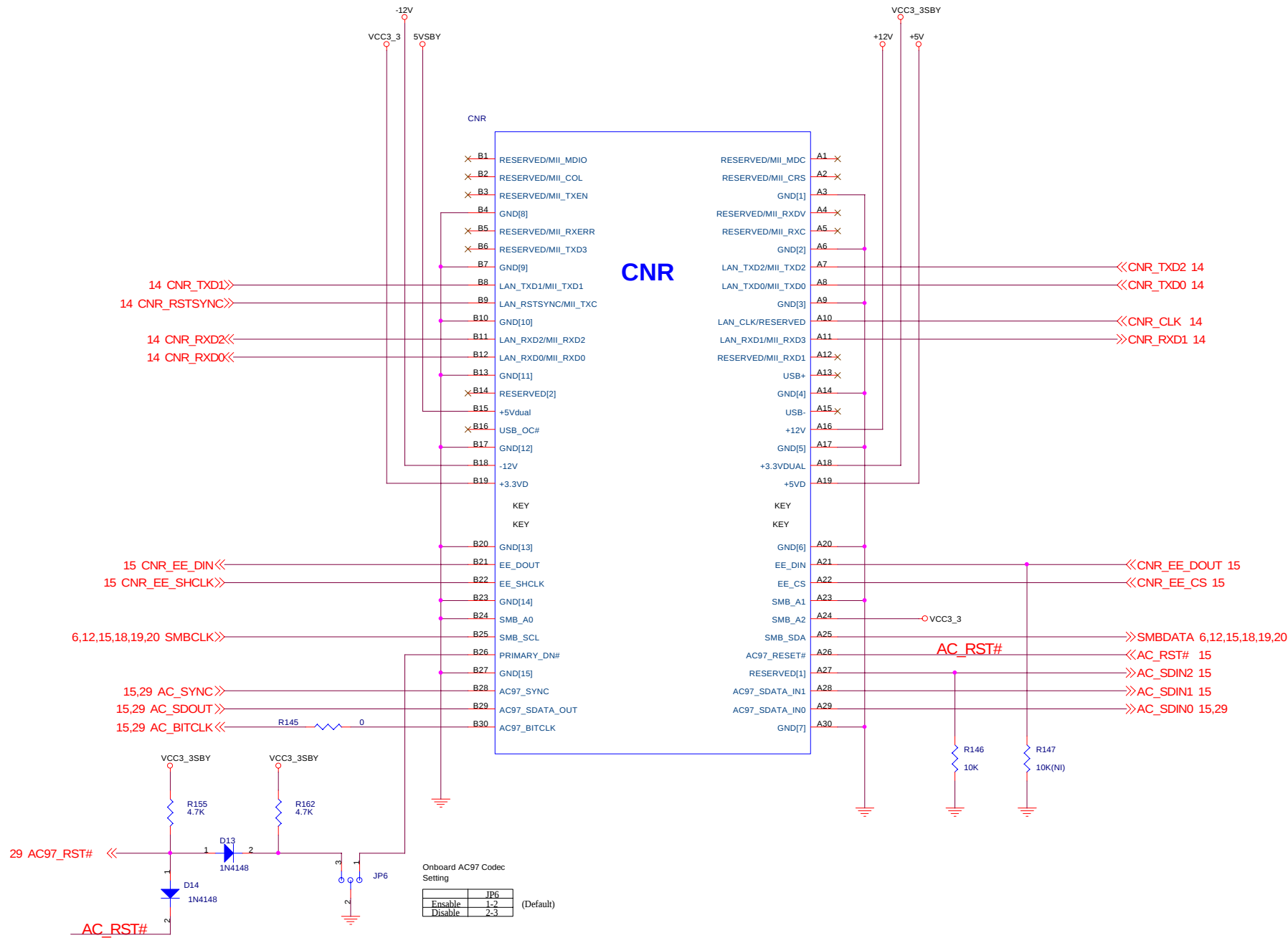


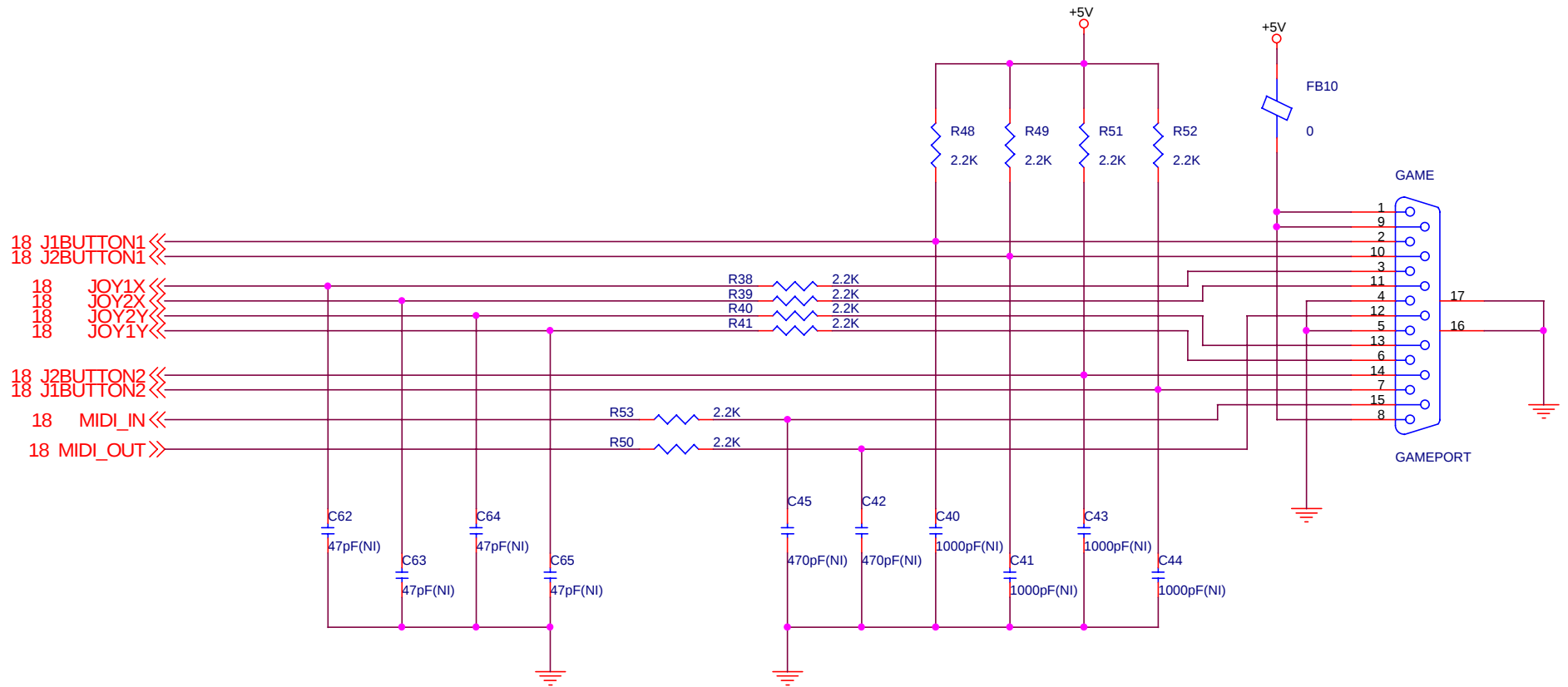


Serial Port/COM Headers

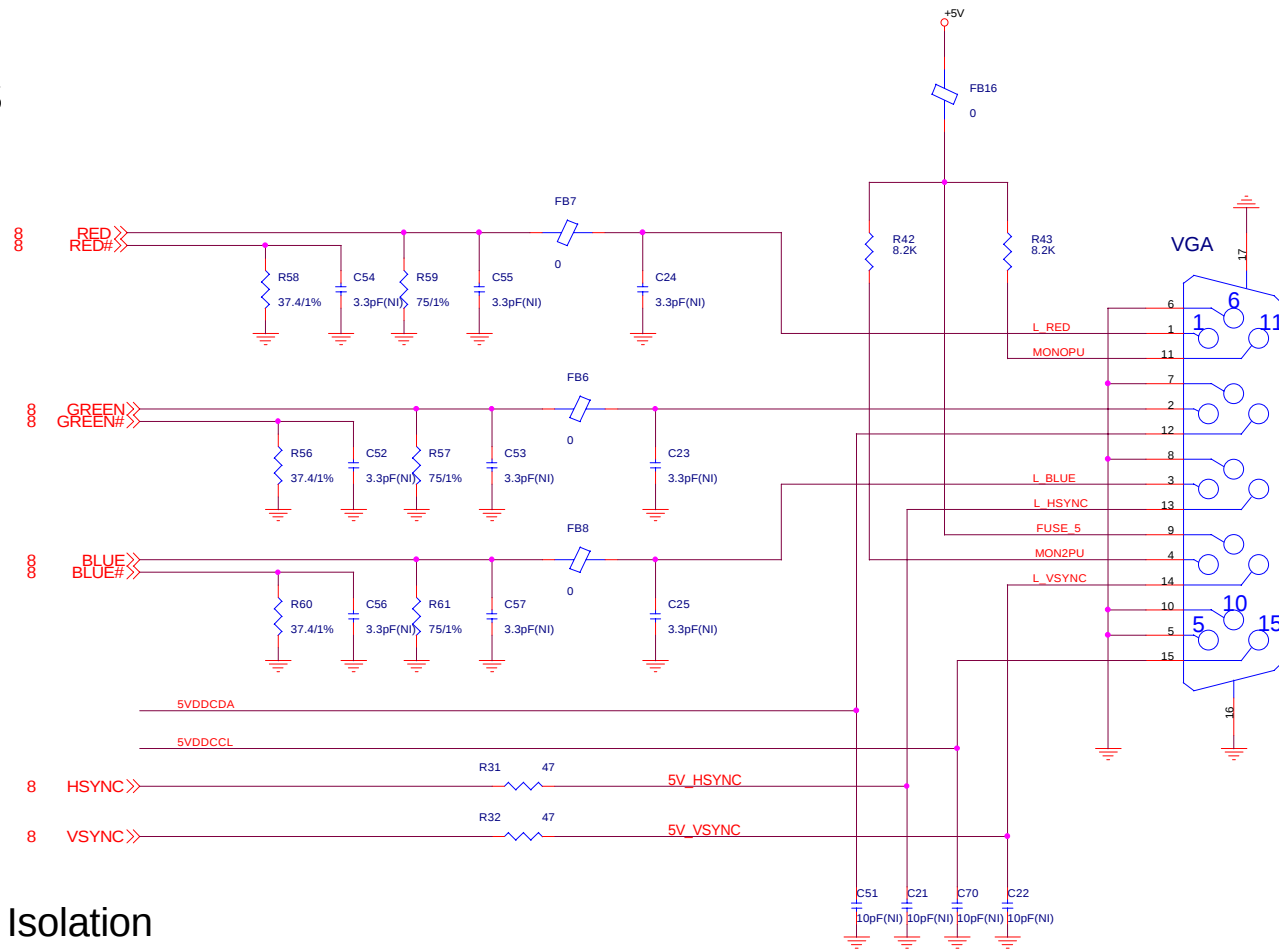




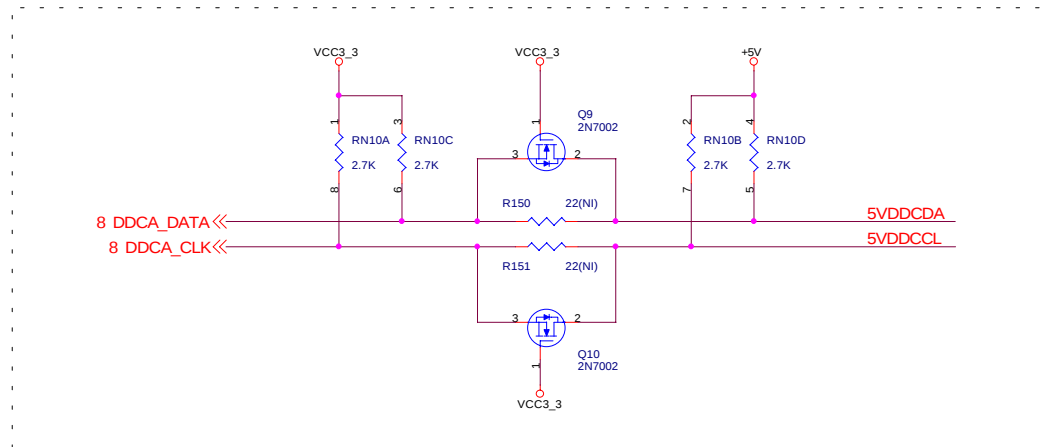


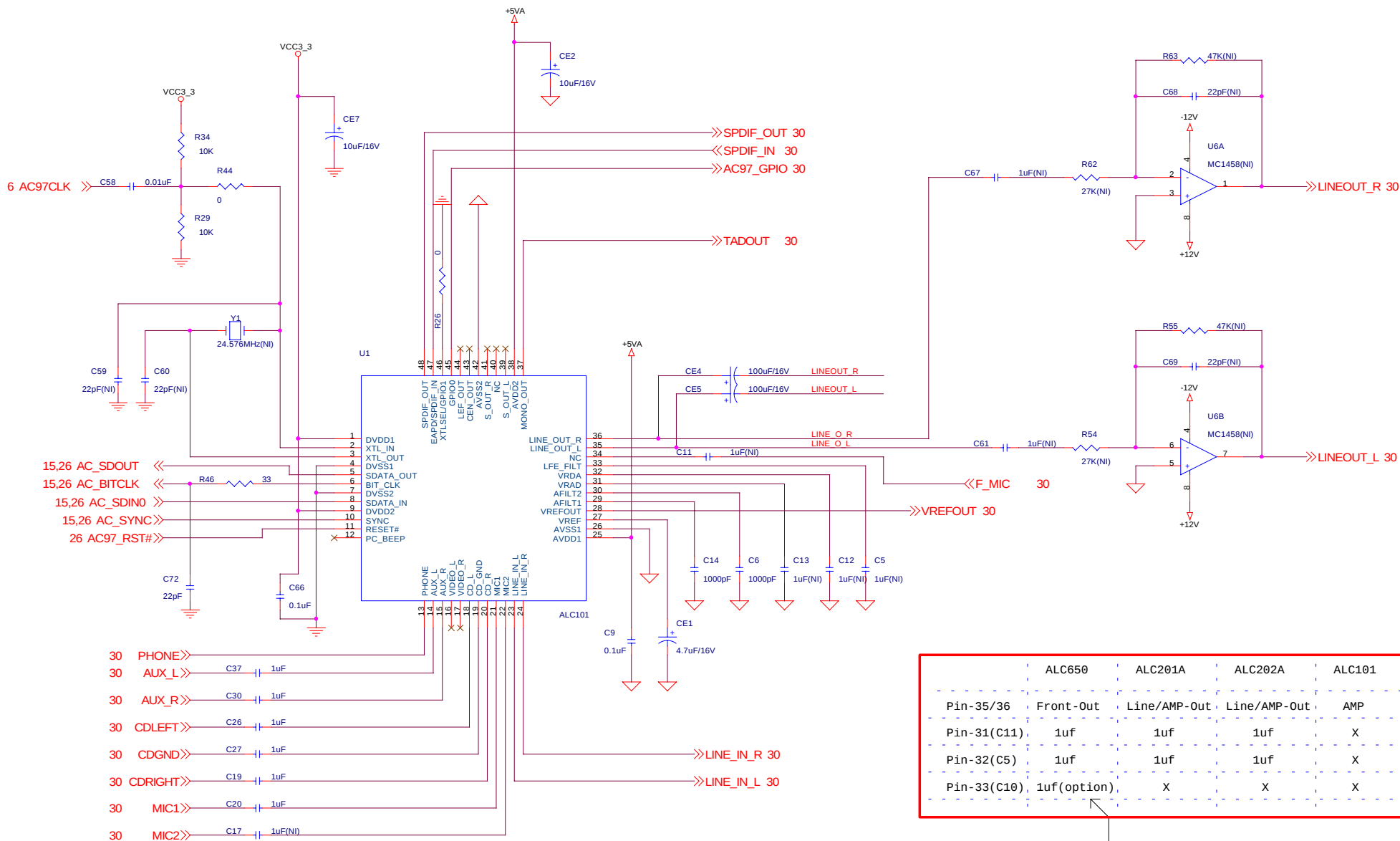


VGA Connectors



5V to 3.3V Translation / Isolation





If H/W LFE effect is not preferred, C12 can be saved.

	ALC650	ALC201A	ALC202A	ALC101
Pin-35/36	Front-Out	Line/AMP-Out	Line/AMP-Out	AMP
Pin-31(C11)	1uf	1uf	1uf	X
Pin-32(C5)	1uf	1uf	1uf	X
Pin-33(C10)	1uf(option)	X	X	X

