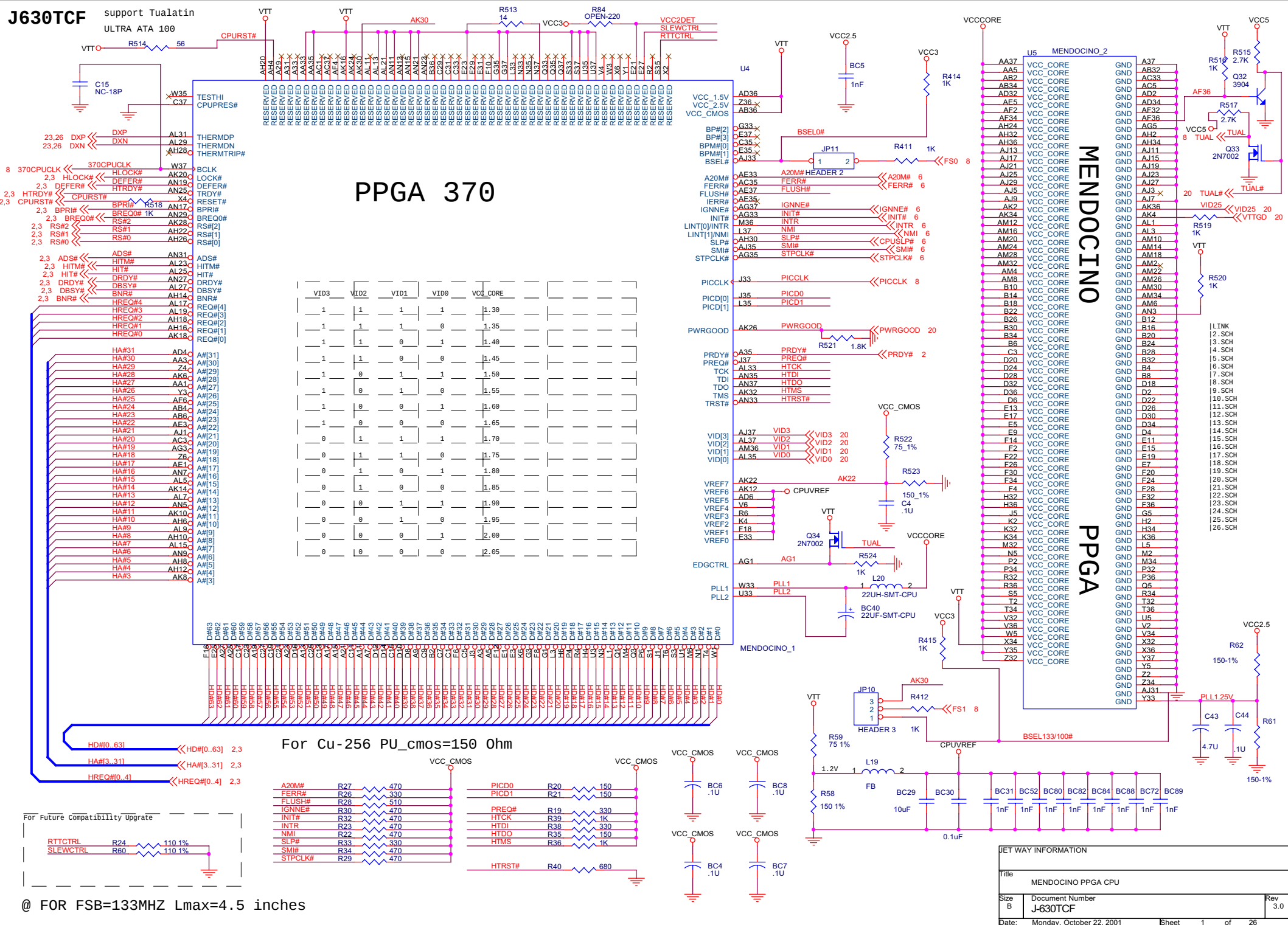
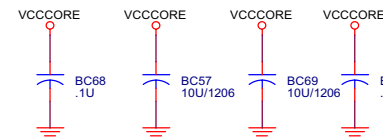
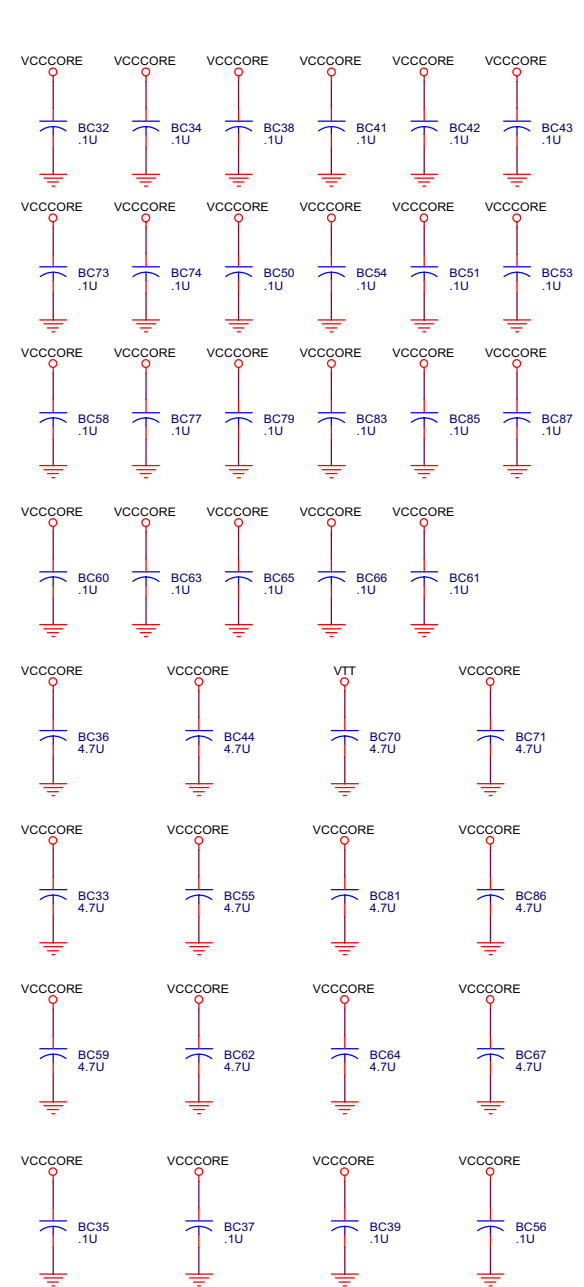
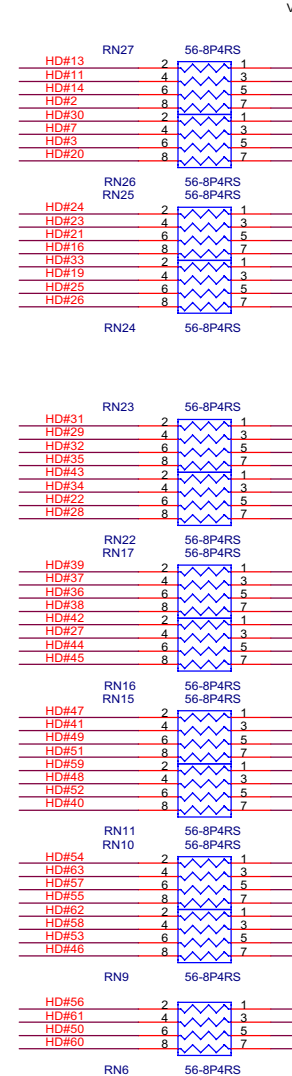


J630TCF

support Tualatin
ULTRA ATA 100

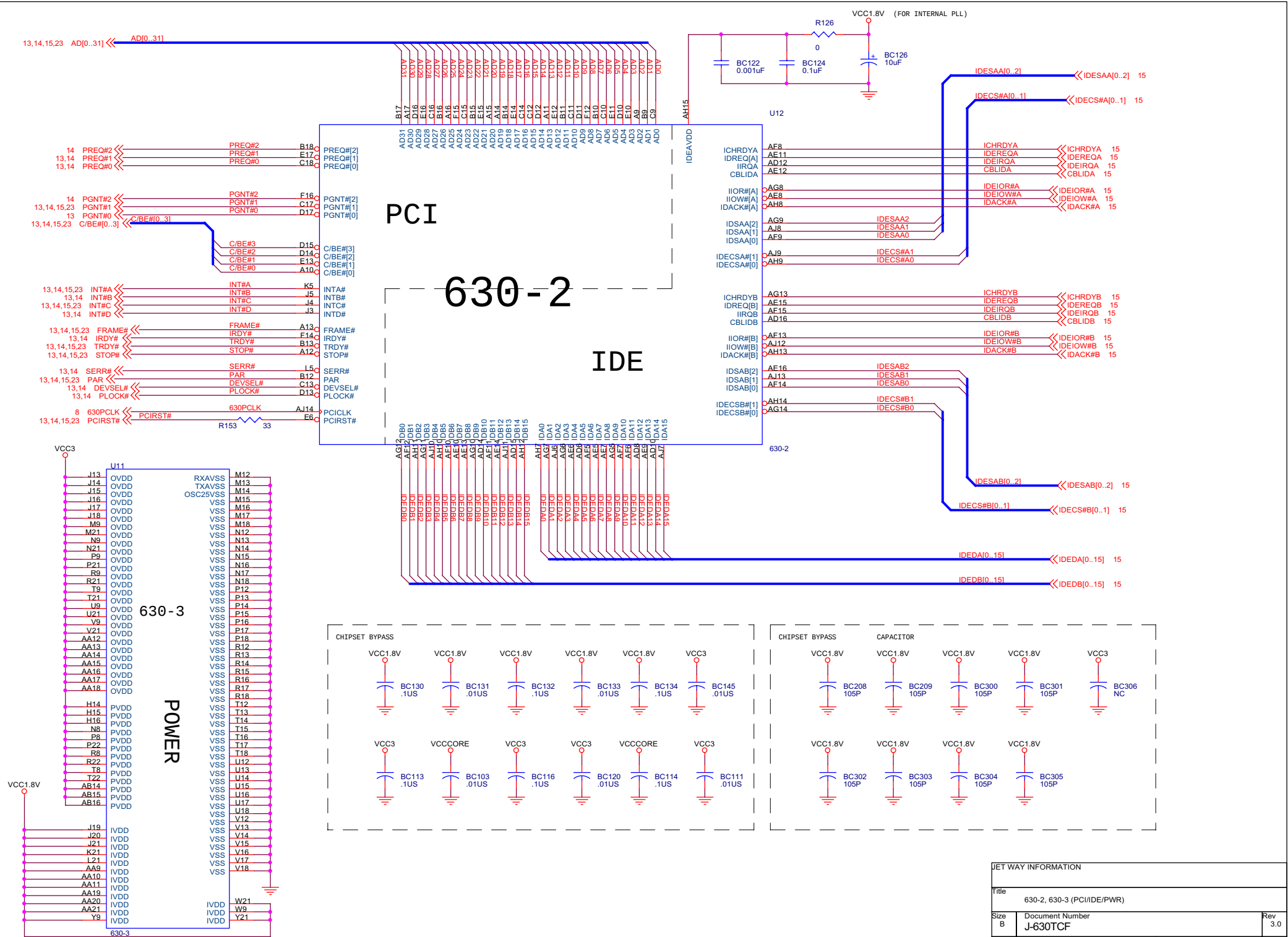


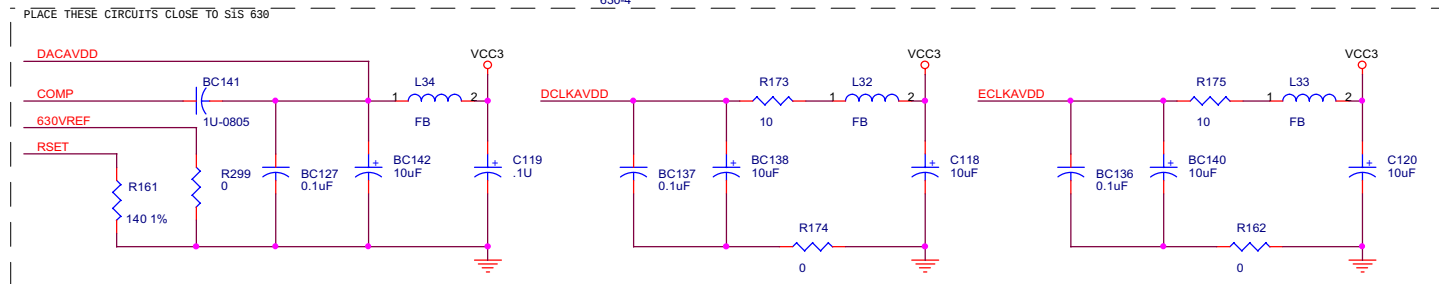
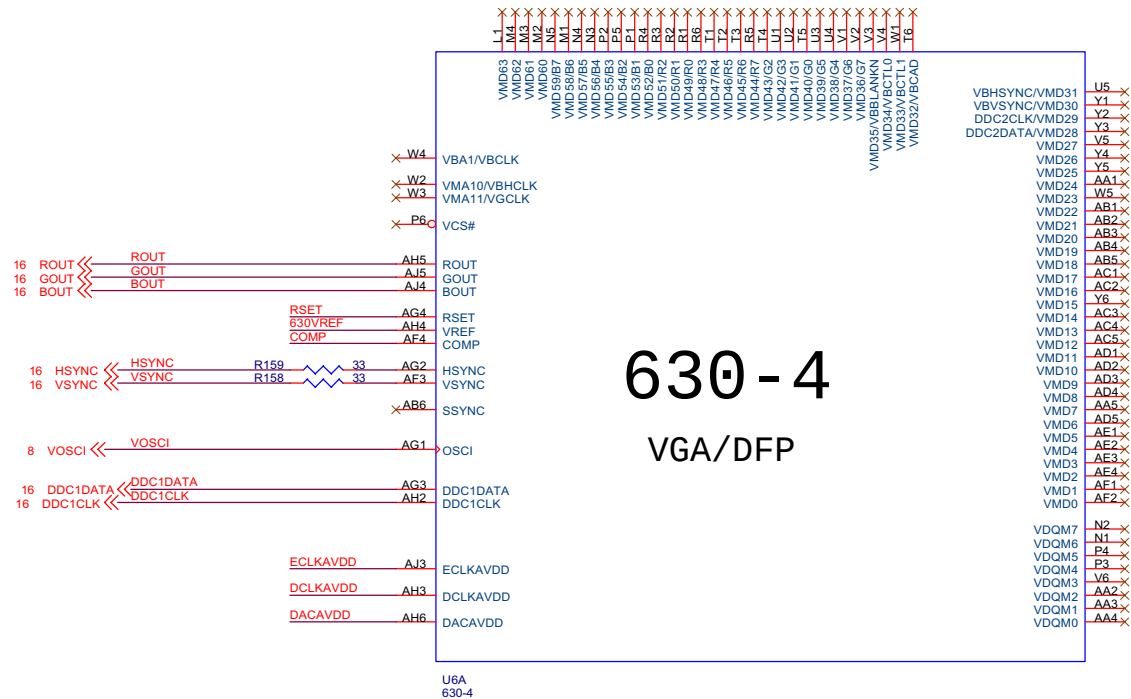
@ FOR FSB=133MHZ Lmax=4.5 inches



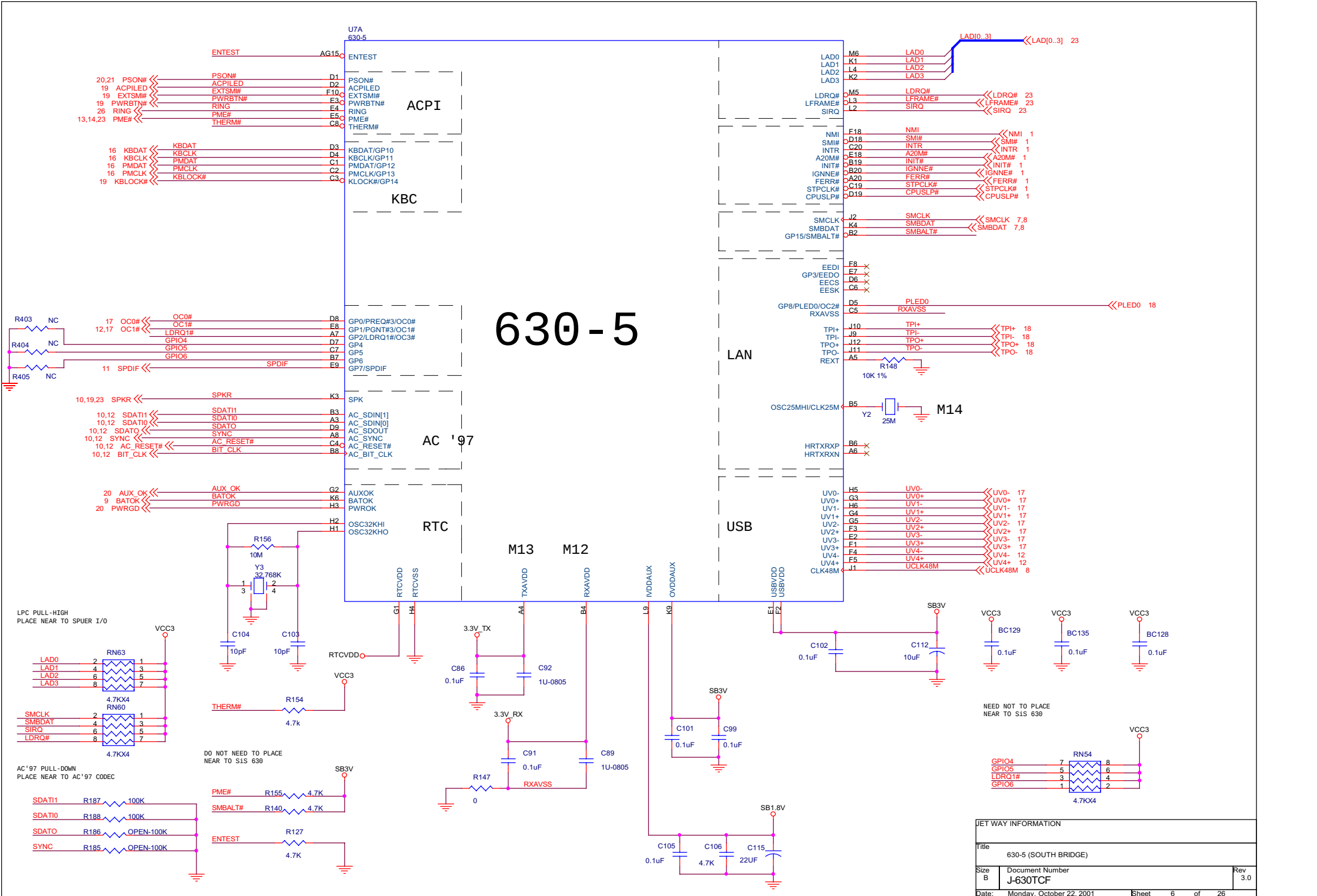
JET WAY INFORMATION			
Title GTL+ TERMINATION RESISTORS			
Size B	Document Number J-630TCF	Rev 3.0	
Date:	Monday, October 22, 2001	Sheet	2 of 26

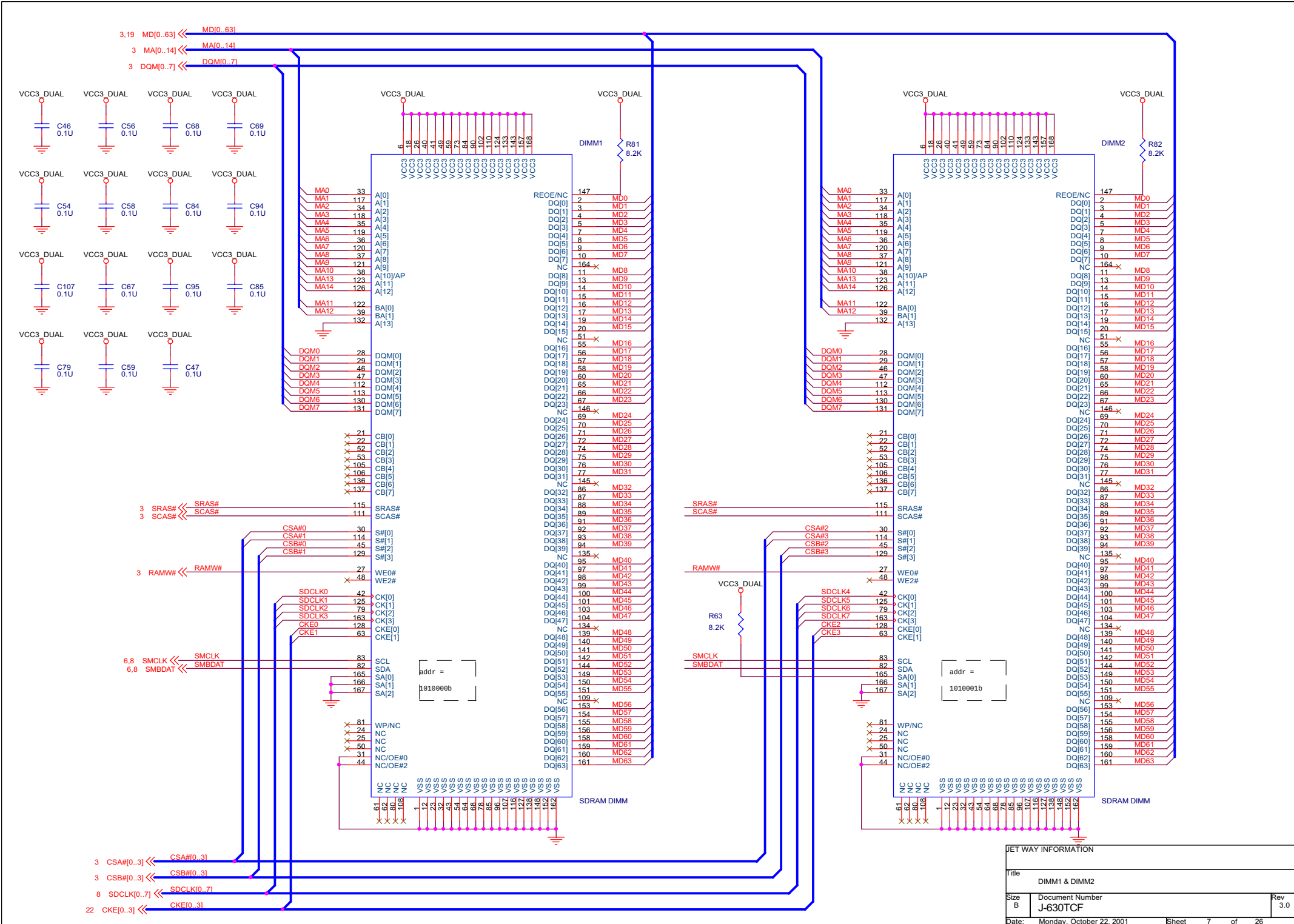
NOTE: GTL+ TERMINATION RESISTORS ONLY FOR MENDOCINO PPGA PROCESSOR.





JET WAY INFORMATION			
Title			
630-4 (VGA/DFP)			
Size	Document Number		Rev
B	J-630TCF		3.0
Date:	Monday, October 22, 2001	Sheet	5 of 26

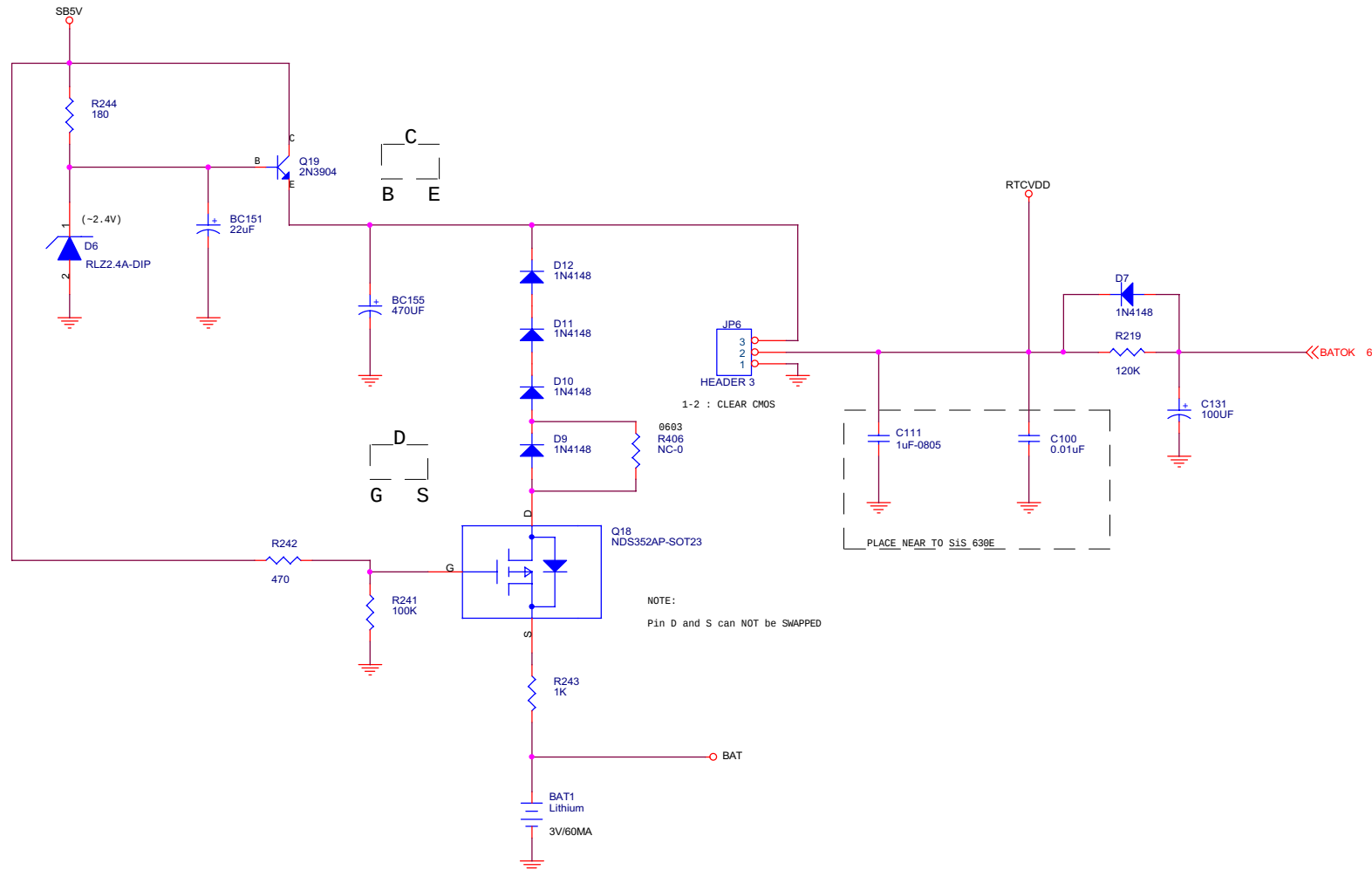




JET WAY INFORMATION

Title			DIMM1 & DIMM2
Size			Document Number
B			J-630TCF
Date:	Monday, October 22, 2001	Sheet	7 of 26
			Rev 3.0

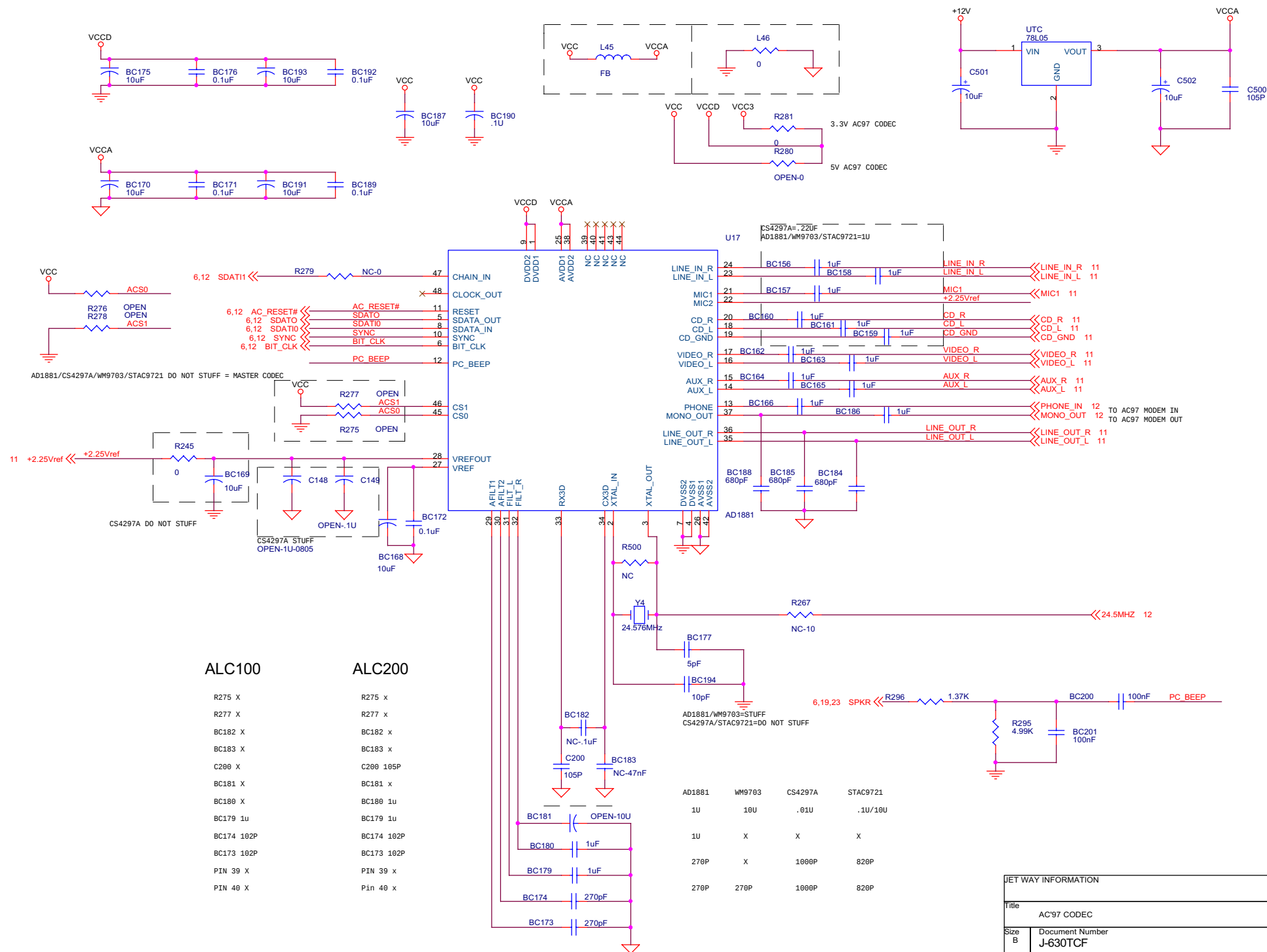
INT. RTC
MUST CLOSE TO CHIP RTCVDD PIN



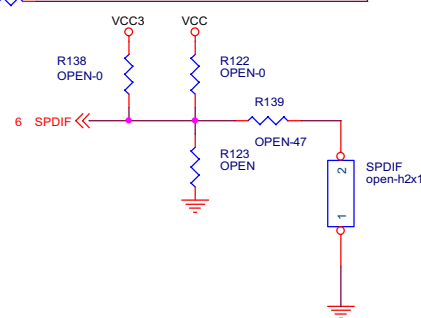
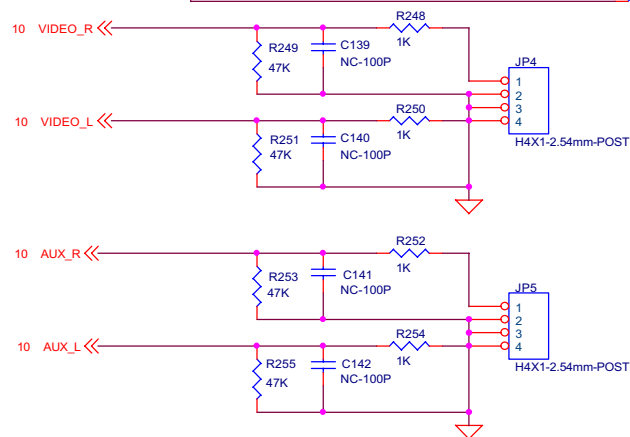
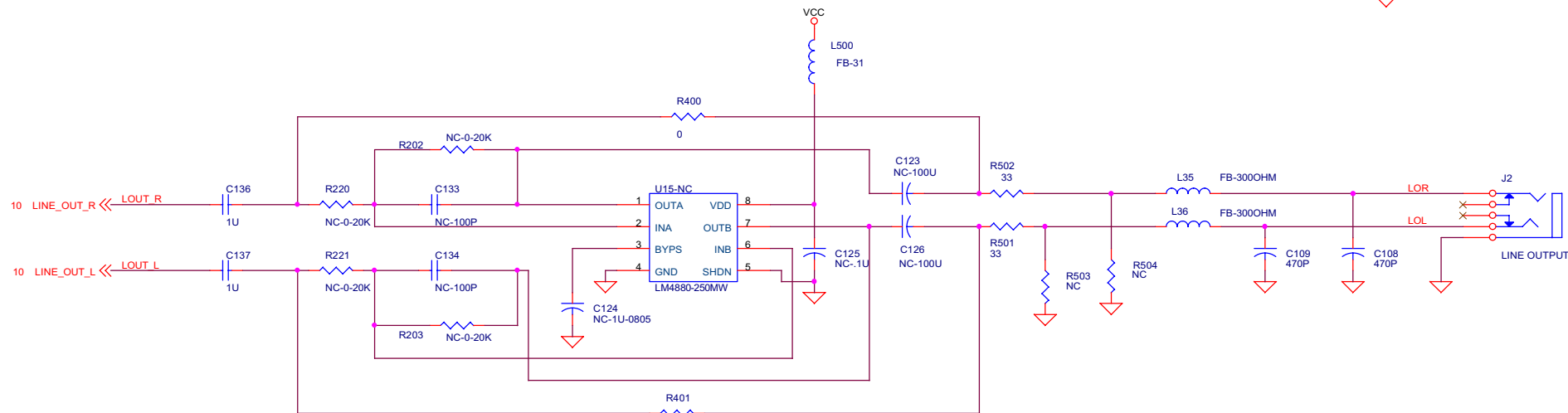
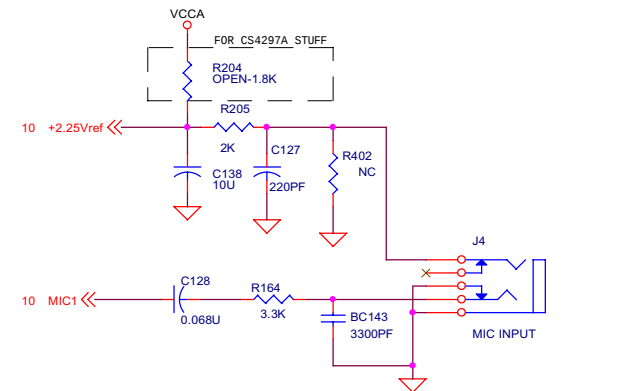
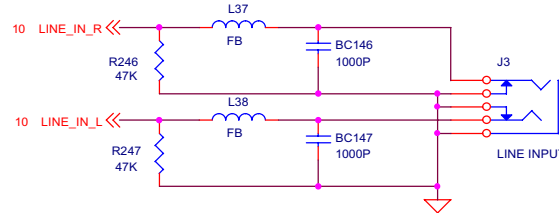
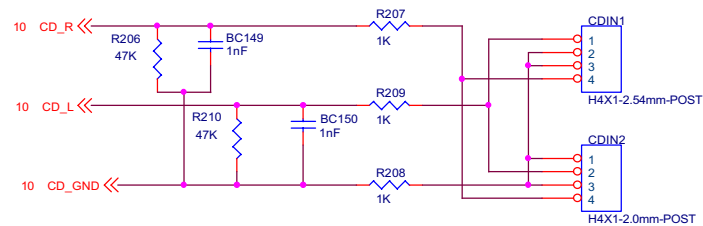
NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION			
Title	RTC		
Size B	Document Number	Rev	
	J-630TCF	3.0	
Date:	Monday, October 22, 2001	Sheet	9 of 26

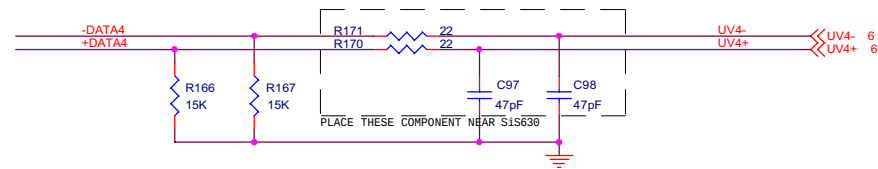
ALC100/ALC200/AD1881/WM9703/CS4297A/STAC9721



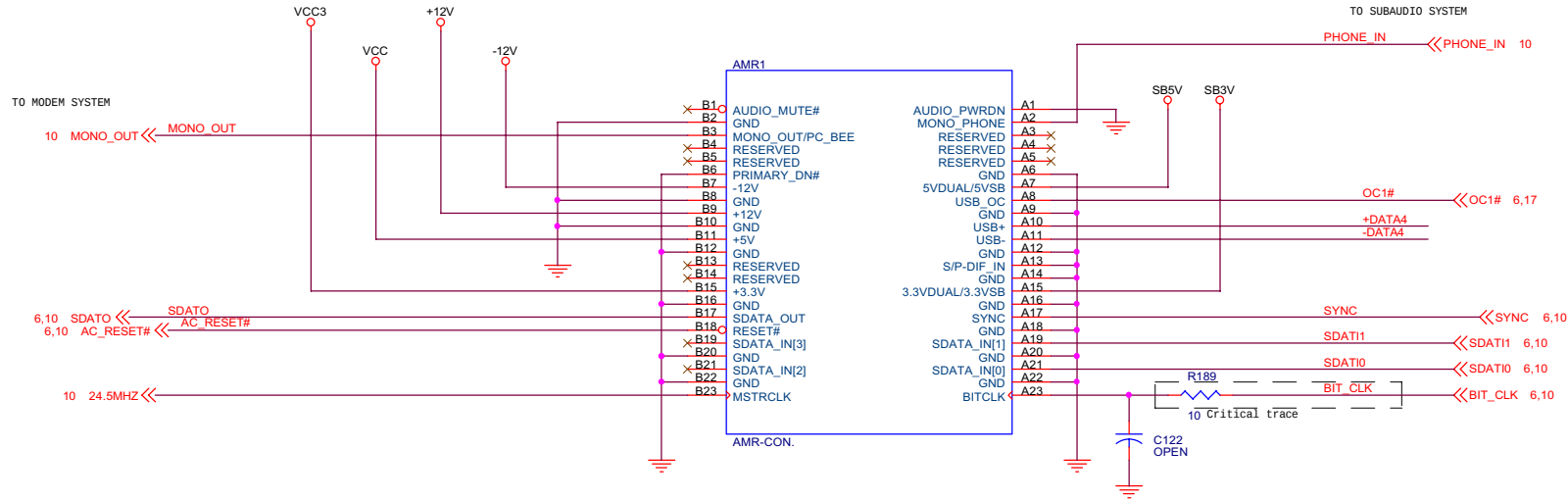
JET WAY INFORMATION			
Title AC'97 CODEC			
Size B	Document Number J-630TCF		Rev 3.0
Date:	Monday, October 22, 2001	Sheet	10 of 26



JET WAY INFORMATION			
Title		AUDIO ANALOGUE IN/OUT	
Size	Document Number	Rev	
B	J-630TCF	3.0	
Date:	Monday, October 22, 2001	Sheet	11 of 26



AMR



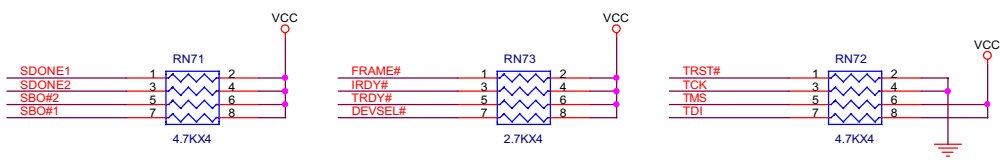
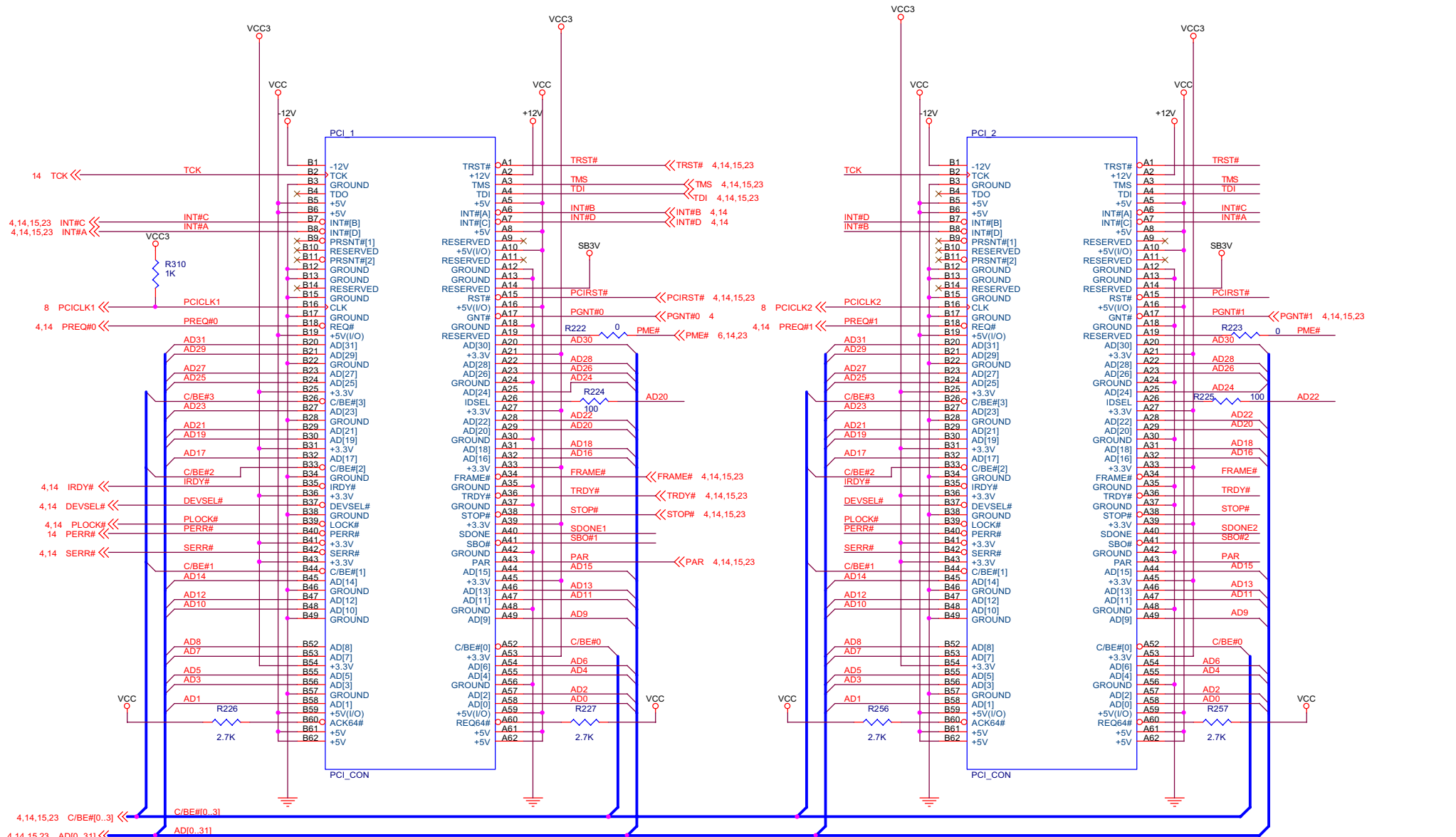
JET WAY INFORMATION

Title
AUDIO/MODEM RISER

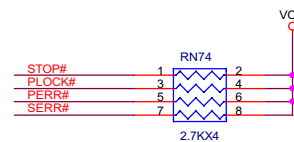
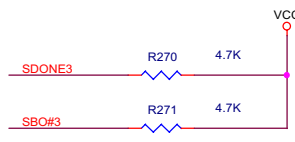
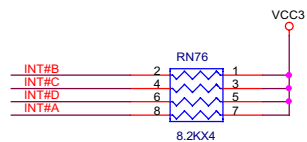
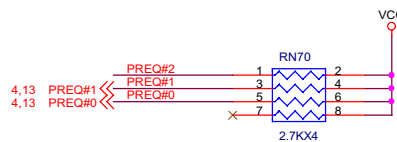
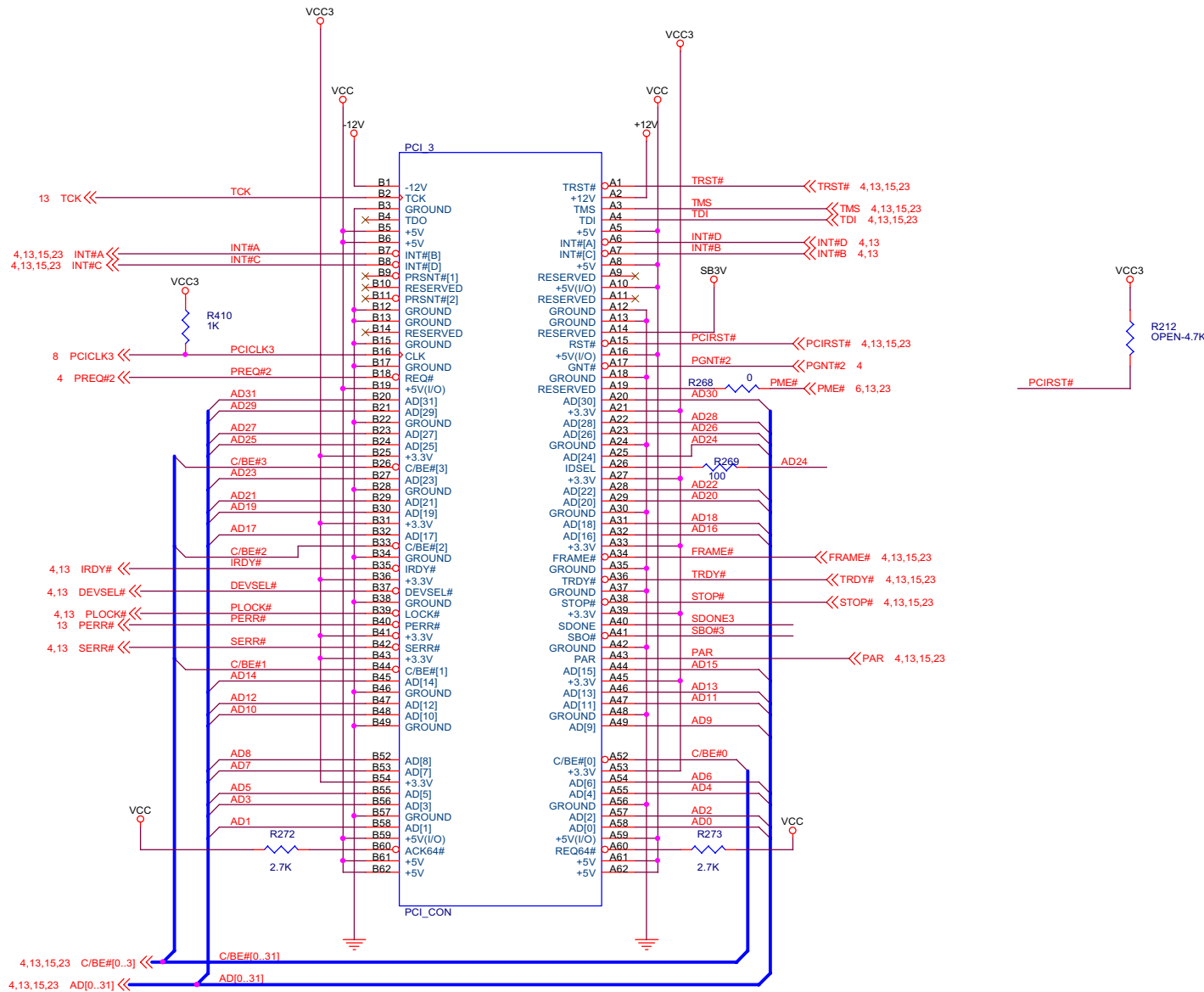
Size
B Document Number
J-630TCF

Rev
3.0

Date: Monday, October 22, 2001 Sheet 12 of 26



JET WAY INFORMATION			
Title			
PCI CONNECTOR 1 & 2			
Size B	Document Number		Rev
	J-630TCF		3.0
Date:	Monday, October 22, 2001	Sheet 13 of 26	



JET WAY INFORMATION

Title PCI CONNECTOR 3&4

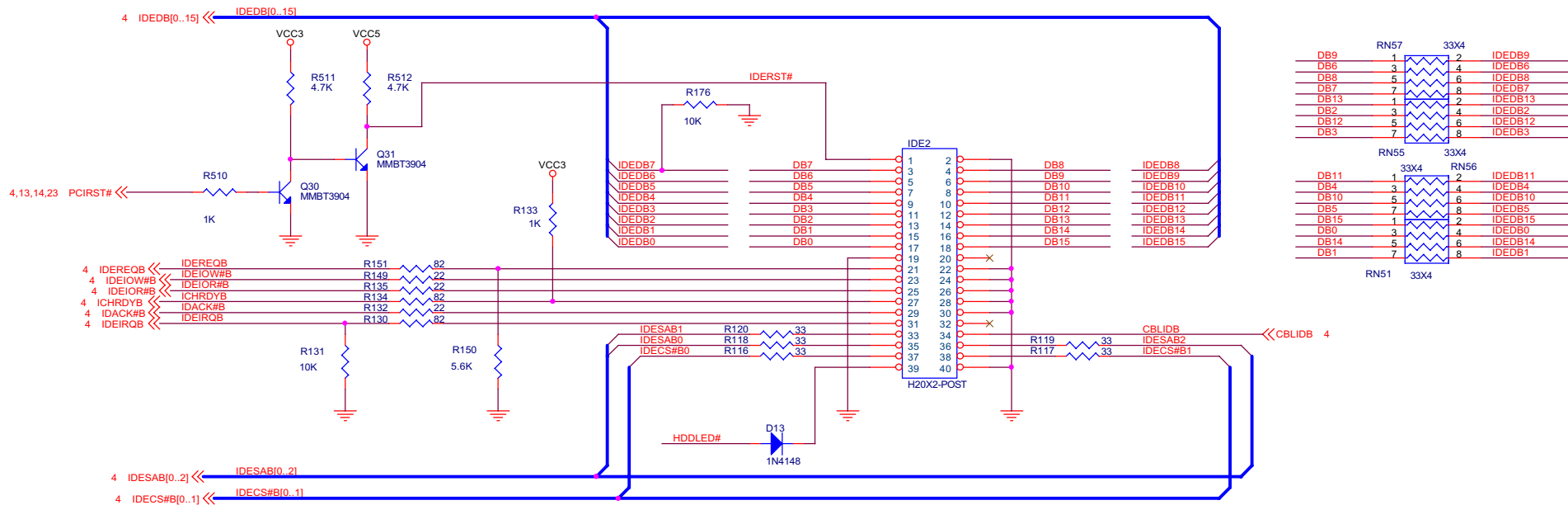
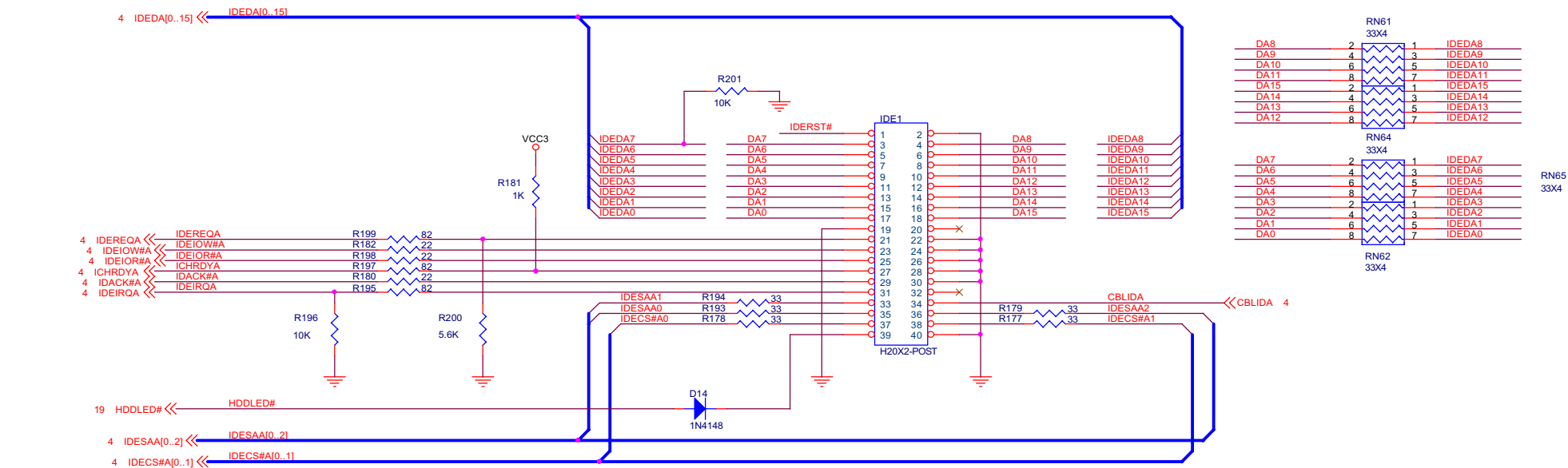
Size B

Document Number J-630TCF

Rev 3.0

Date: Monday, October 22, 2001

Sheet 14 of 26



NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION

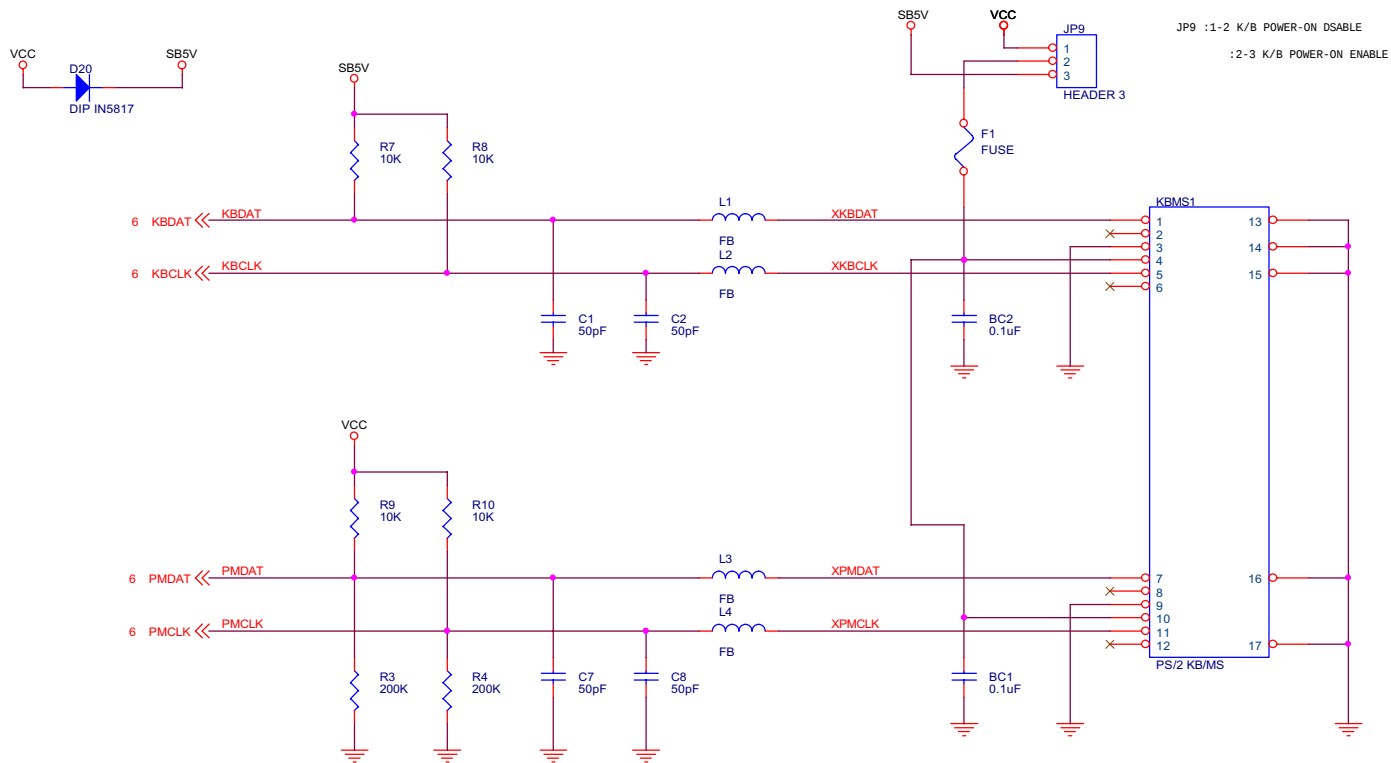
Title IDE CONNECTORS

Size B Document Number J-630TCF

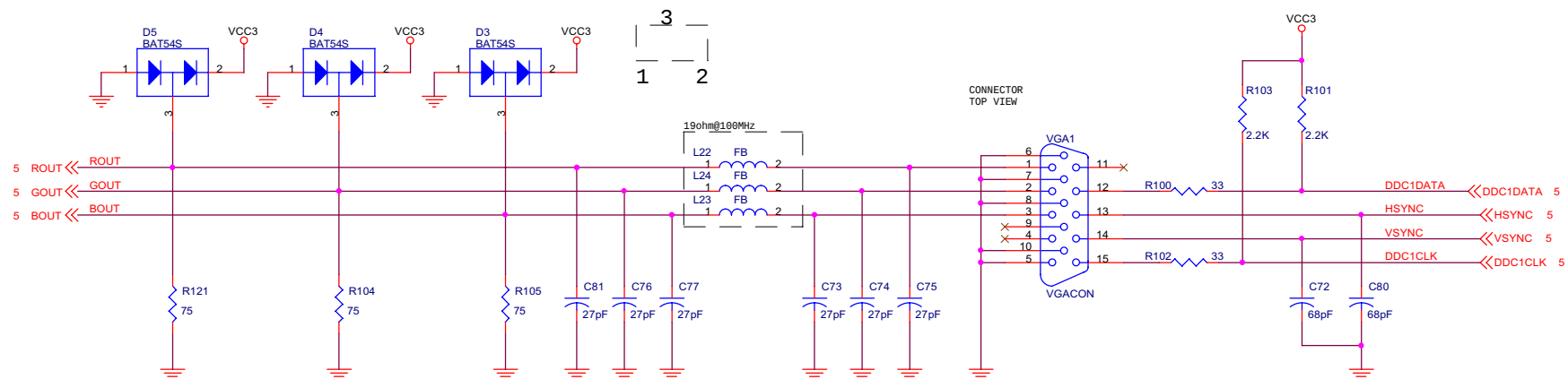
Date: Monday, October 22, 2001

Sheet 15 of 26

Rev 3.0



VGA CONNECTOR 1



NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION

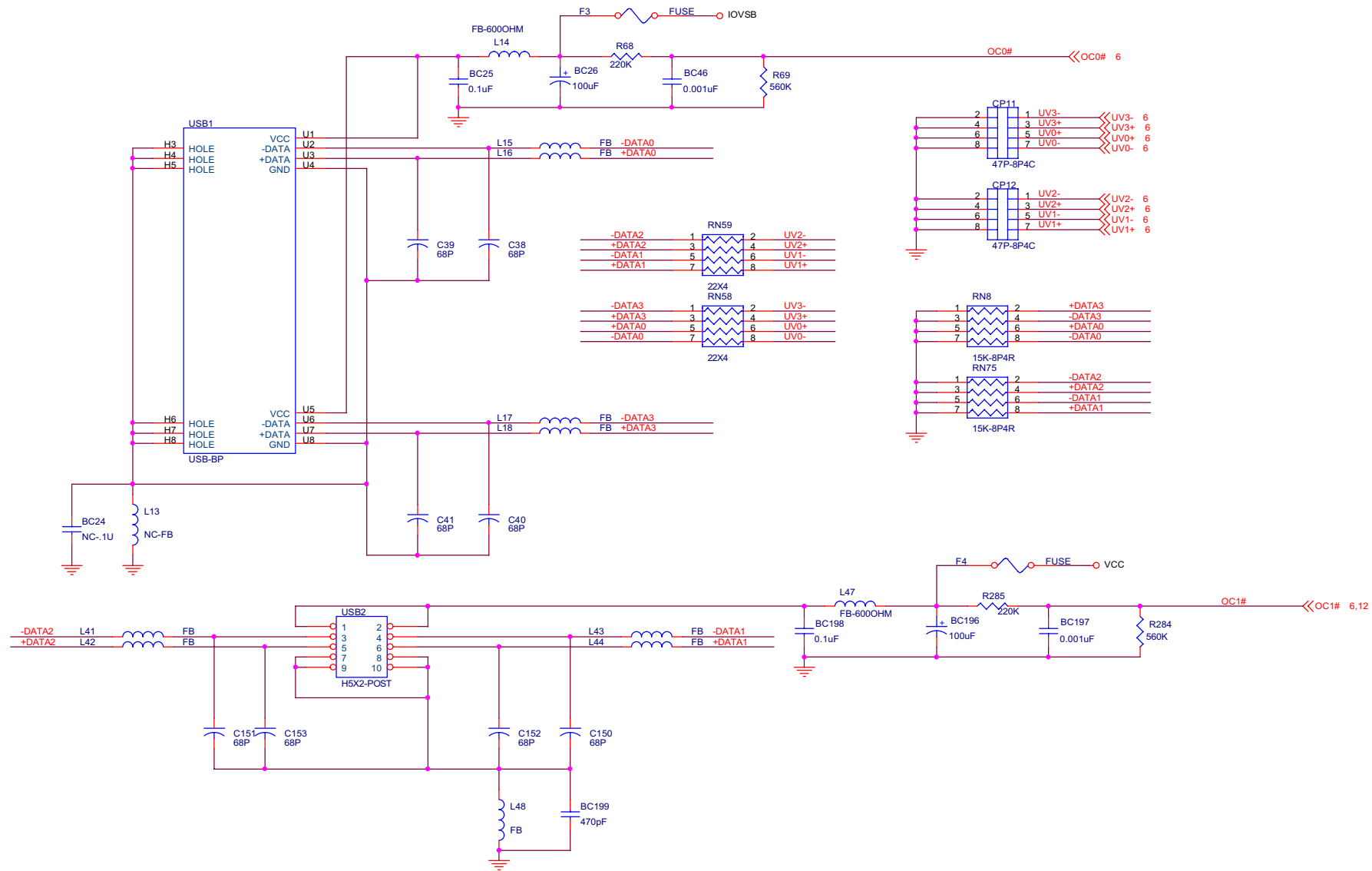
Title
KEYBOARD & MOUSE CONNECTORS

Size
B Document Number
J-630TCF

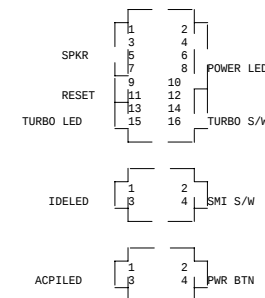
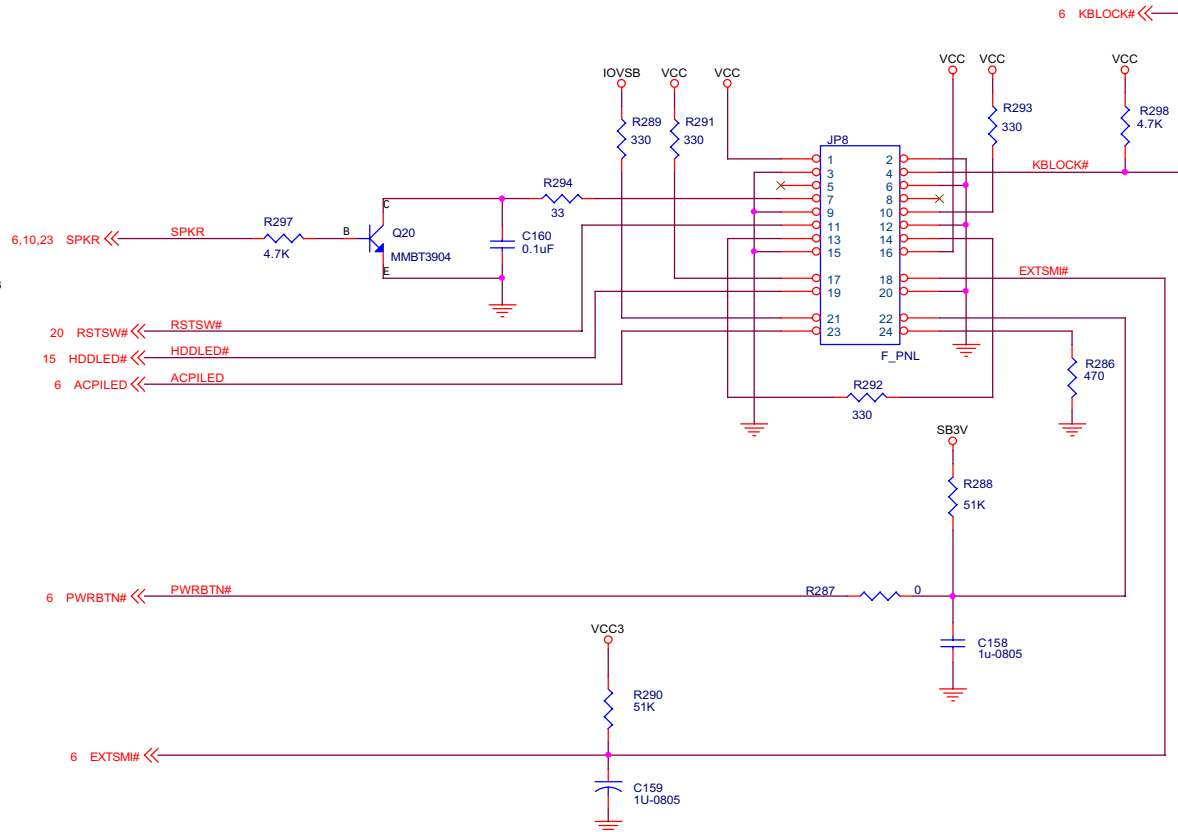
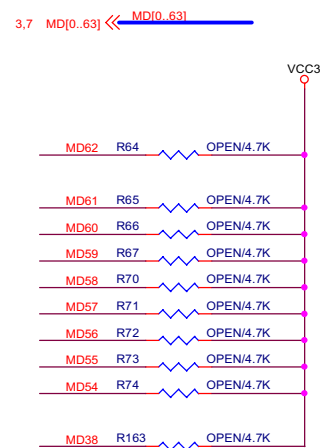
Date: Monday, October 22, 2001

Sheet 16 of 26

Rev
3.0

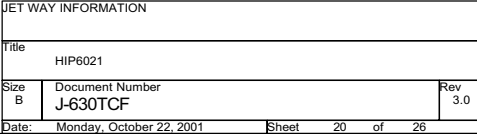


JET WAY INFORMATION			
Title		USB CONNECTORS	
Size	Document Number	Rev	
B	J-630TCF	3.0	
Date:	Monday, October 22, 2001	Sheet	17 of 26

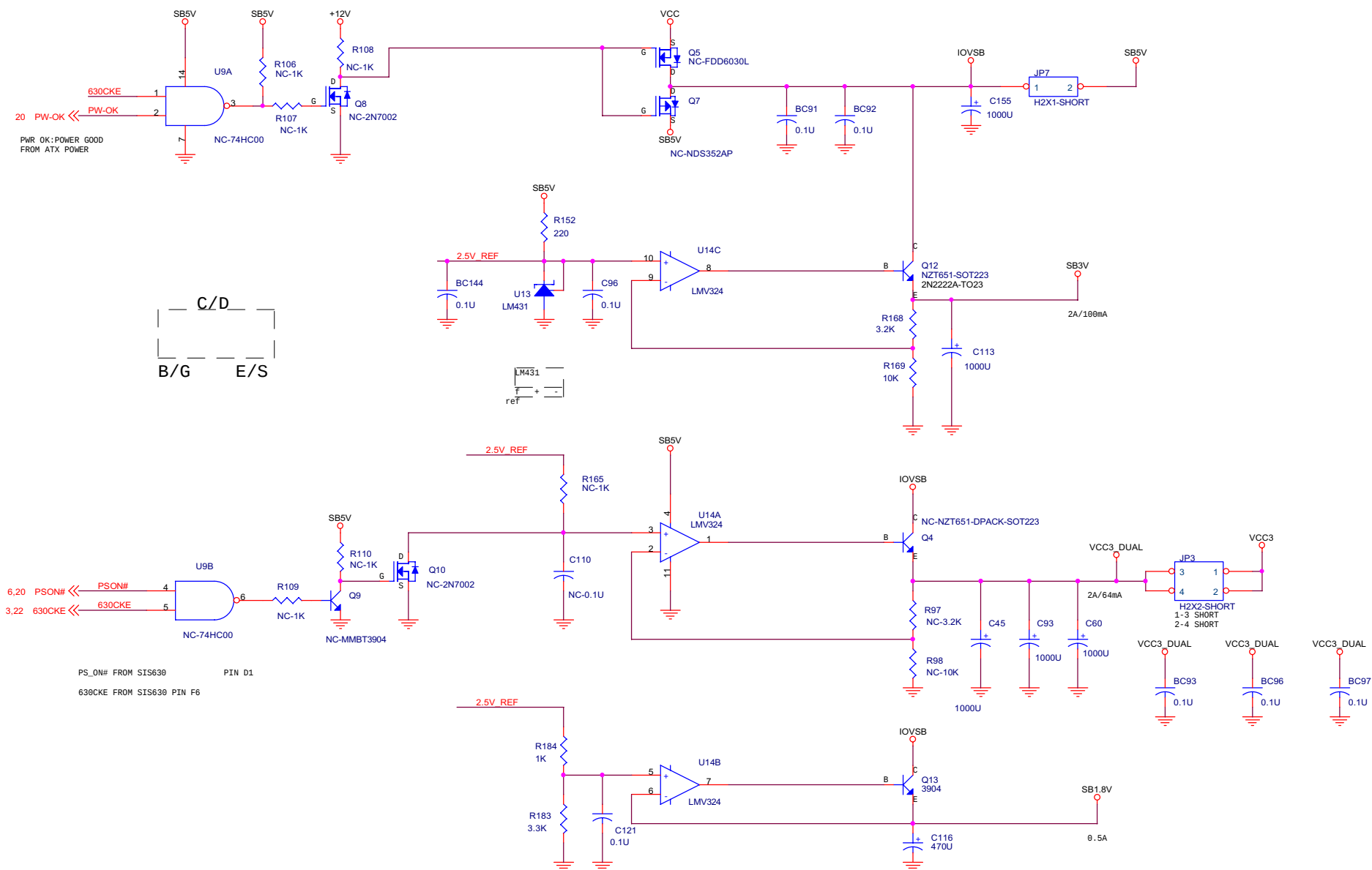


NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION			
Title		DATA ACQUISITION & JUMPERS	
Size B	Document Number	J-630TCF	
Date:	Monday, October 22, 2001	Sheet	19 of 26
			Rev 3.0



RC5058+Discrete ACPI FOR SIS630 - PAGE 2

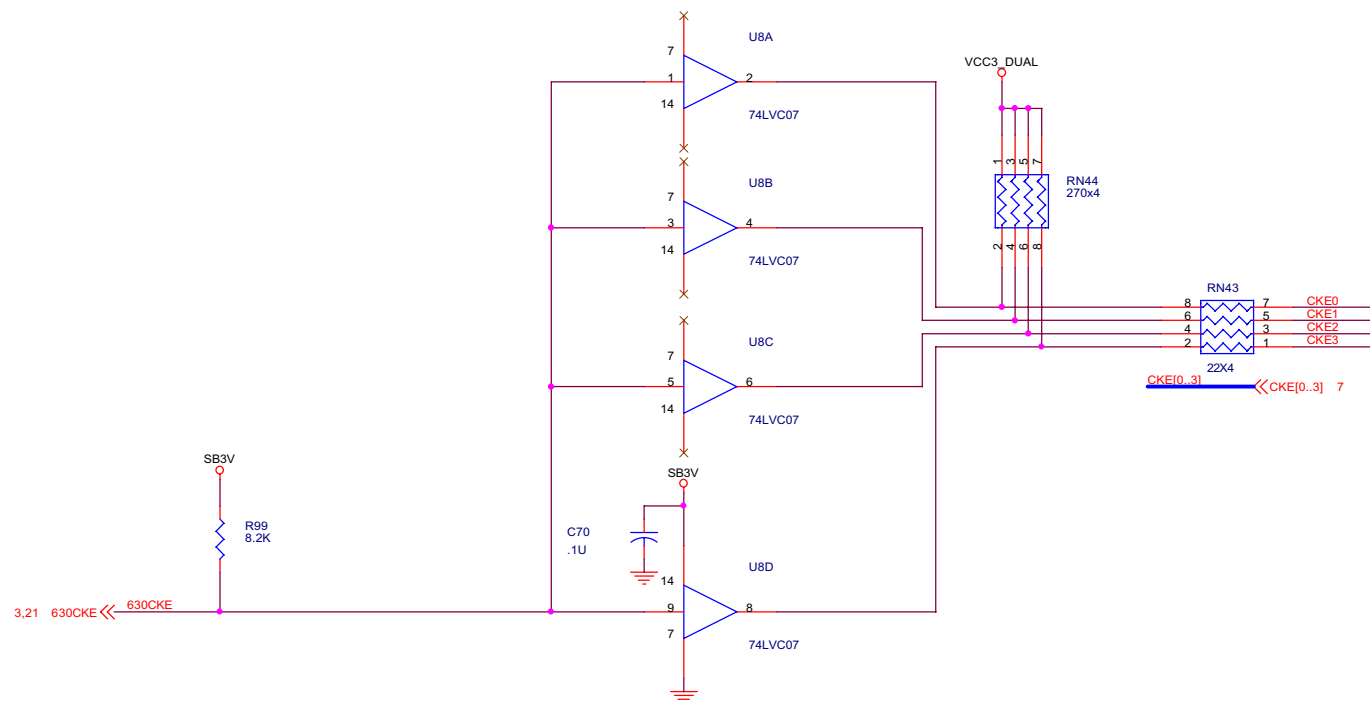


JET WAY INFORMATION			
Title		DISCRETE ACPI FOR SIS630 - 2	
Size	Document Number	Rev	
B	J-630TCF	3.0	
Date:	Monday, October 22, 2001	Sheet	21 of 26

VCC and VCC3 Standby VOLTAGE SWITCH

WHEN IN S0,S1
THIS CIRCUIT PASSES THE NORMAL POWER

WHEN IN S3,S4,S5
THIS CIRCUIT PASSES THE STANDBY POWER



JET WAY INFORMATION			
Title			
VOLTAGE SWITCH FOR SUSPEND TO DRAM			
Size B	Document Number J-630TCF		Rev 3.0
Date:	Monday, October 22, 2001	Sheet	22 of 26

W83697HF

(To monitor battery voltage, this input should be connected directly to battery)

Temperature Sensing

Voltage SENSING

Fan Speed Seneing & Speed Control

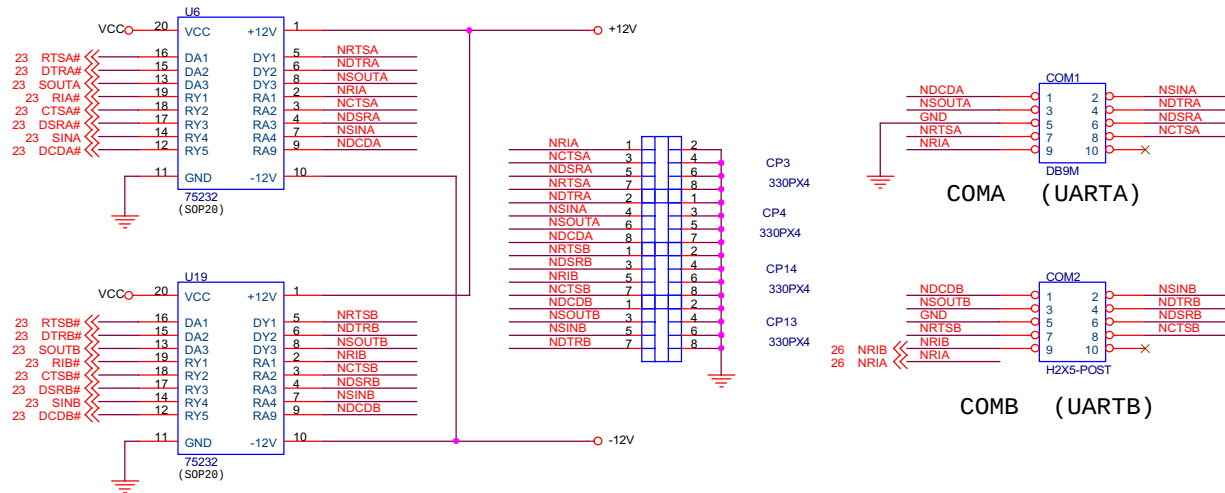
MIDI PORT

GAME PORT

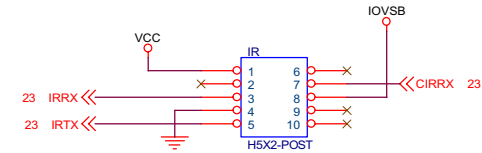
FDC

JET WAY INFORMATION		
Title		
W83697HF (LPC I/O + H/W + FLASH ROM I/N)		
Size	Document Number	Rev
B	J-630TCF	3.0
Date:	Monday, October 22, 2001	Sheet 23 of 26

COM PORT

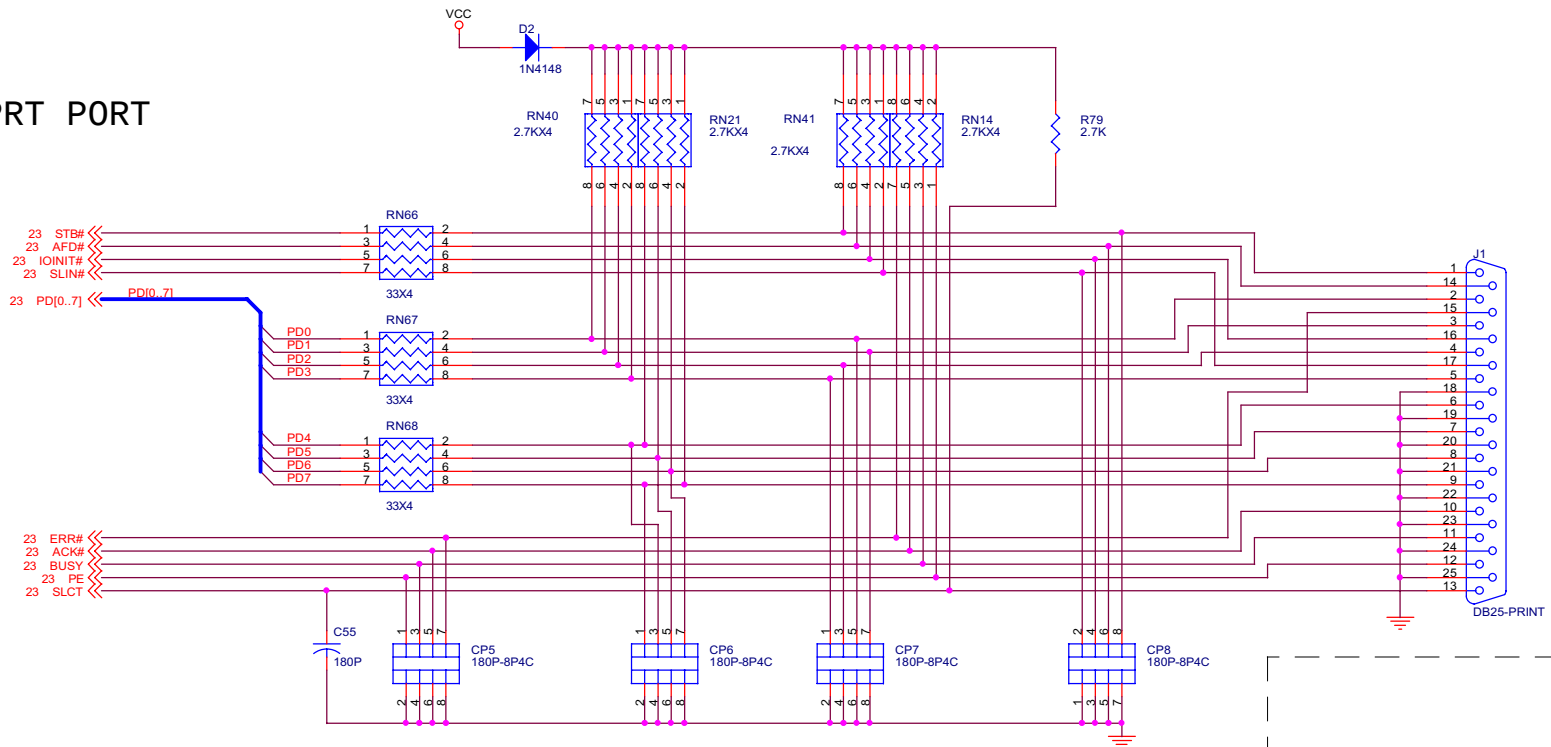


IR/CIR CONNECTOR



THE IOVSB OF PIN 8 IS FOR CIR WAKE-UP FUNCTION.

PRT PORT



JET WAY INFORMATION		
Title		
W83697HF (LPC I/O + H/W + FLASH ROM I/F)		
Size		
Document Number		
J-630TCF		
Date:		
Monday, October 22, 2001		
Sheet		
24 of 26		
Rev		
3.0		

FLASH ROM

SELECT 2M-FLASH ROM, UNINSTALL R258
SELECT 4M-FLASH ROM, INSTALL R258

W29C020/40

IOVSB CIRCUIT

IOVSB CIRCUIT

OnNow or Wake_up function power

RX1 OFF, D1,D2 ON: Wake_up fuction

RX1 ON, D1,D2 OFF:NO Wake_up fuction

IOVSB

VCC

OPEN-5817

D15

OPEN-0

R266

C156 NC-10U

SB5V

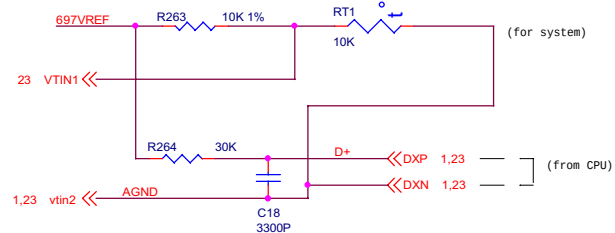
D16

OPEN-5817

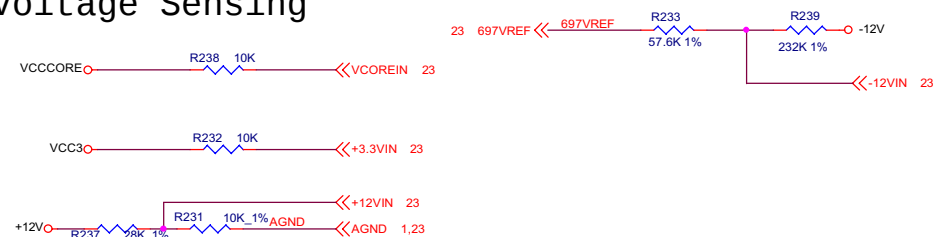
REFERENCE TO PAGE 21

Hardware Monitor circuits

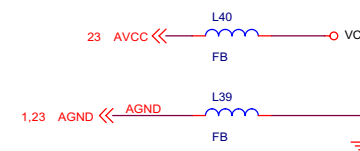
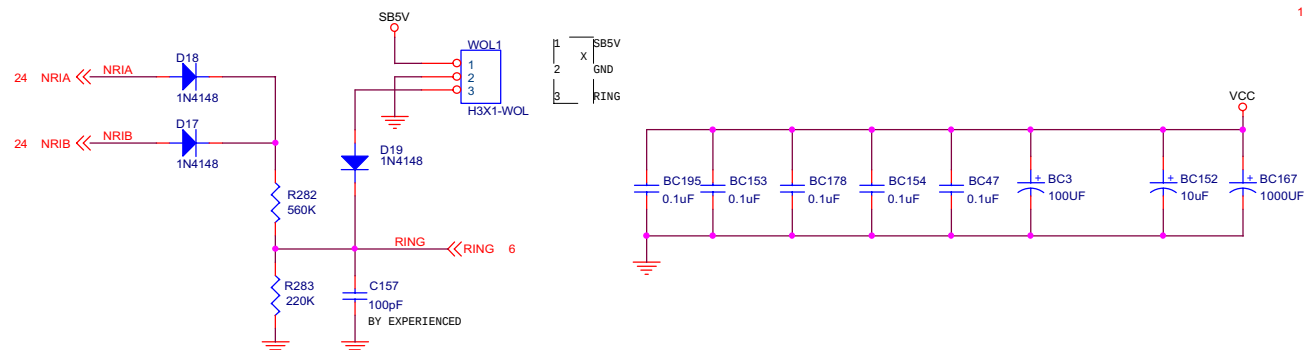
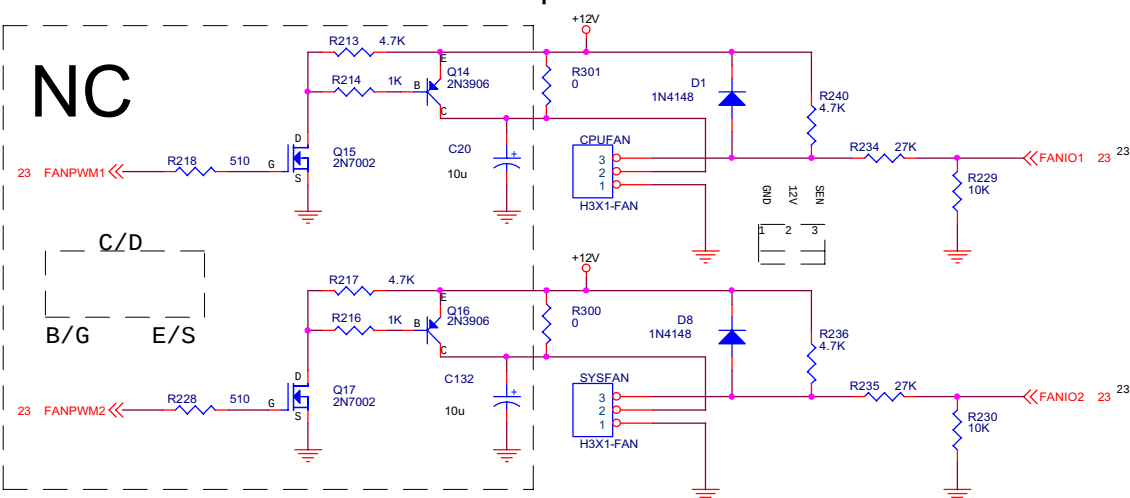
Temperature Sensing



Voltage Sensing



PWM Circuit for FAN speed control



JET WAY INFORMATION				
Title W83697HF (LPC I/O+ H/W + FLASH ROM I/F)				
Size B	Document Number J-630TCF			Rev 3
Date:	Monday, October 22, 2001	Sheet	26 of 26	