

VIA Apollo ProMedia Board Schematics

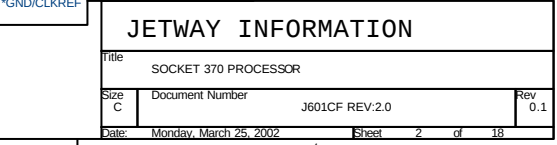
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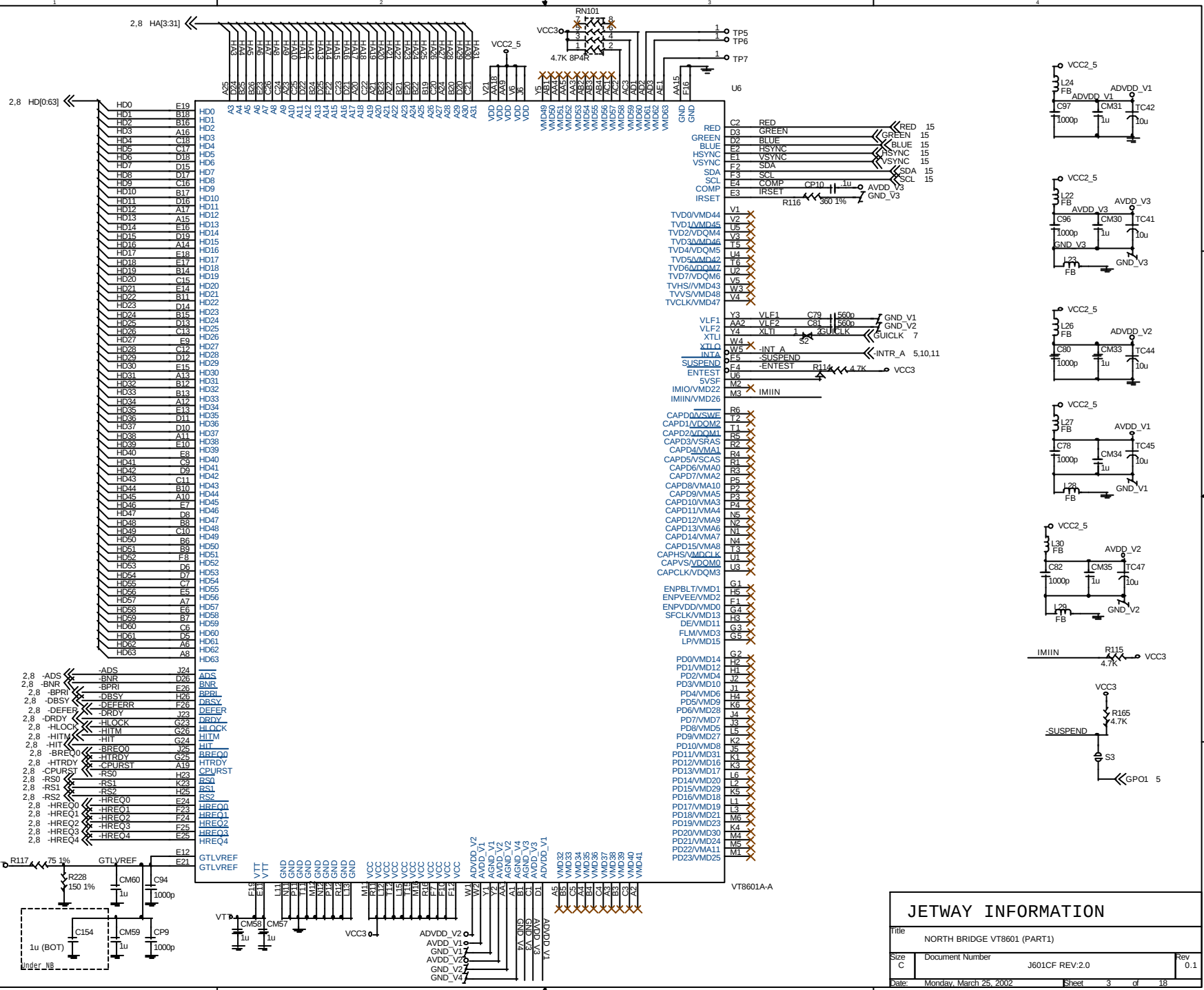
REV:2.0

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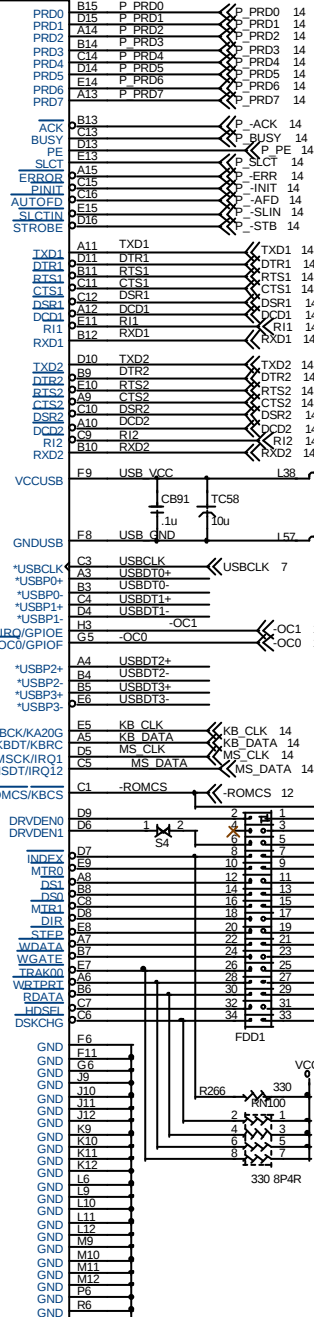
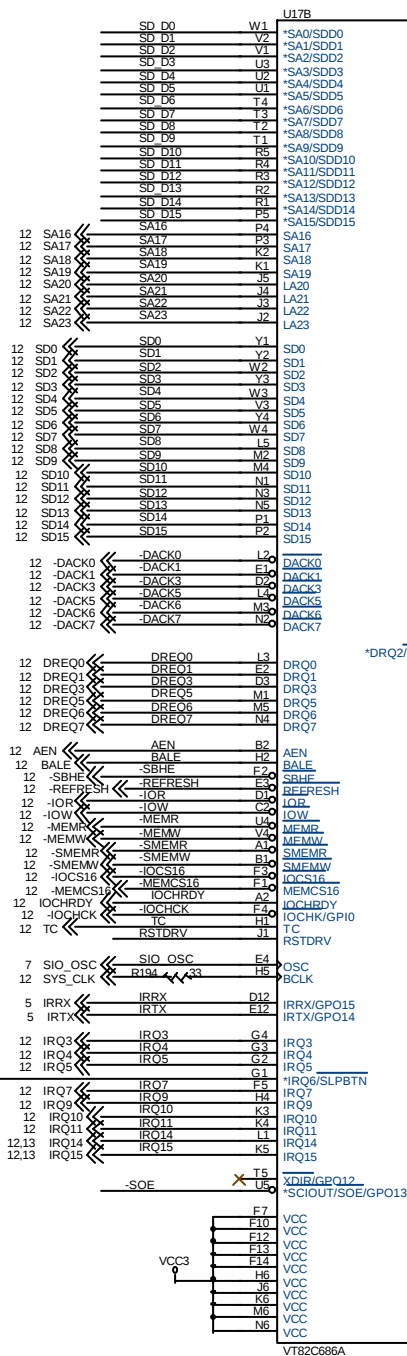
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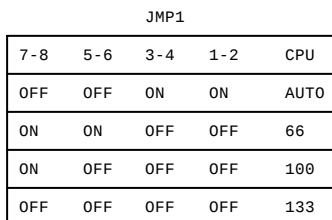
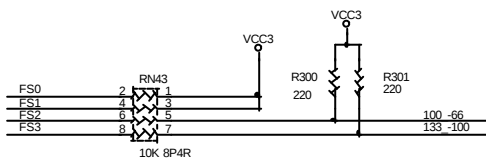
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Title COVER SHEET				
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Title					NORTH BRIDGE VT8601 (PART1)					
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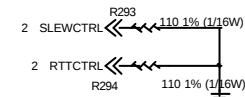
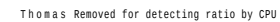
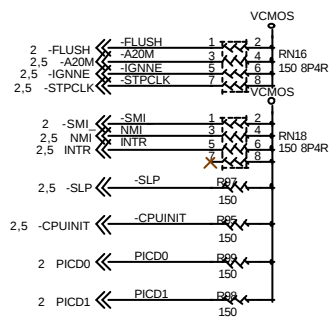
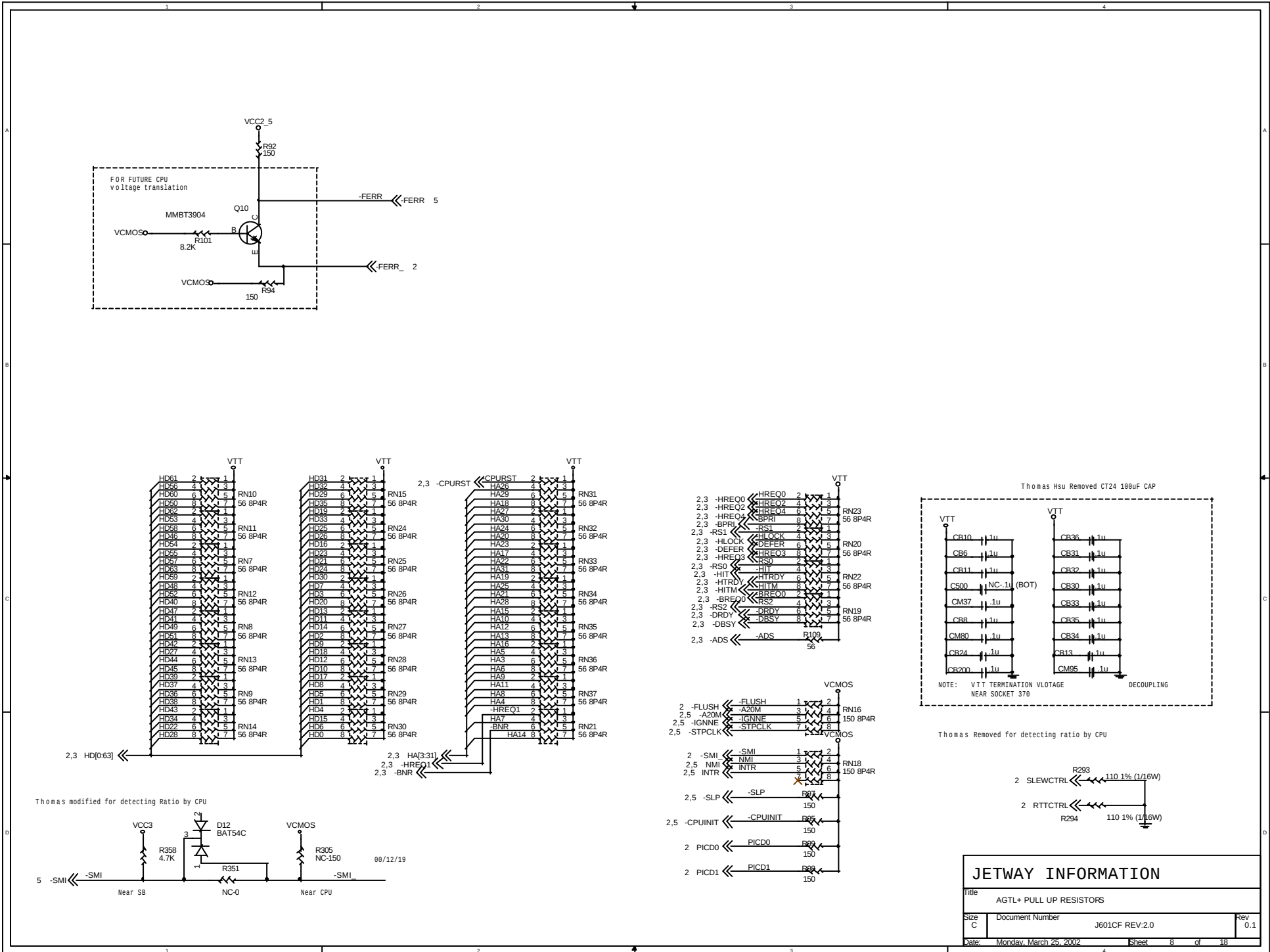




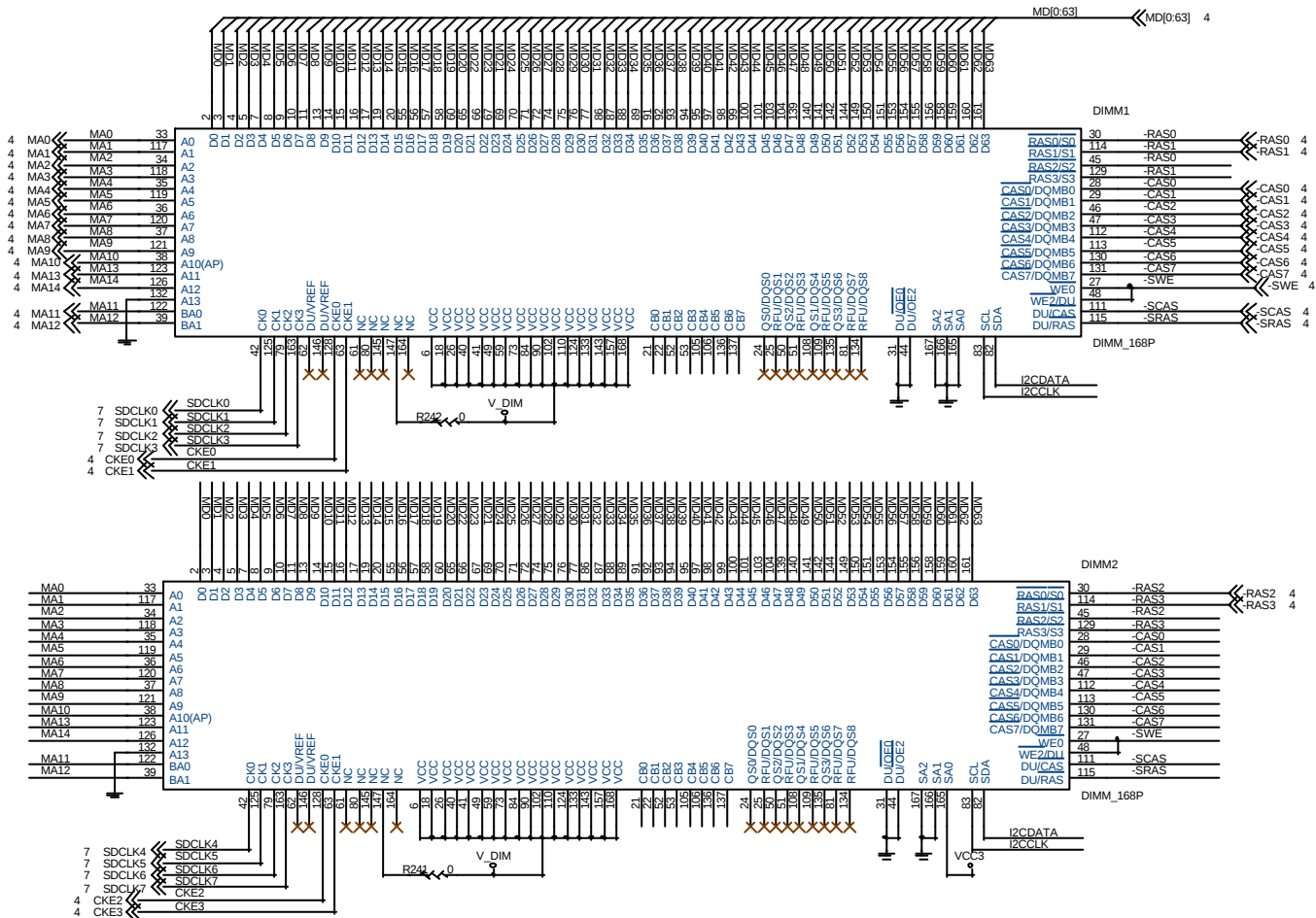
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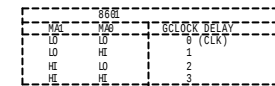
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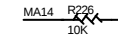
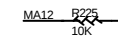
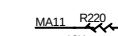
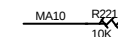
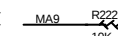
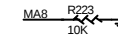
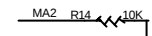
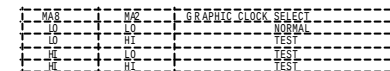
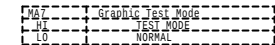
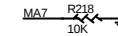
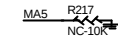
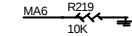
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AGTL+ PULL UP RESISTORS			
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MA POWER-UP STRAPPING OPTIONS



MA6:L0 DISABLE LCD FUNCTION

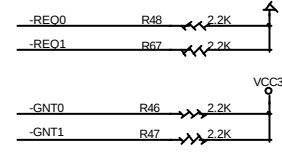
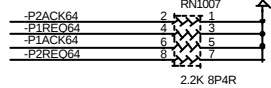
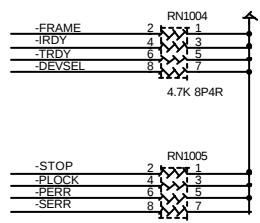
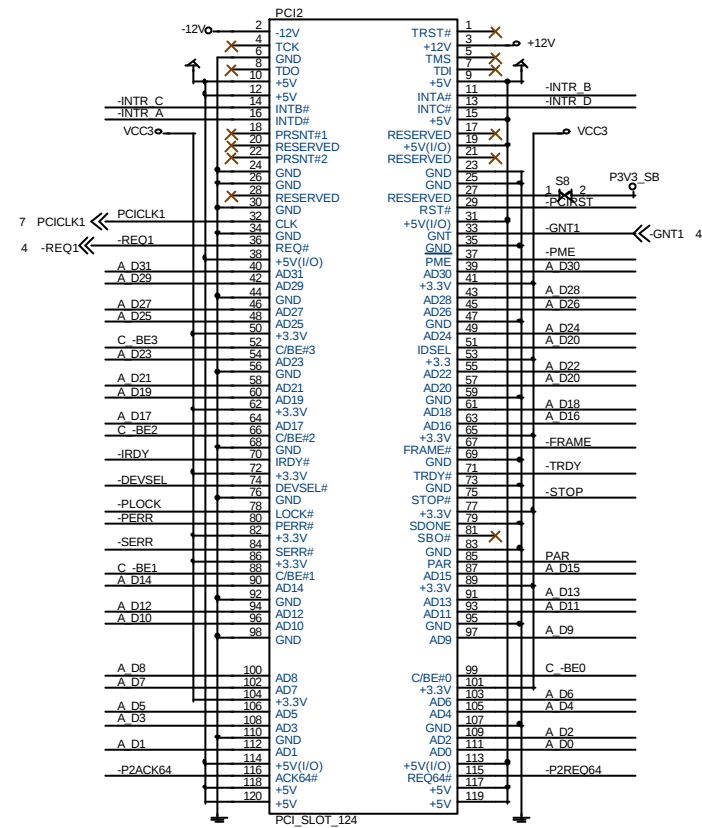
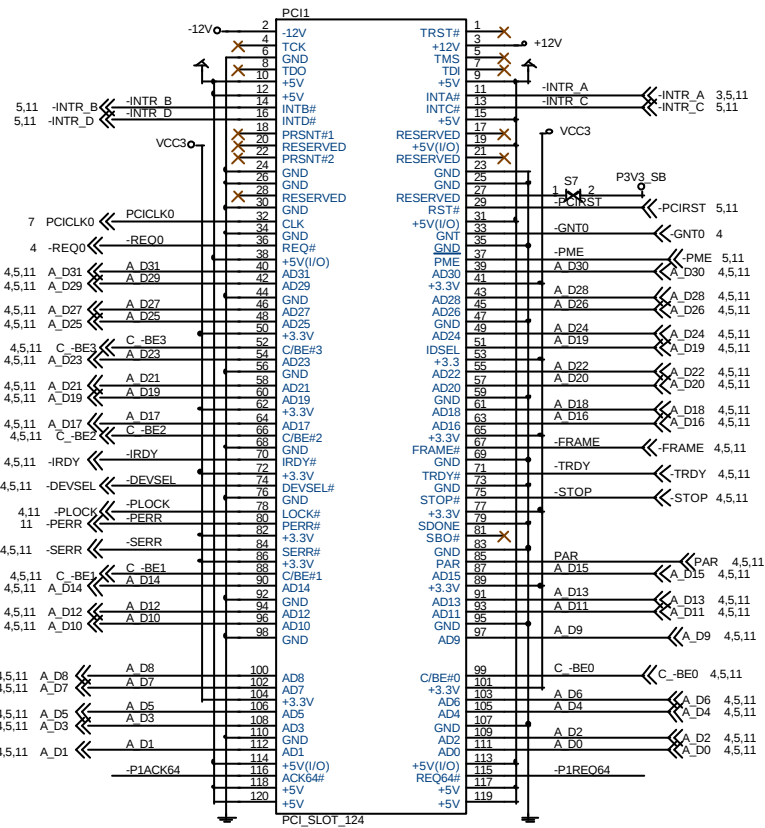


CPU FREQ FIX AUTO MODE

Thomas

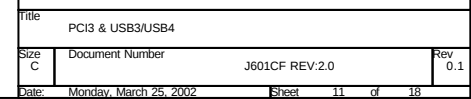
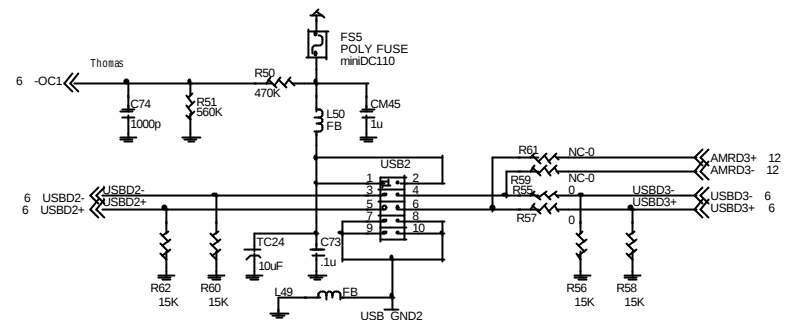
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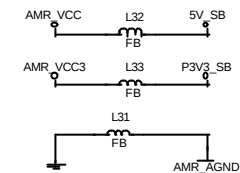
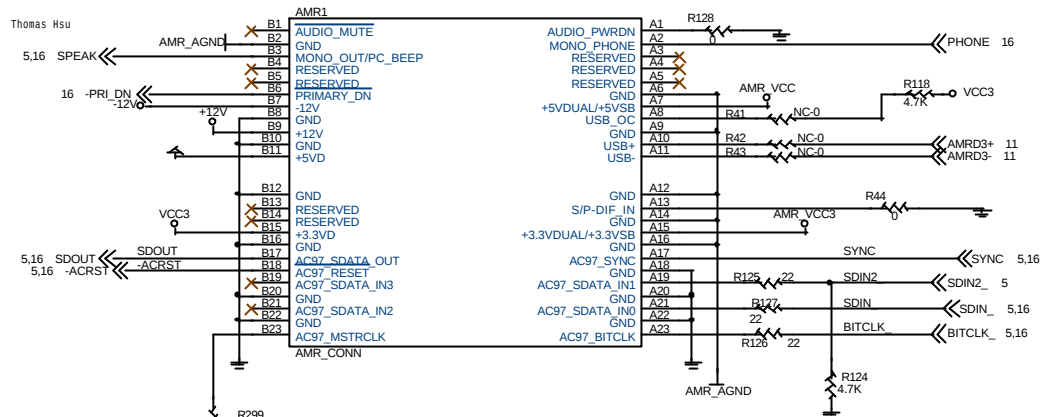
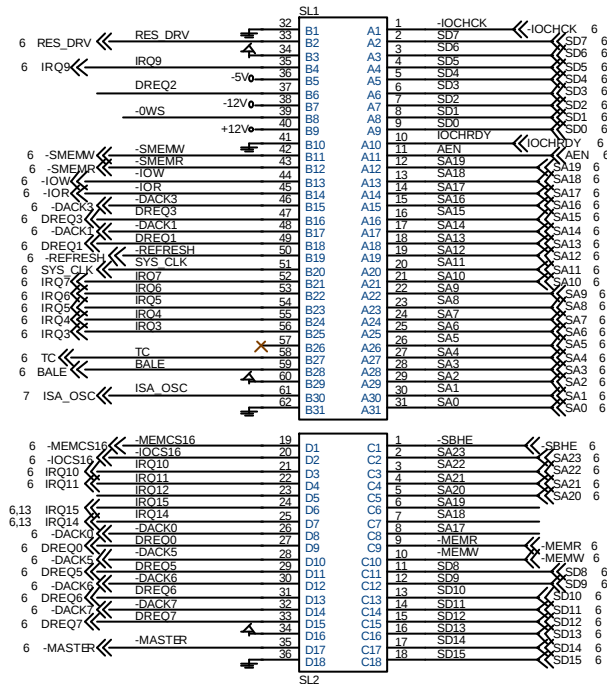
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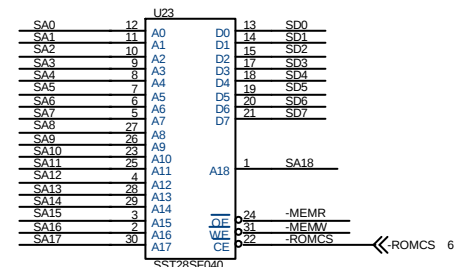
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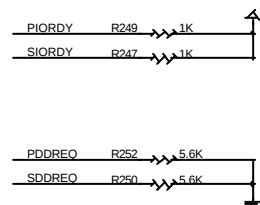
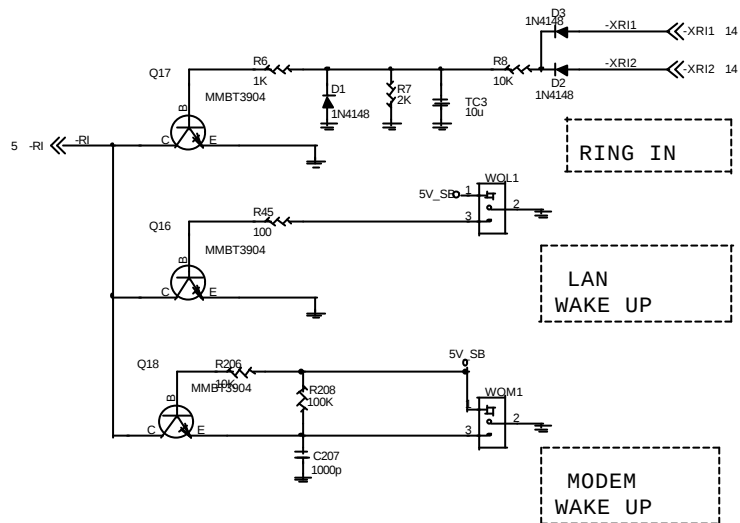


Thomas Hsu Removed XD SD connectors

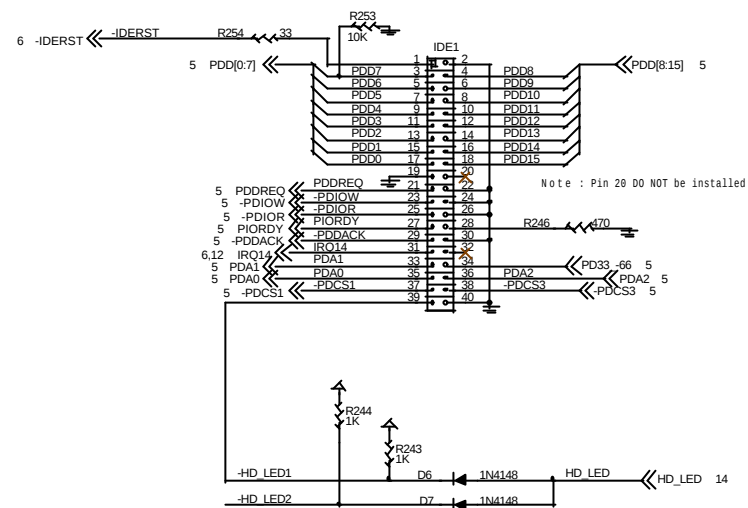


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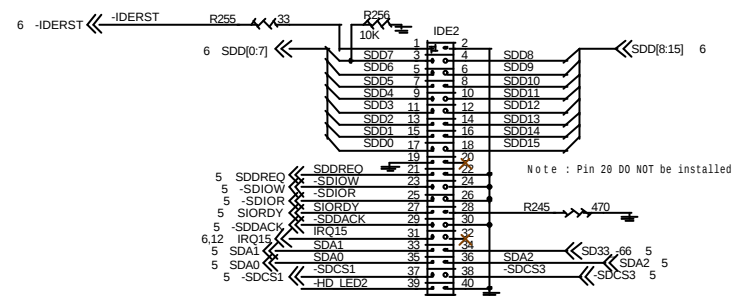
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PRIMARY



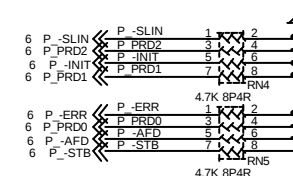
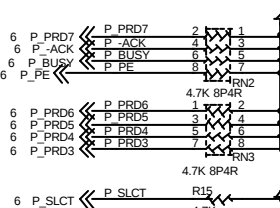
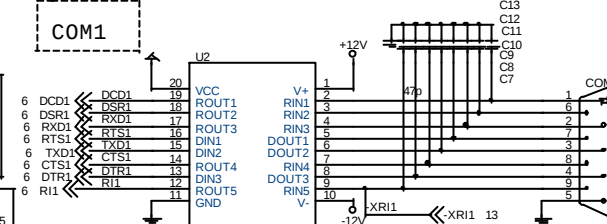
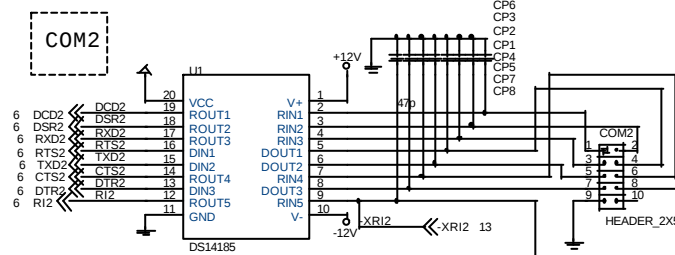
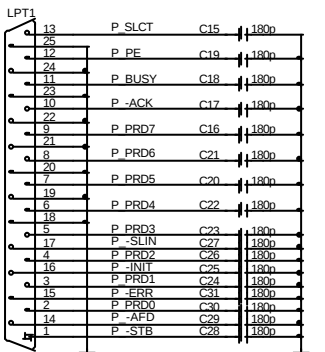
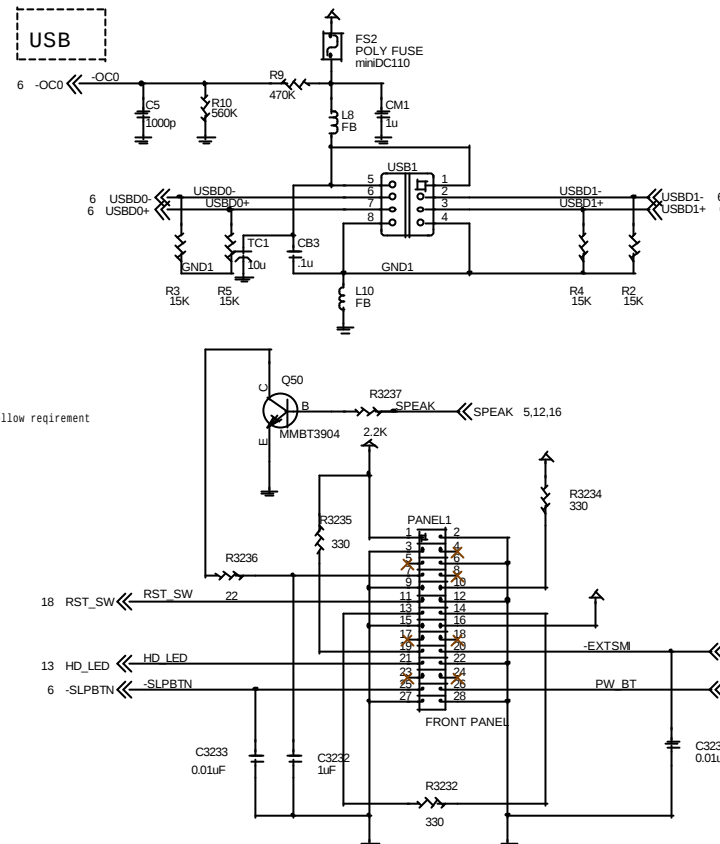
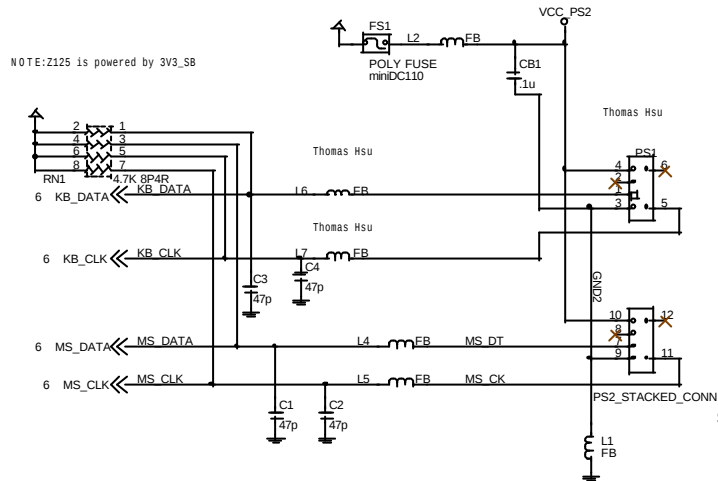
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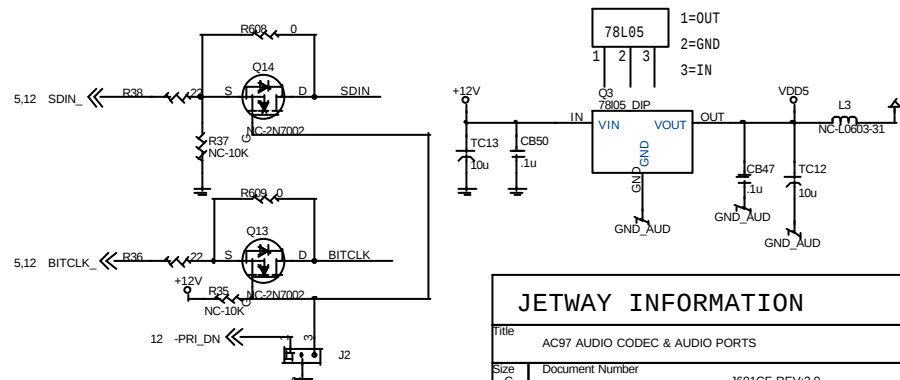
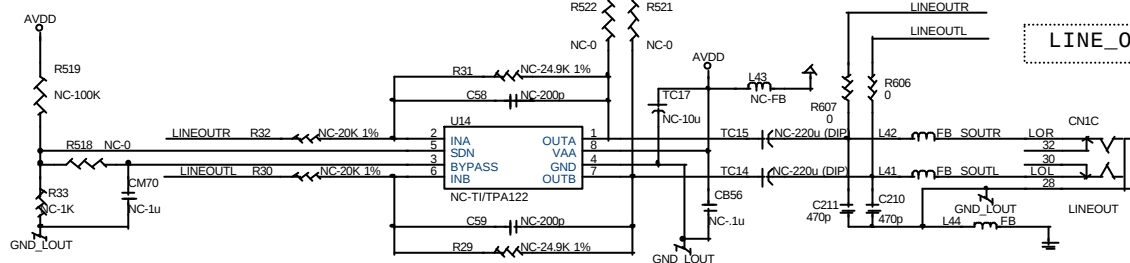
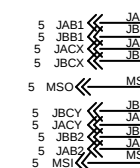
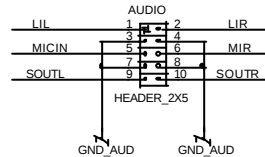
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IDE1 & IDE2		
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NOTE: Z125 is powered by 3V3_S8



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AC97 AUDIO CODEC & AUDIO PORTS			
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