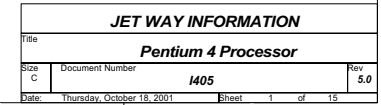
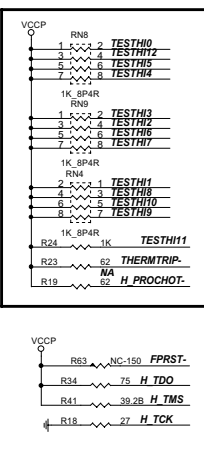
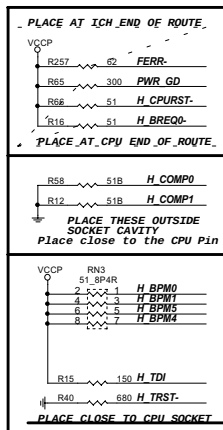
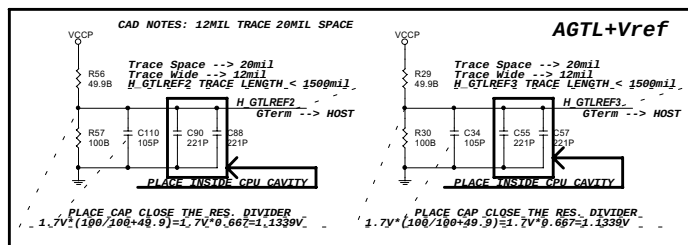
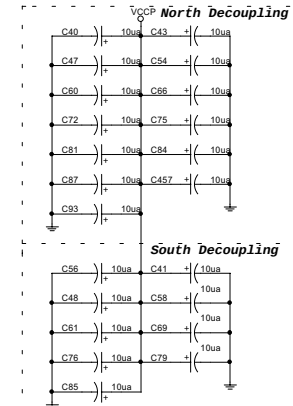
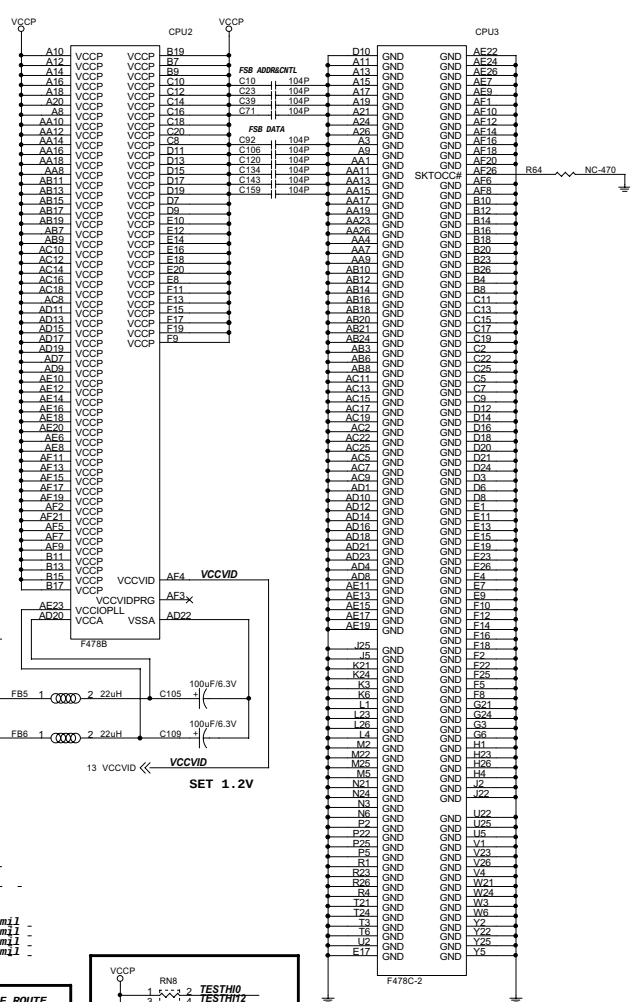
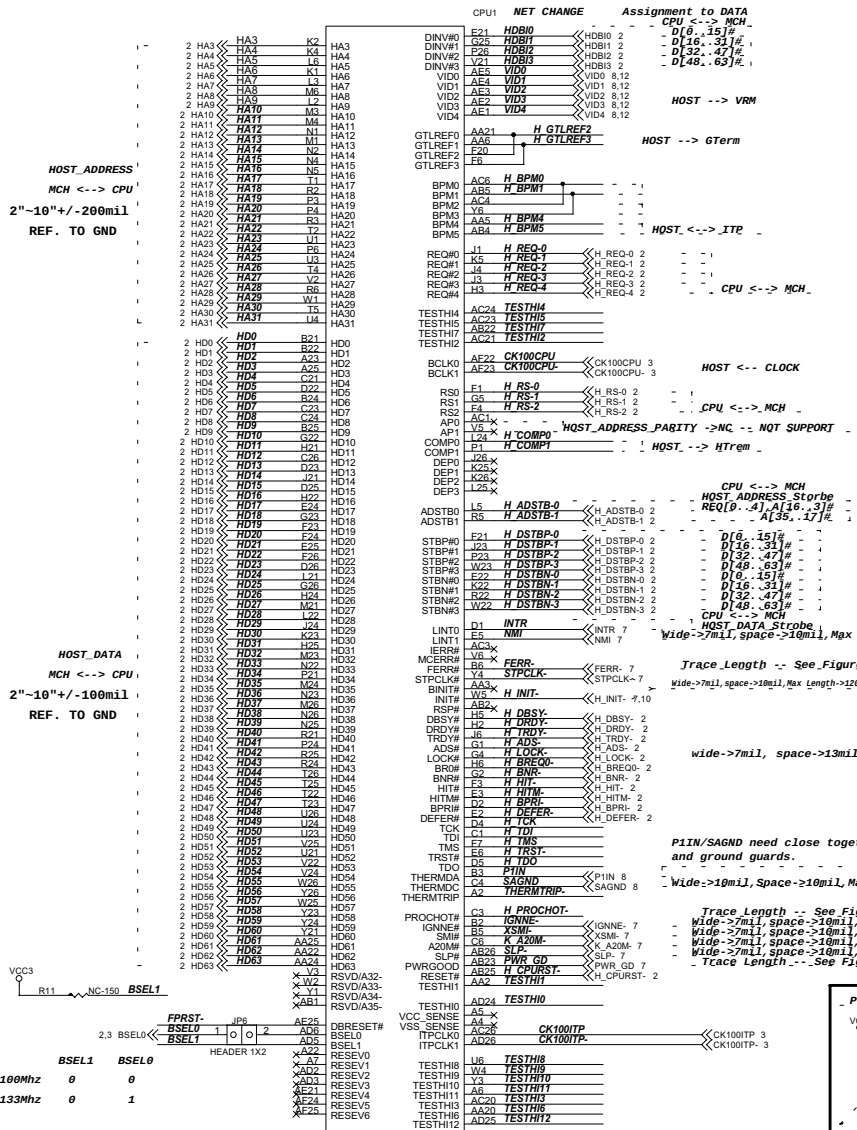
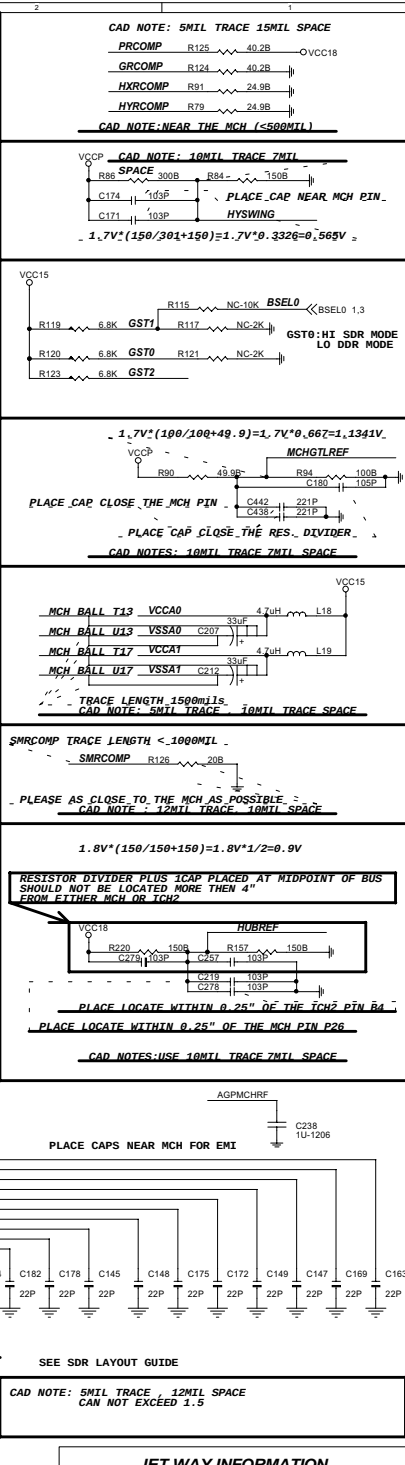
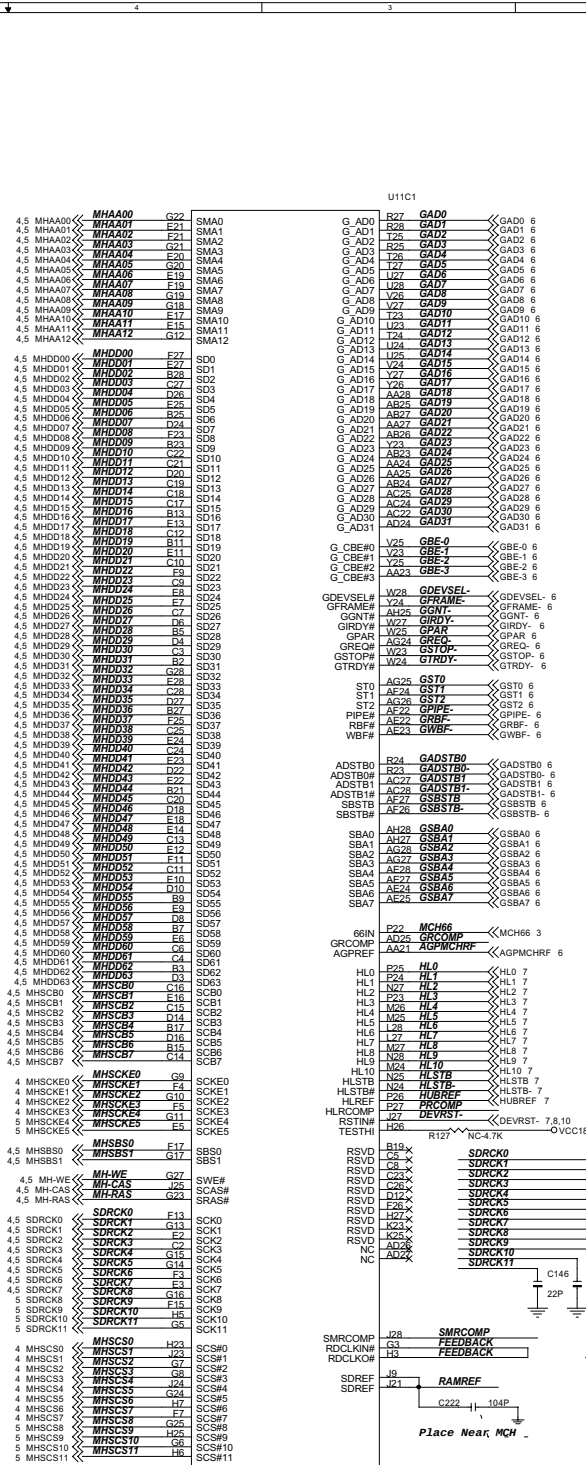
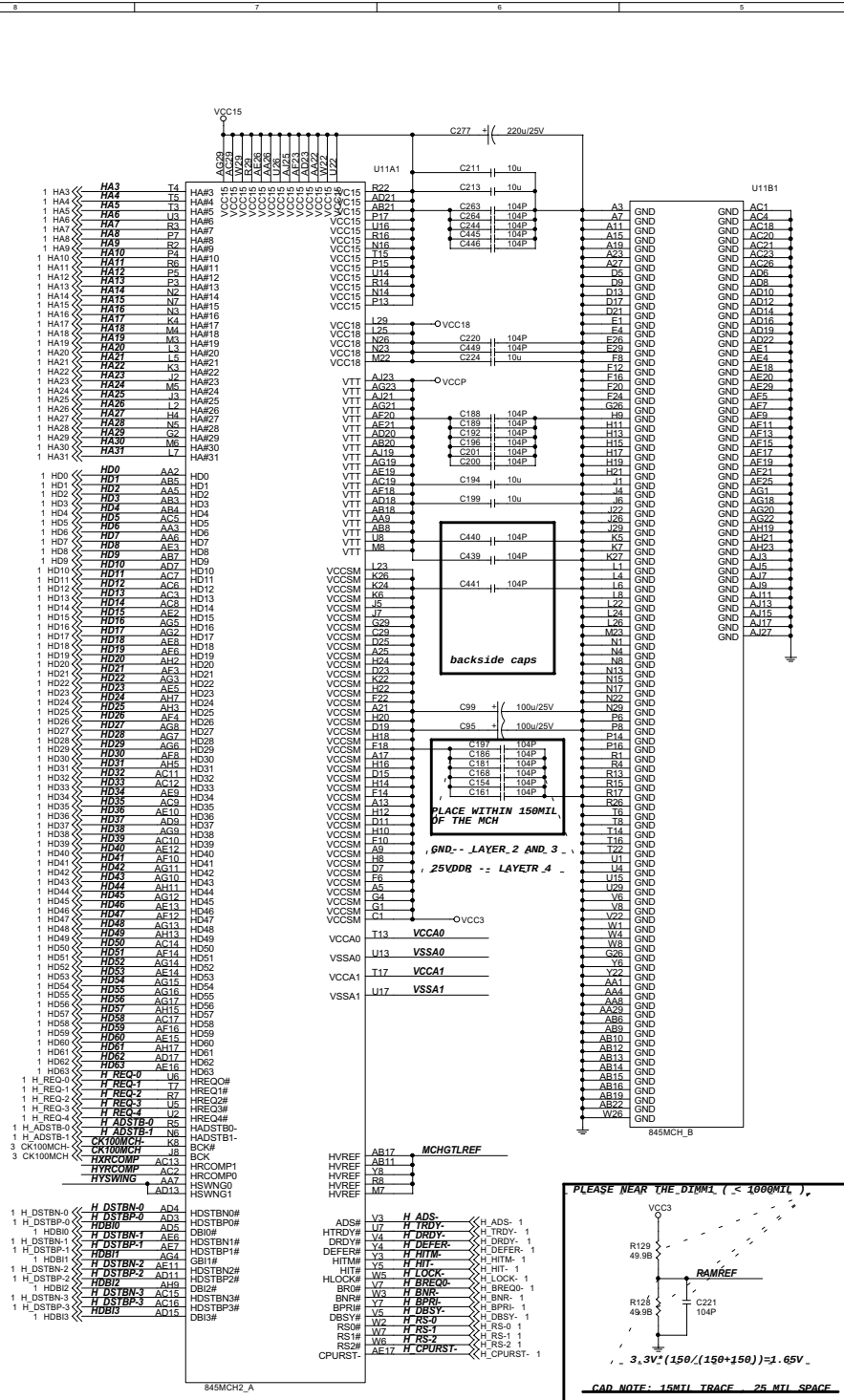
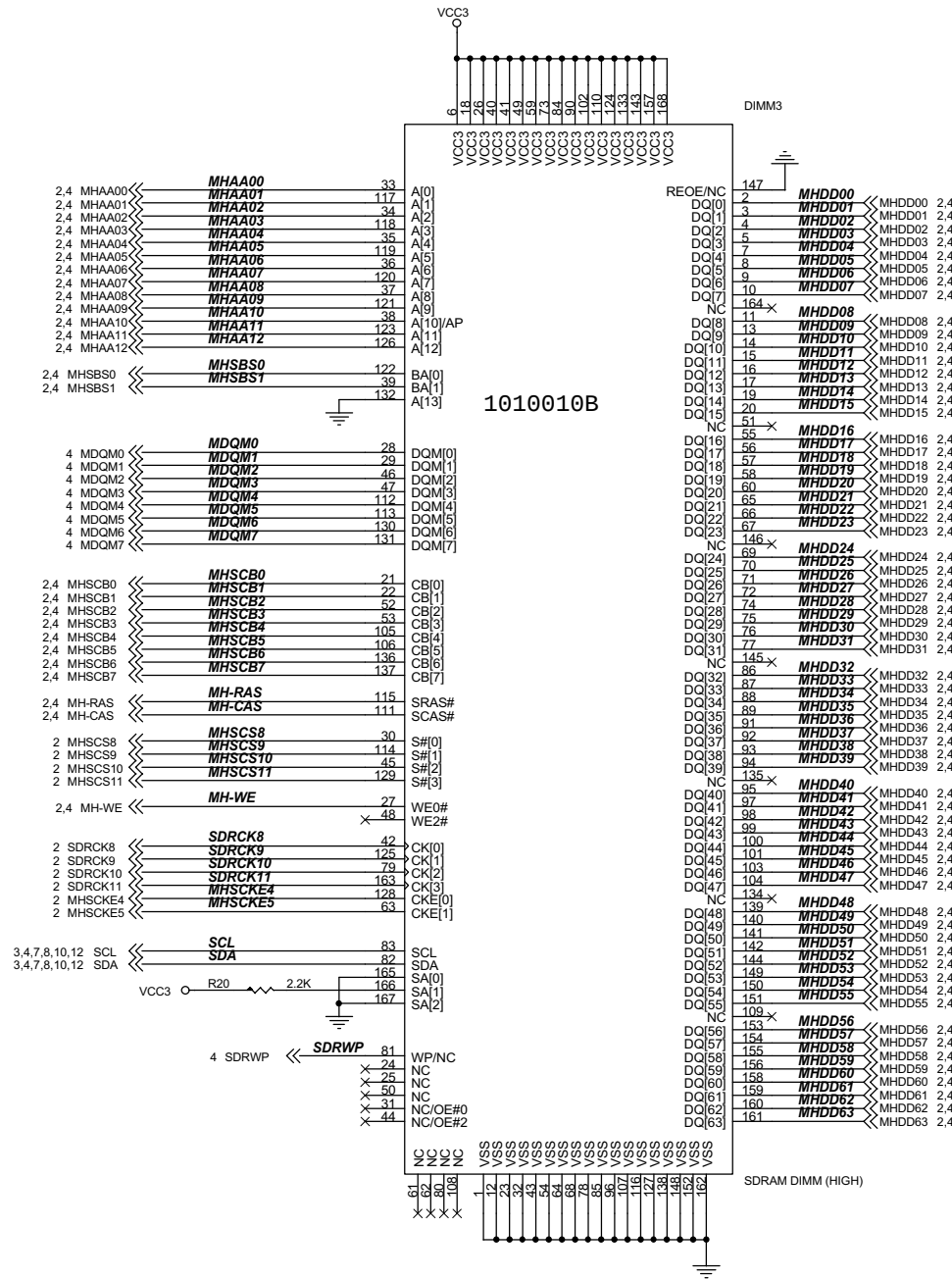


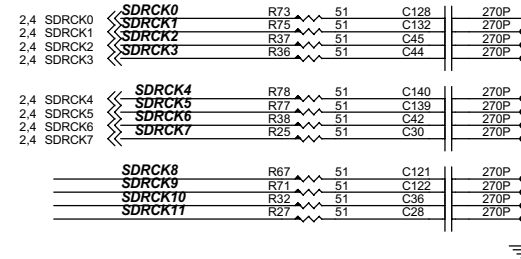
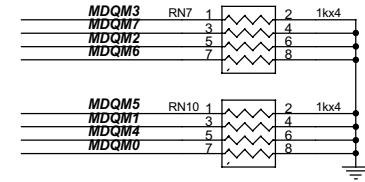
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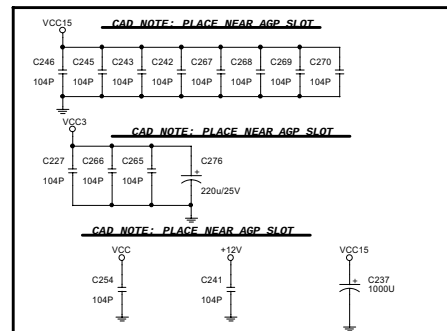
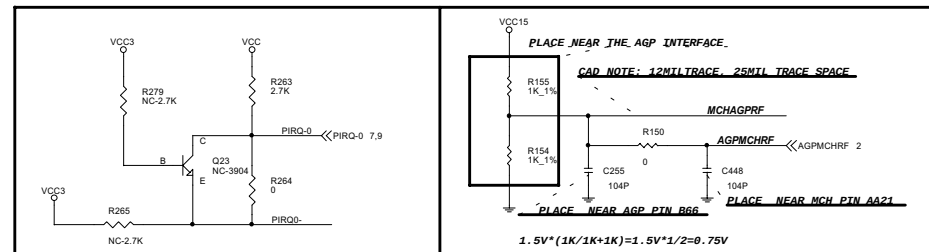
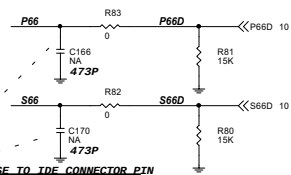
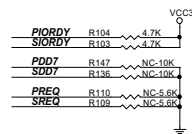
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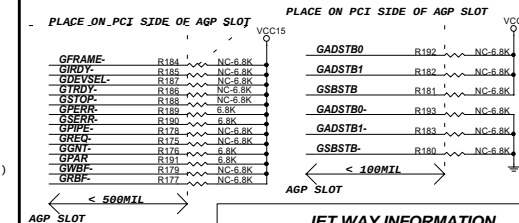
PLACE THOSE PARTS BELOW
3RD DIMM FOR TERMINATION

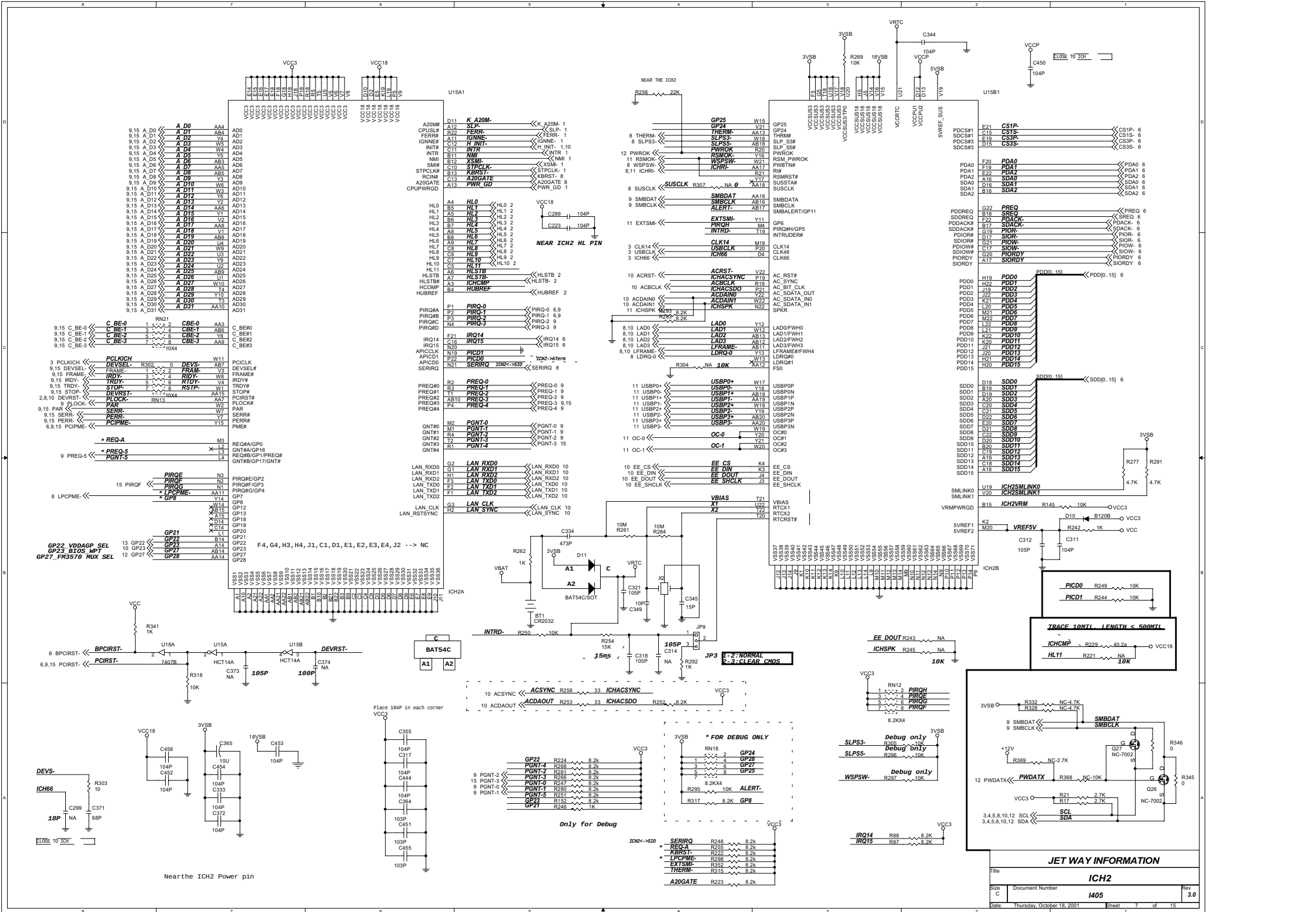
JET WAY INFORMATION

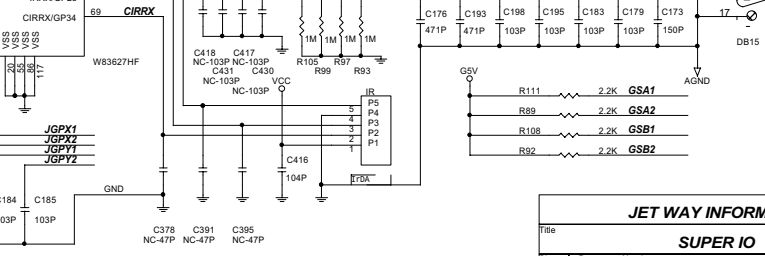
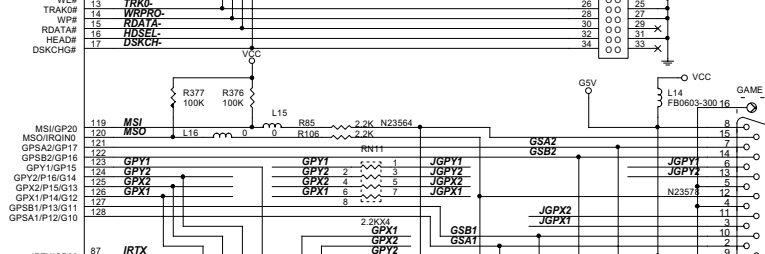
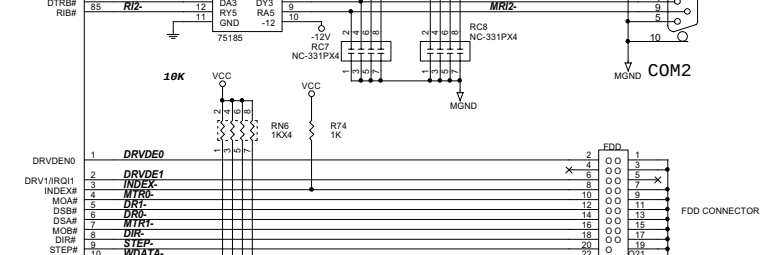
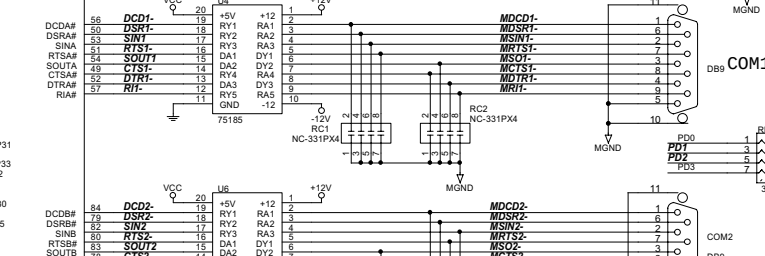
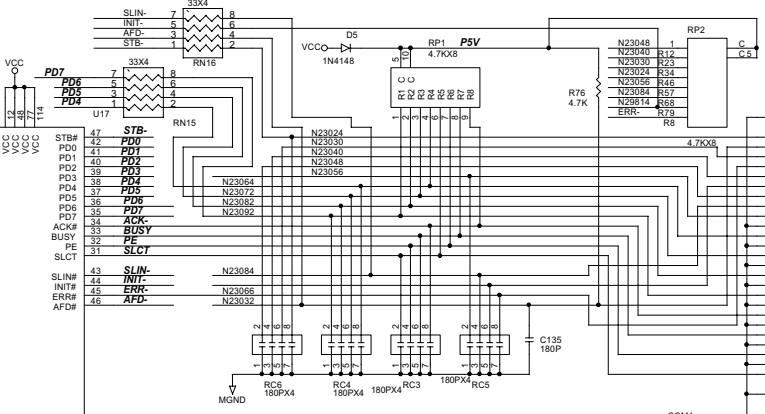
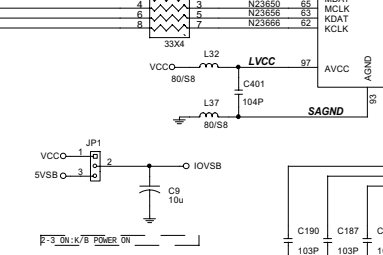
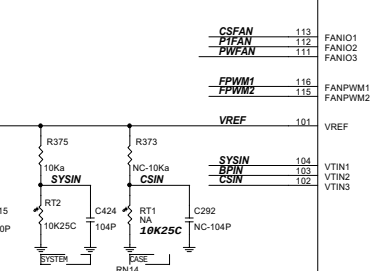
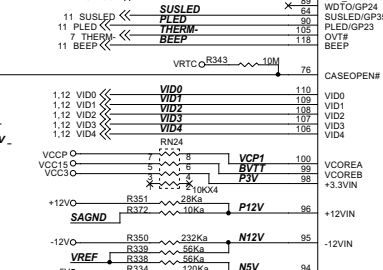
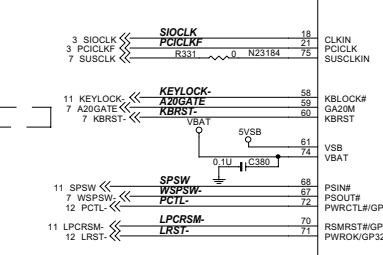
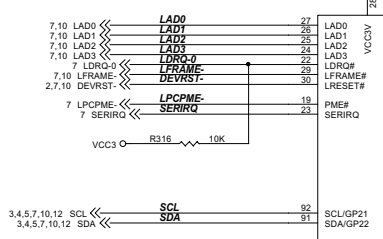
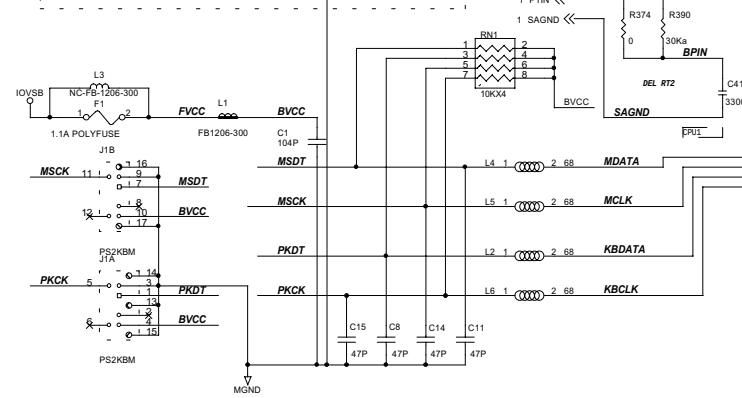
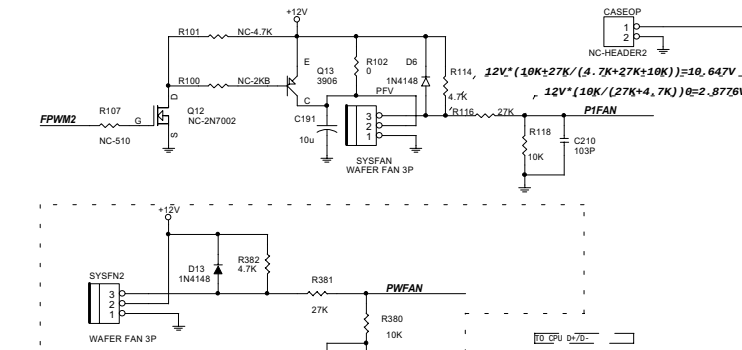
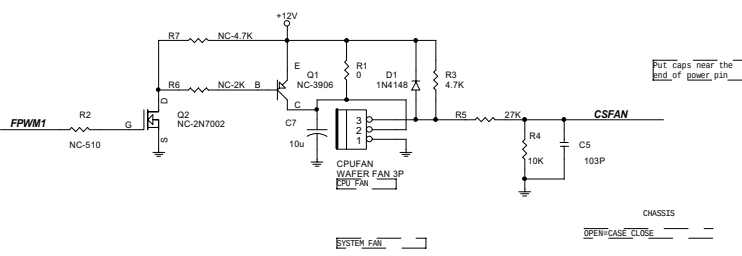
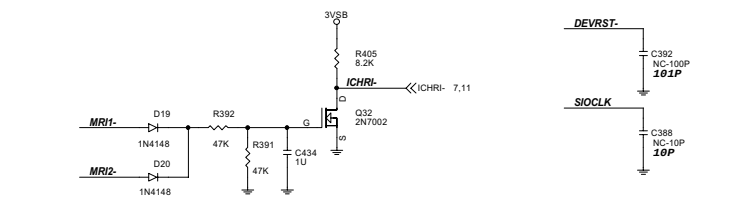
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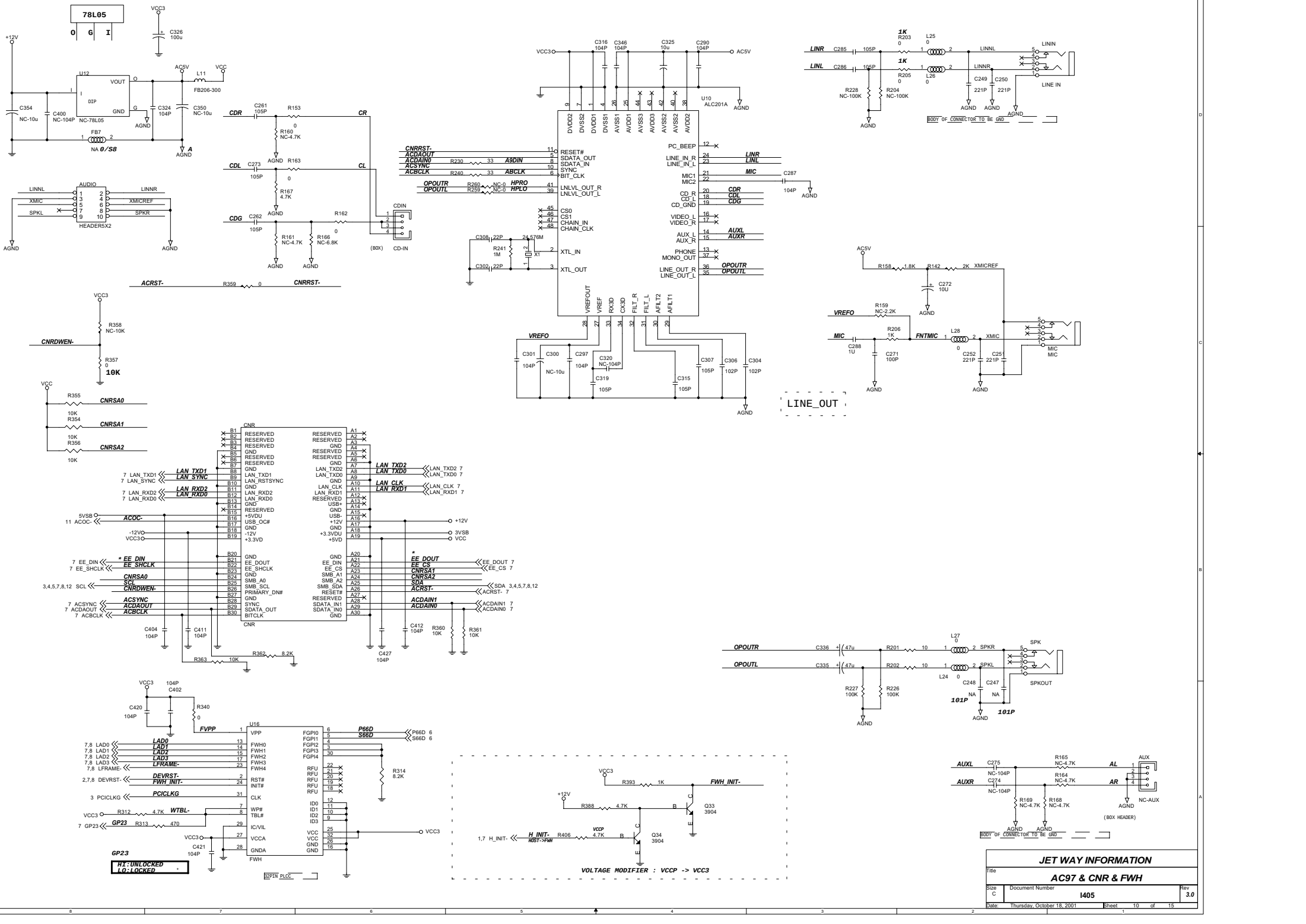


FX LTIMING DOMAIN SIGNALS	MISCELLANEOUS SIGNALS	2X/4X TIMING DOMAIN ROUTE 5 ON 15 FOR STROB SIGNAL (1:3:4) STROBE SIGNAL TO OTHERS ON 29 (1:3:4)
FXVENSEL	USBAPG#	GAD[09..31]
GDEVSEL	USBAPG-	GBE[0..3]
GTSTBY	MBPCC	GADSTB0
GTSTBY	PCIPME	GADSTB8
GPWR	TPYDDET	GADSTB1
GPWR	GPERR	GADSTB10
GPWR	GSERR	GADSTB11
GGNT	PIRQ0	CSBA[0..7]
GPME	PIRQ1	GSBSTB
GQMB-		GSBSTB-
GGST[0..2]		



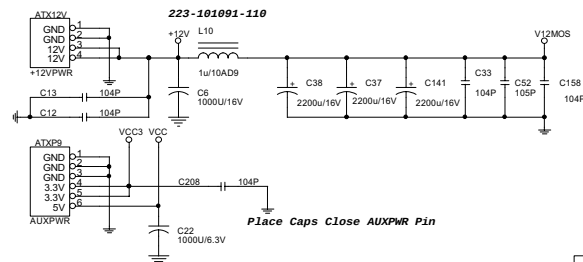
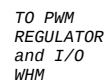
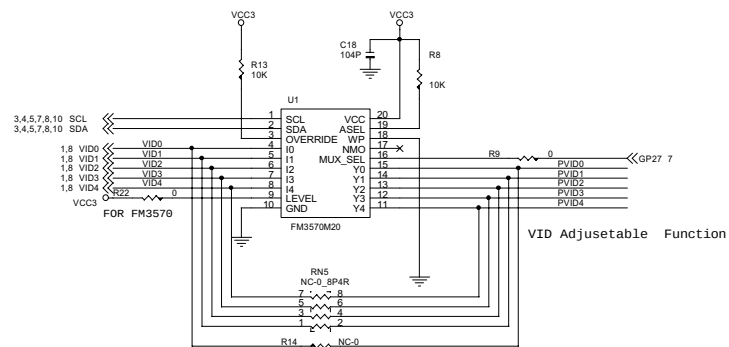
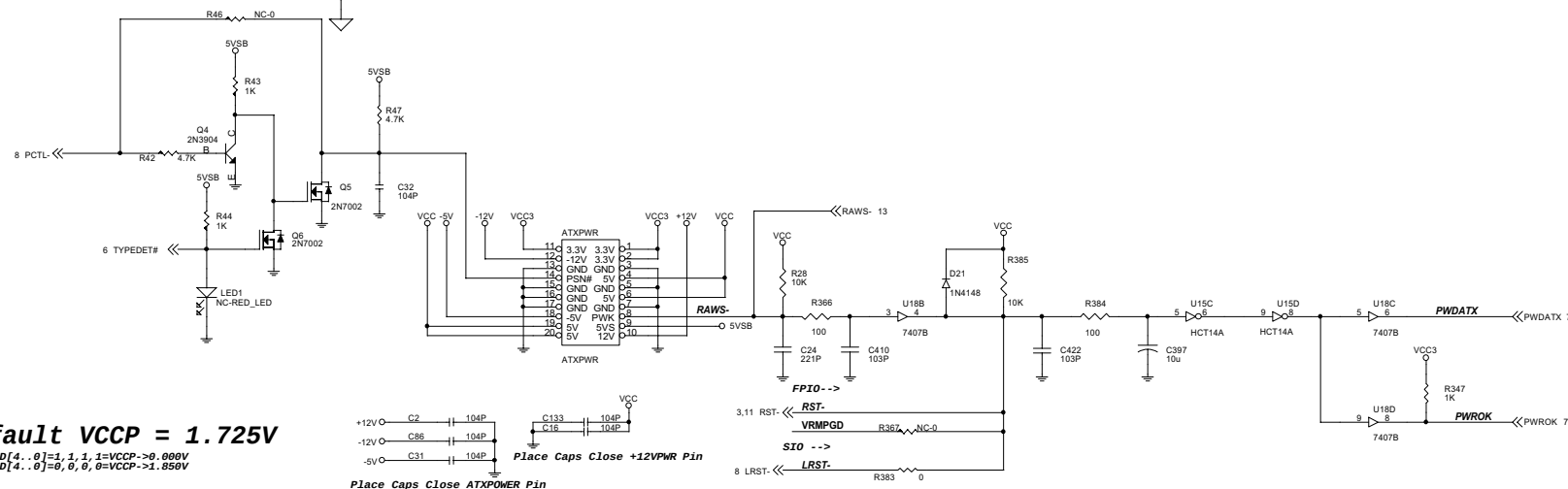
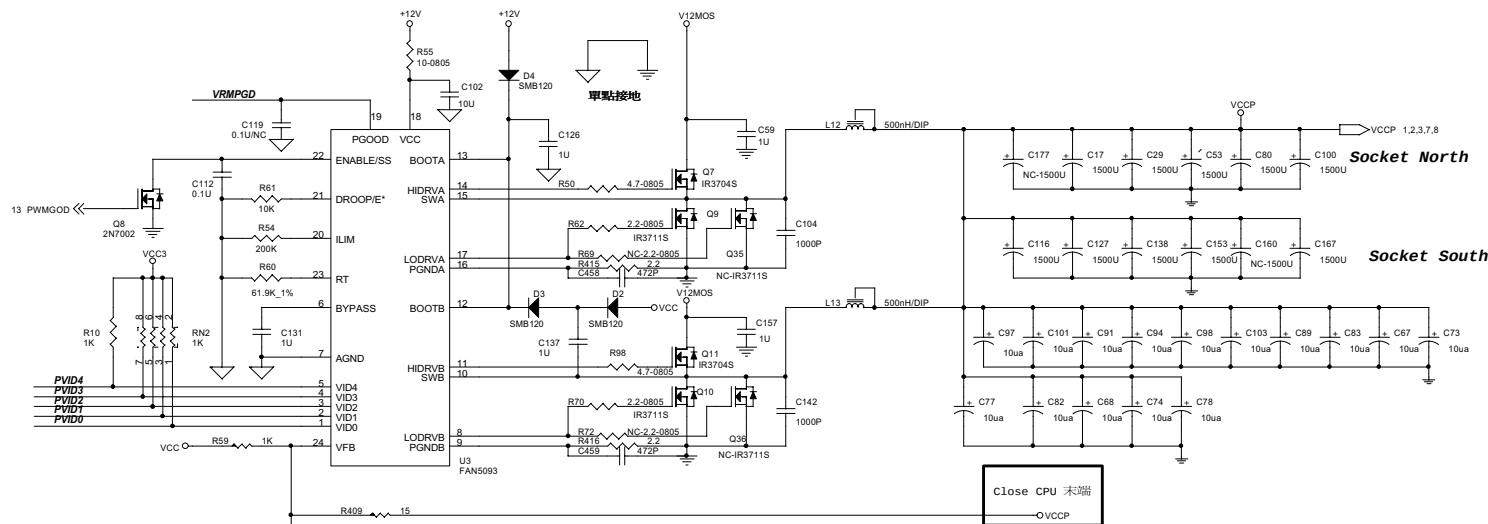






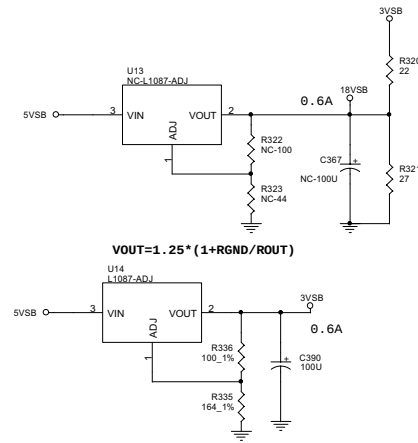
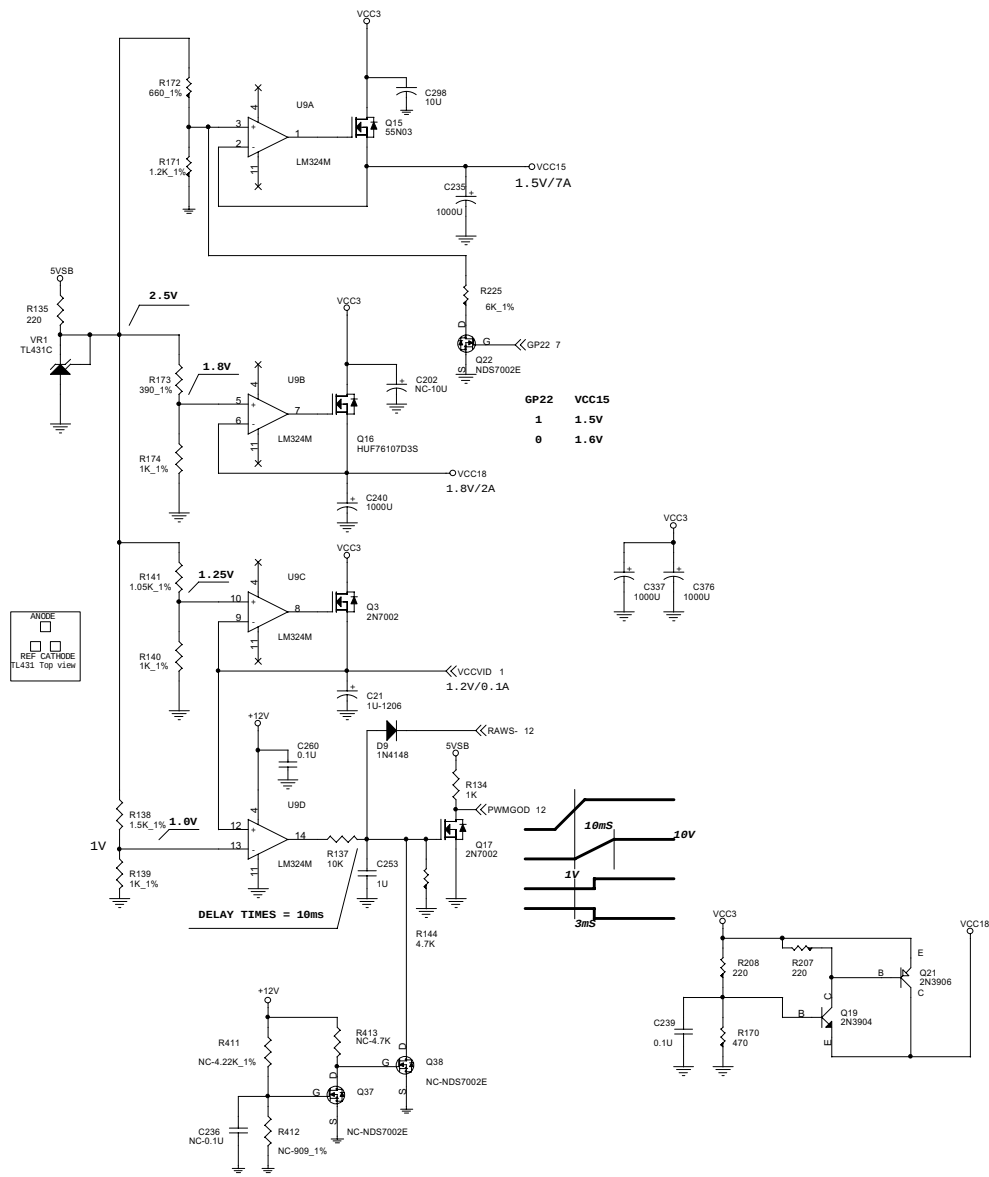


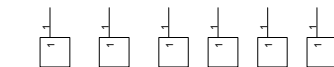
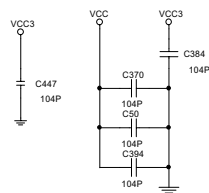
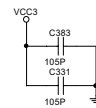
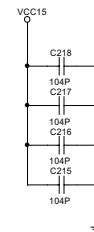
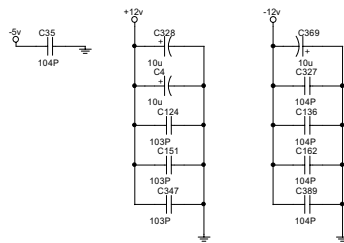
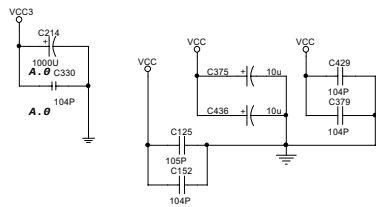
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1	1	1	1	0	1.125
1	1	1	0	1	1.125
1	1	1	0	0	1.150
1	1	0	1	1	1.150
1	1	0	1	0	1.200
1	1	0	0	1	1.200
1	1	0	0	0	1.250
1	0	1	1	1	1.275
1	0	1	1	0	1.300
1	0	1	0	1	1.325
1	0	1	0	0	1.350
1	0	0	1	1	1.375
1	0	0	1	0	1.400
1	0	0	0	1	1.450
1	0	0	0	0	1.450
0	1	1	1	0	1.500
0	1	1	0	1	1.500
0	1	1	0	0	1.550
0	1	0	1	1	1.575
0	1	0	1	0	1.600
0	1	0	0	1	1.625
0	1	0	0	0	1.650
0	0	1	1	1	1.700
0	0	1	1	0	1.725
0	0	1	0	1	1.750
0	0	1	0	0	1.750
0	0	0	1	1	1.800
0	0	0	1	0	1.825
0	0	0	0	1	1.850



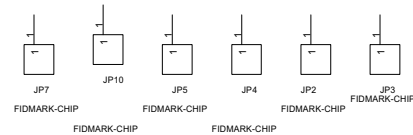
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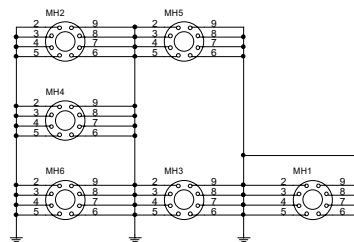
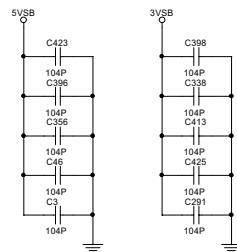
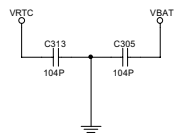




M5 M3 M1 M2 M6 M4
FIDMARK FIDMARK PAD4MM PAD4MM PAD4MM PAD4MM



JP7 JP10 JP5 JP4 JP2 JP3
FIDMARK-CHIP FIDMARK-CHIP FIDMARK-CHIP FIDMARK-CHIP FIDMARK-CHIP FIDMARK-CHIP



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