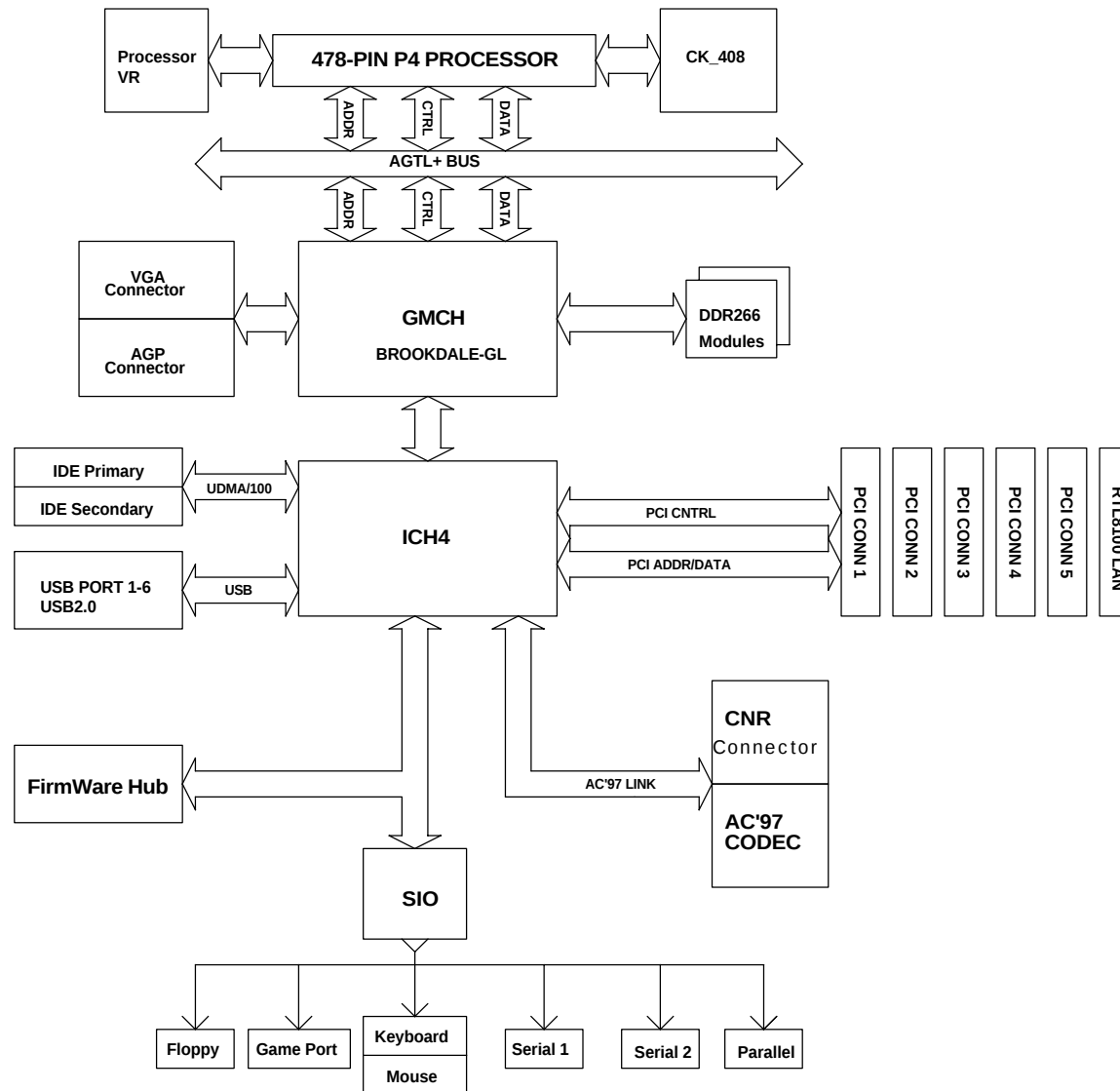


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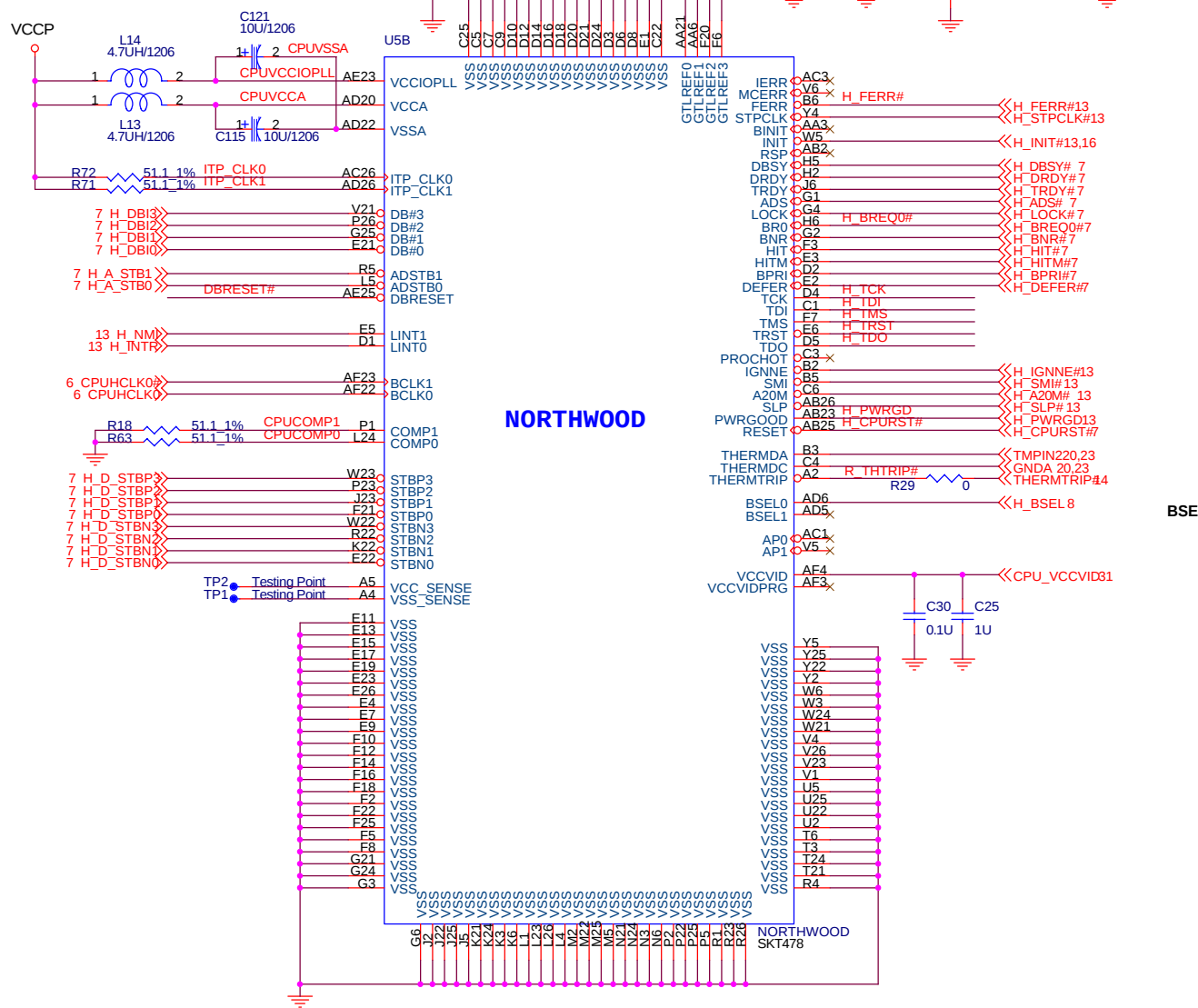
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BLOCK DIAGRAM

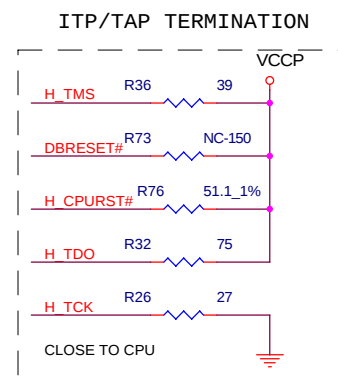
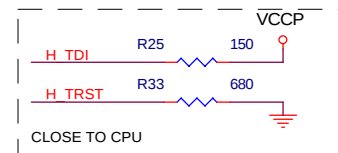
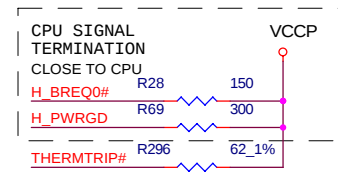


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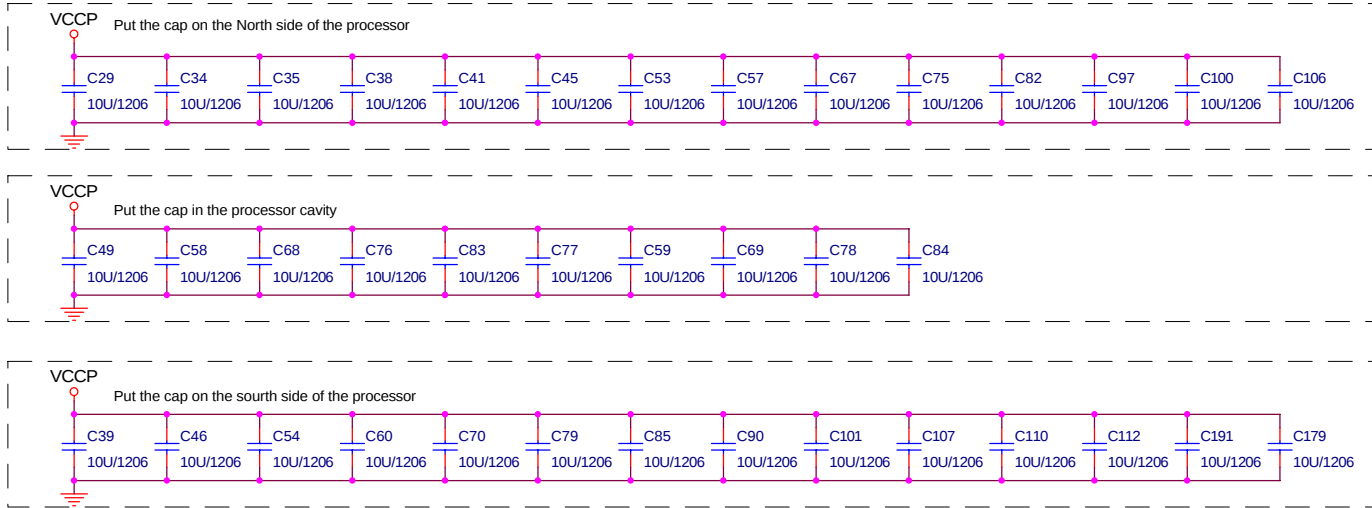
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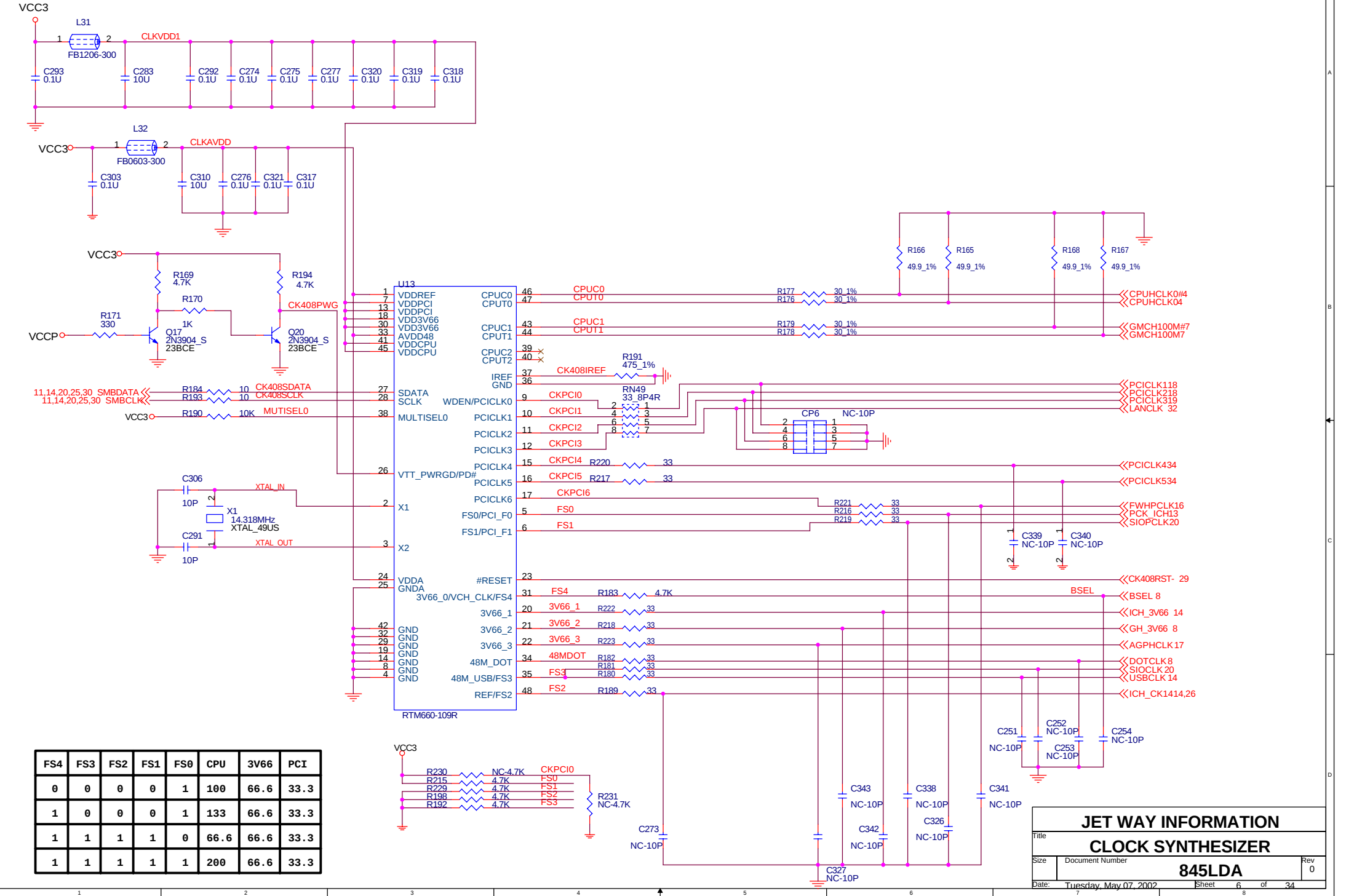
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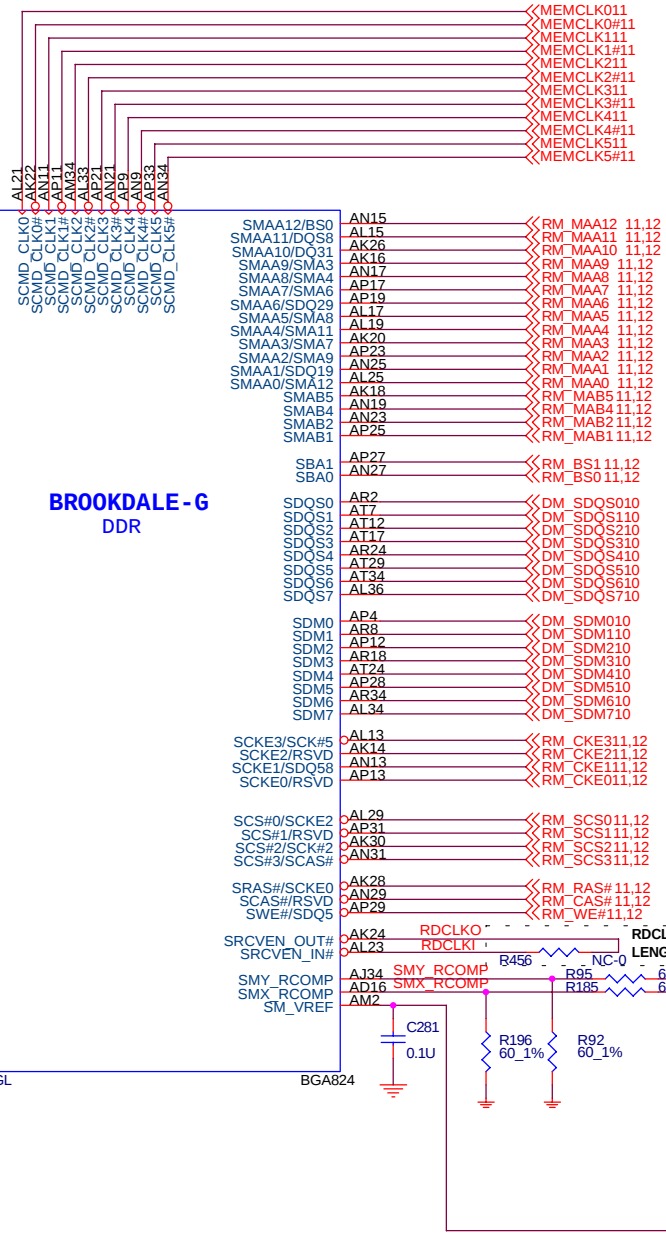
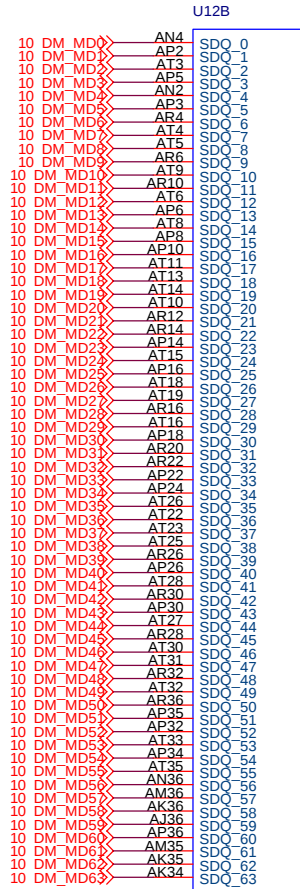


BSEL0: L:100M
H:133M

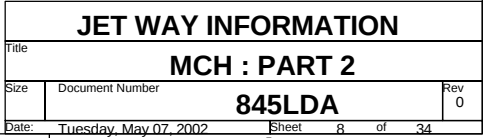


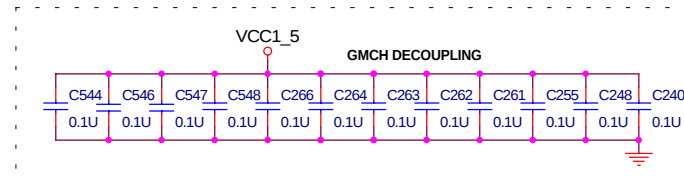
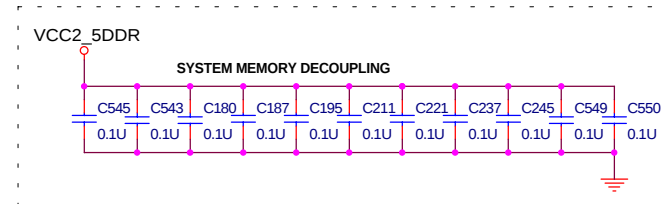
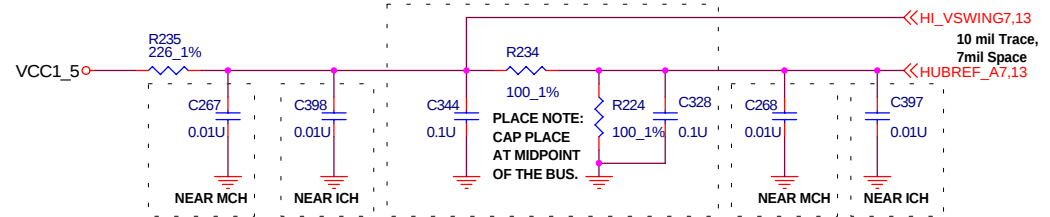
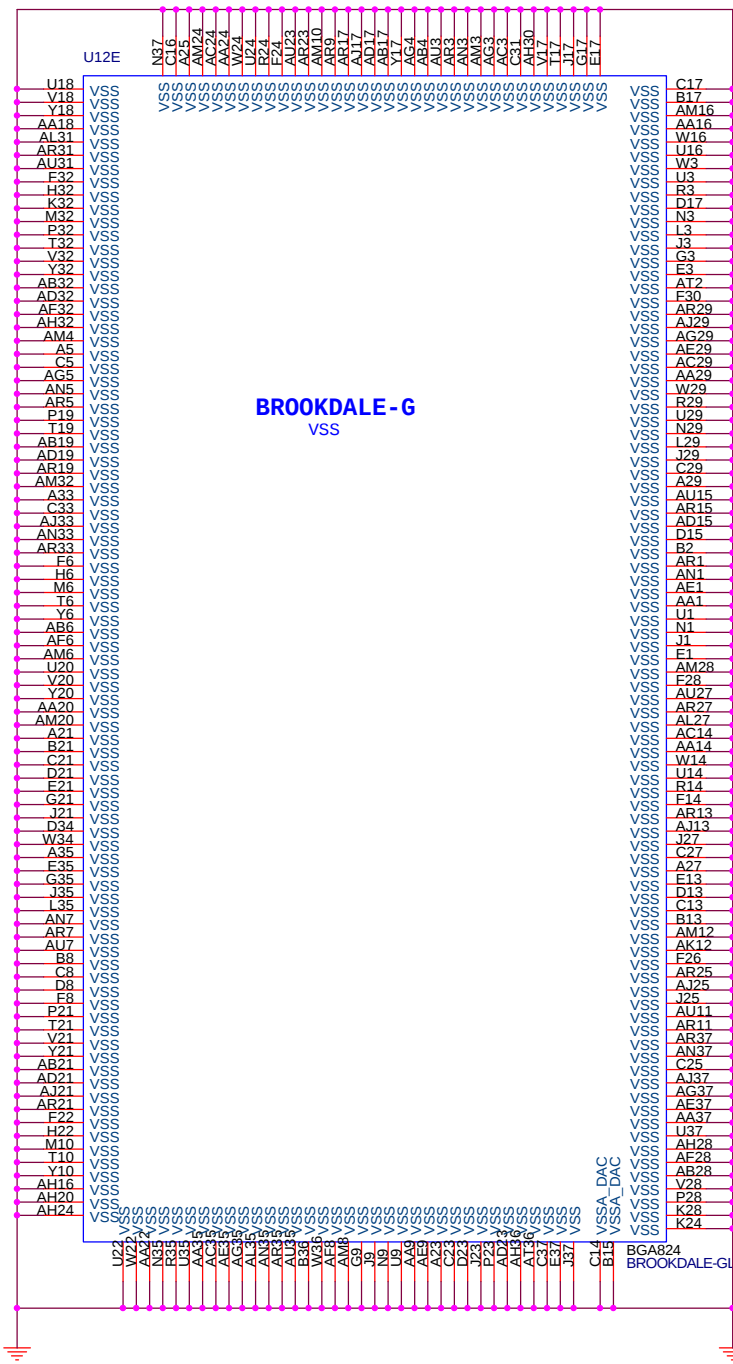
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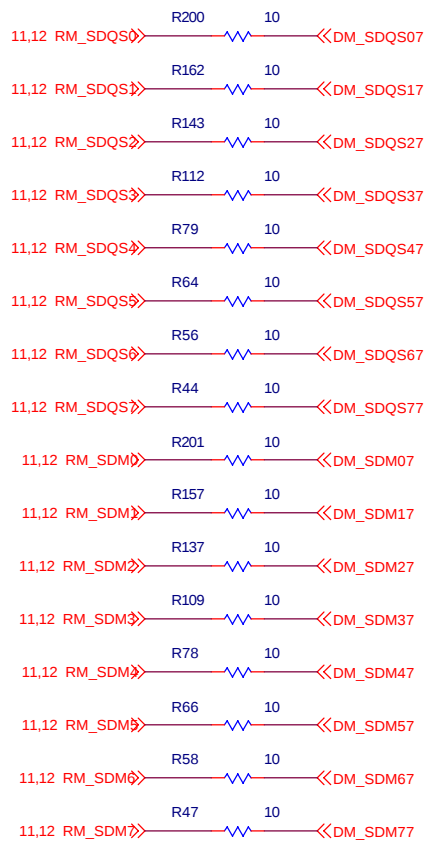
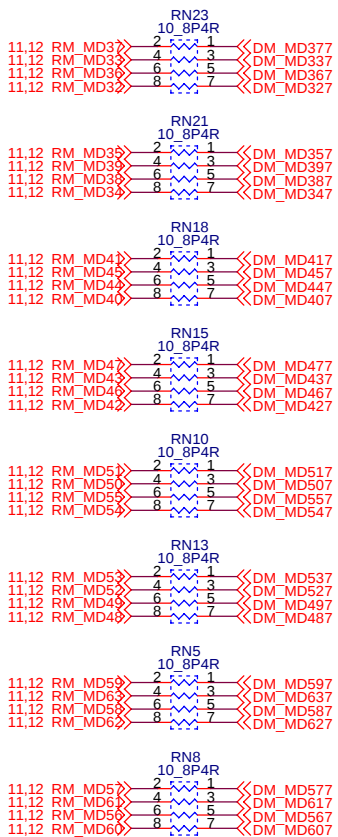
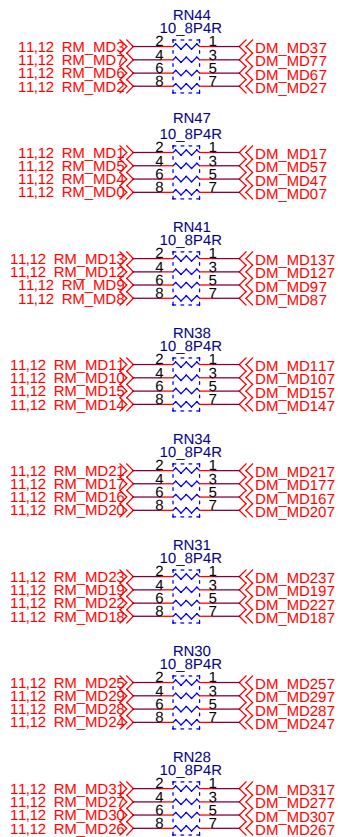


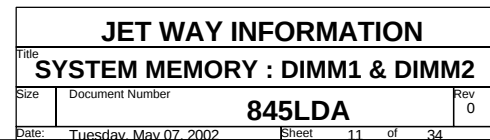
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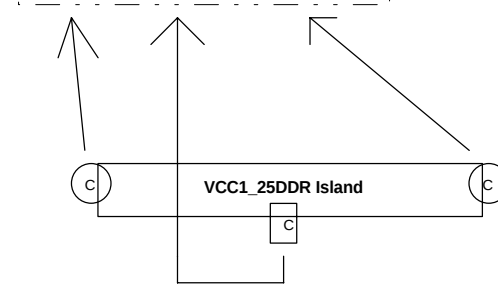
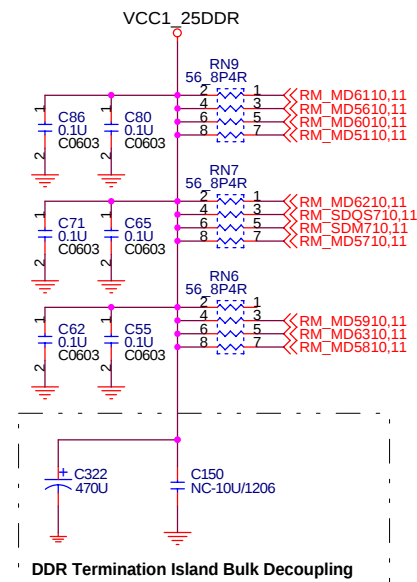
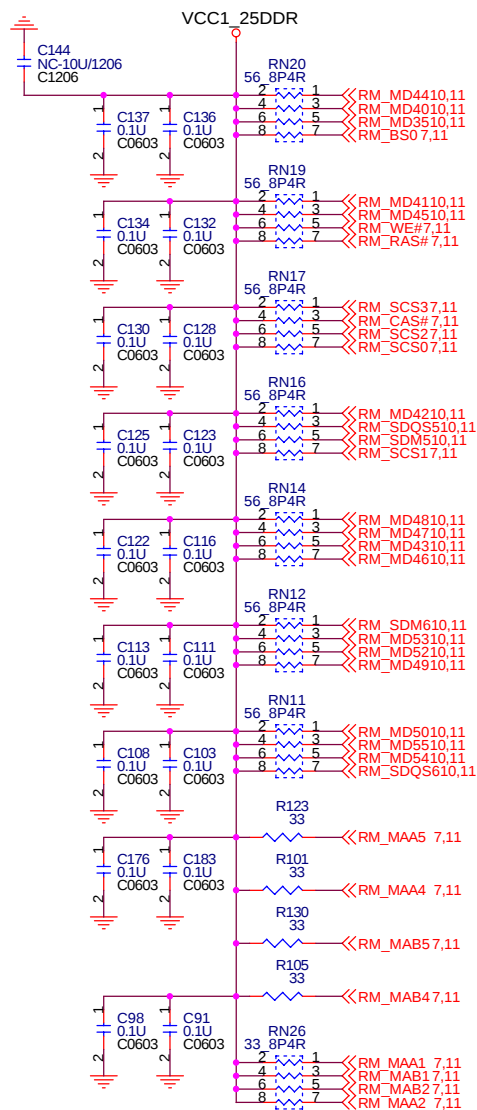
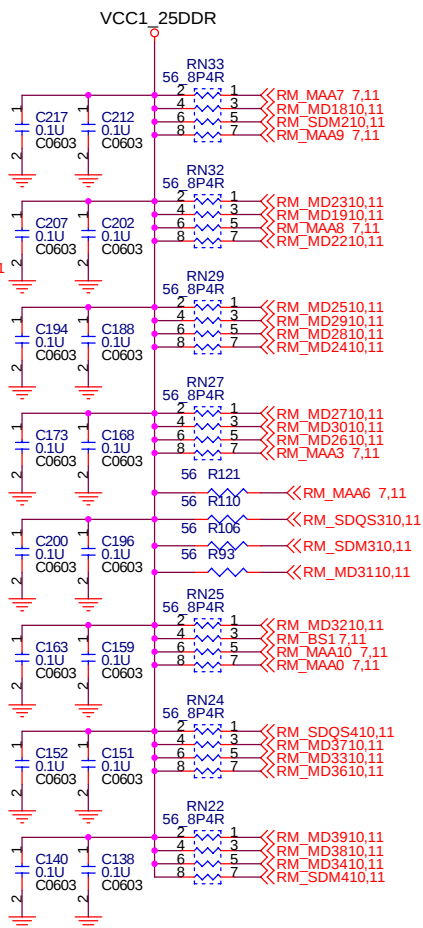
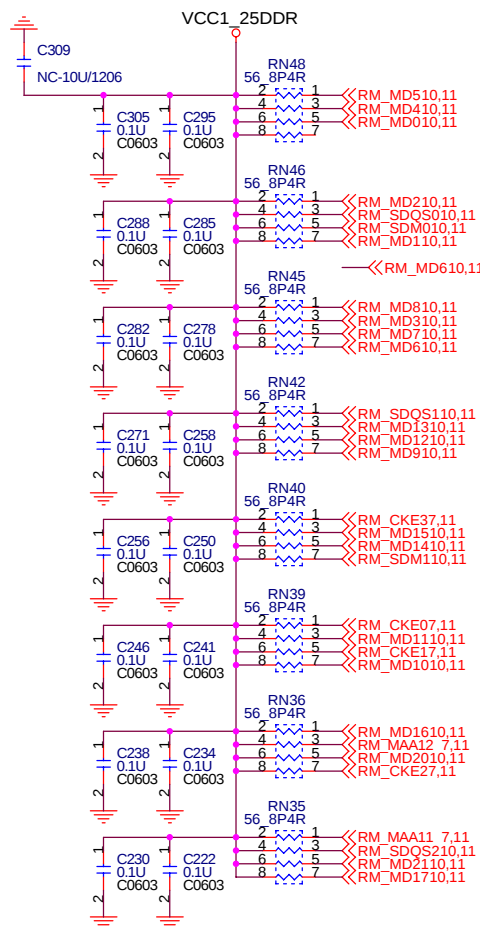




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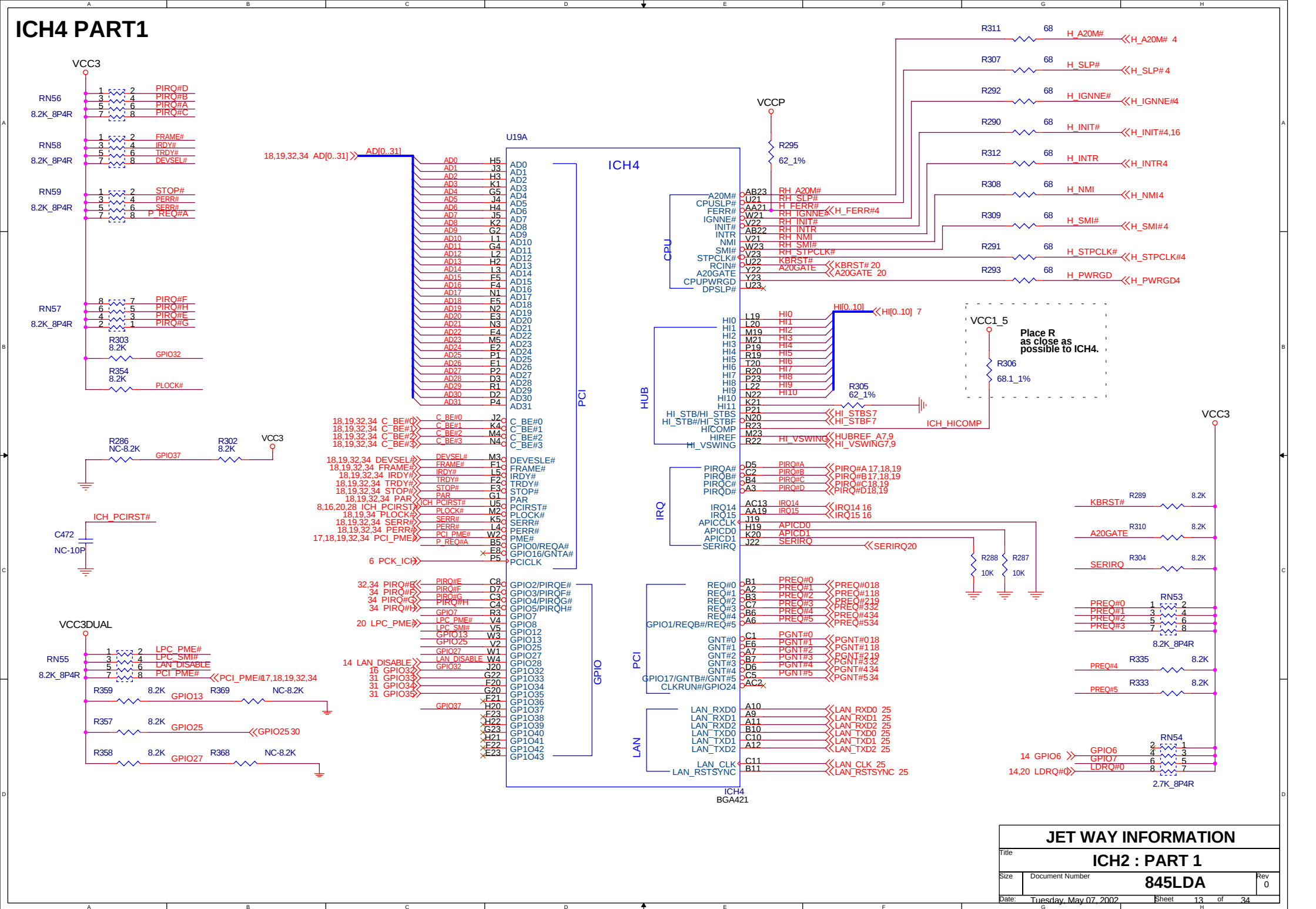




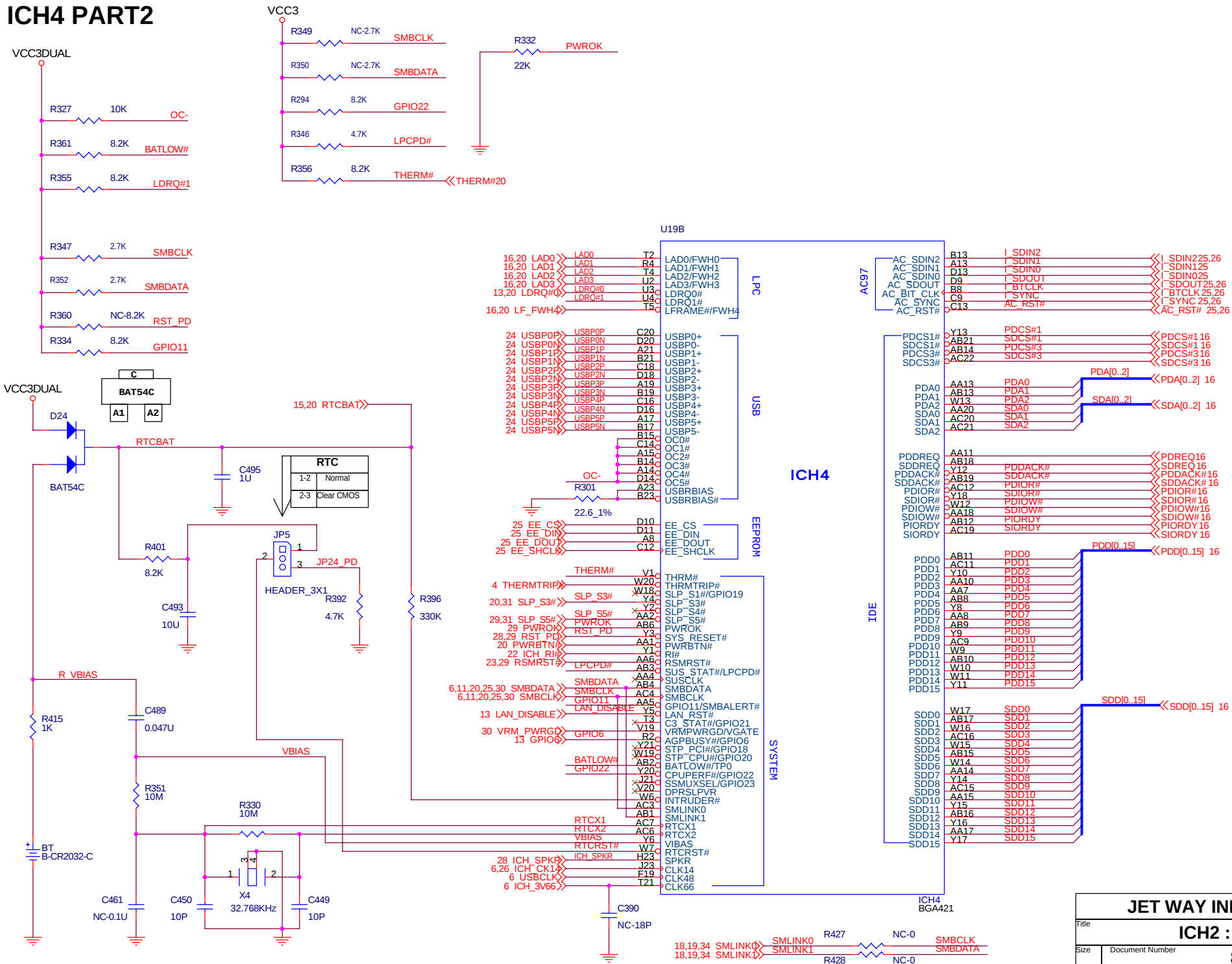


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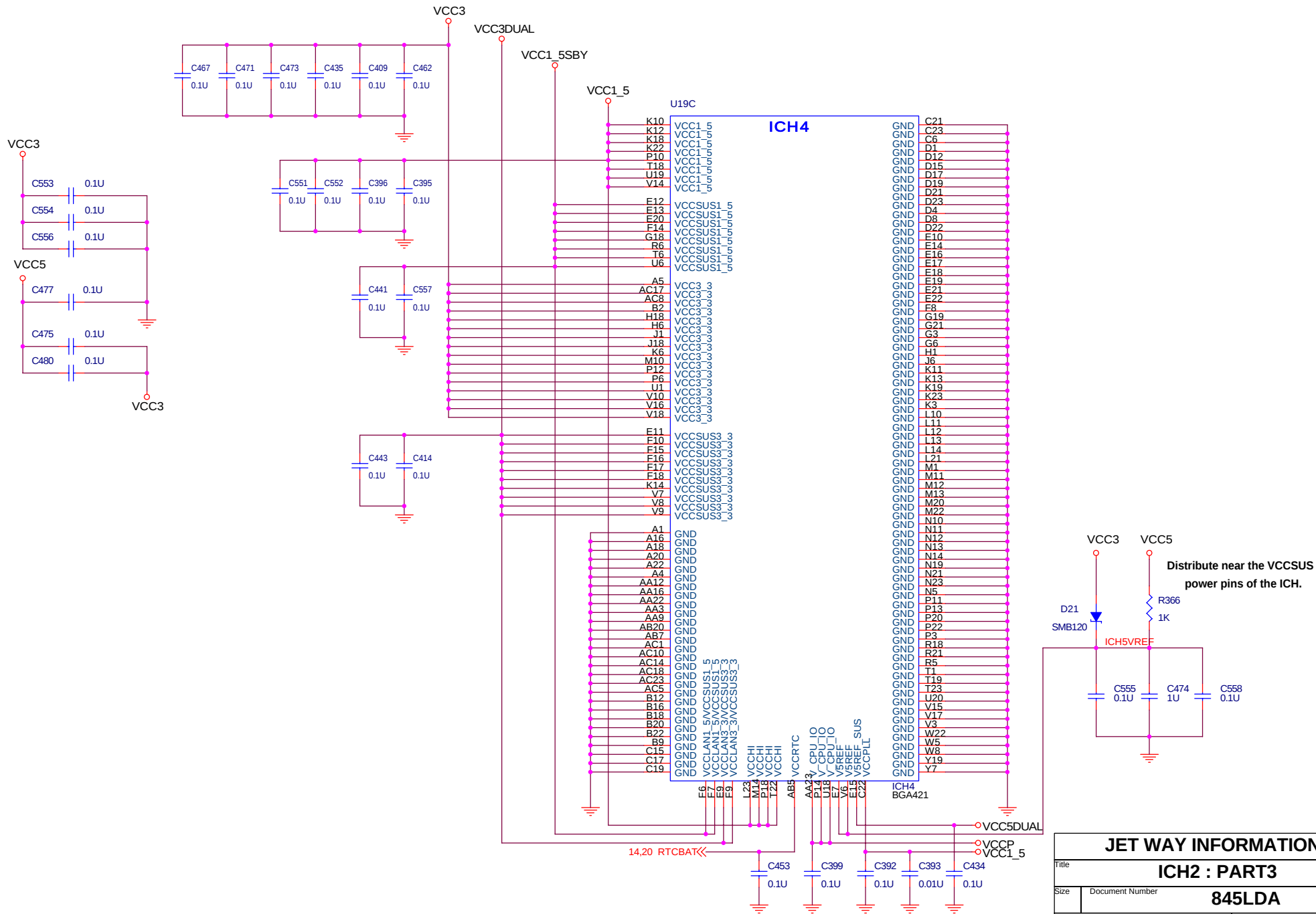
ICH4 PART1



ICH4 PART2



ICH4 PART3



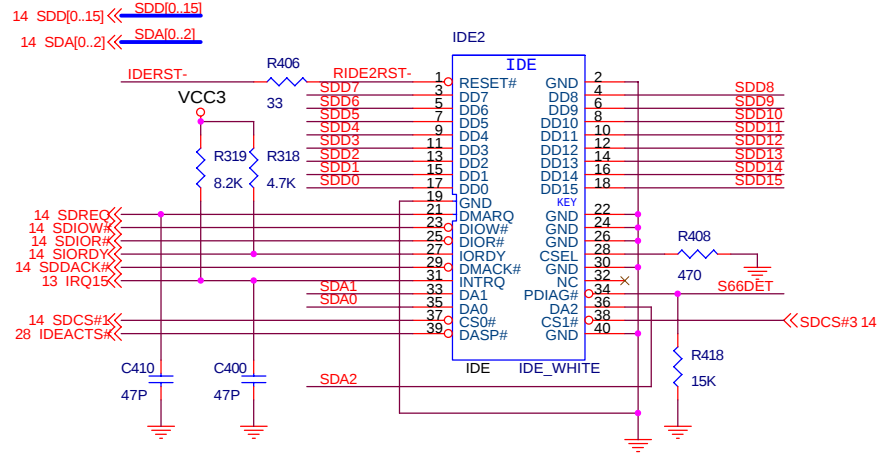
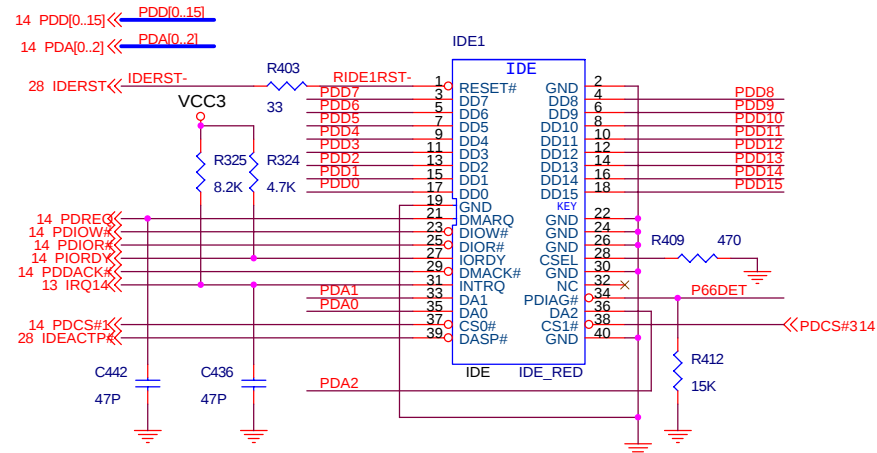
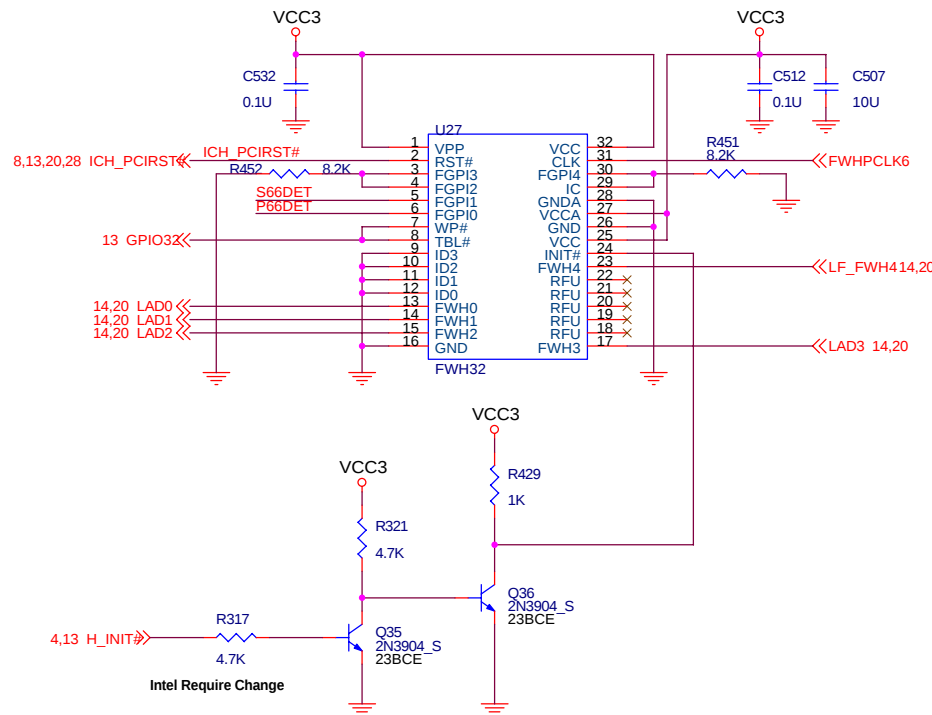
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ICH2 : PART3

845LDA

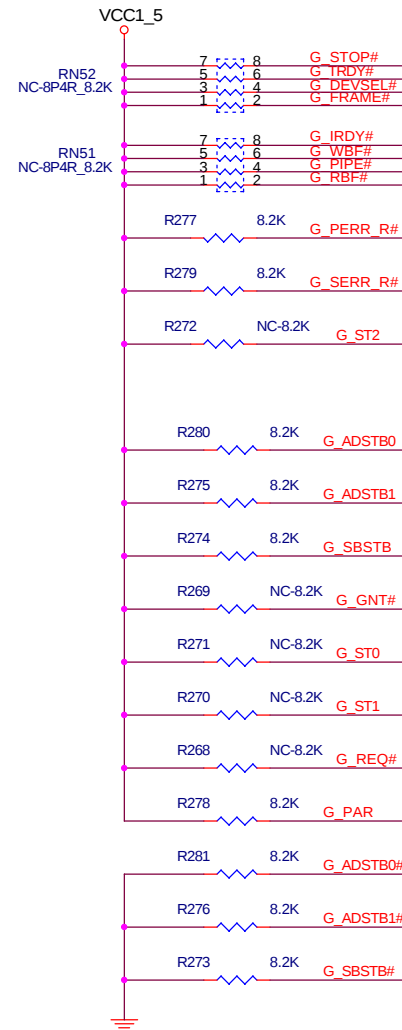
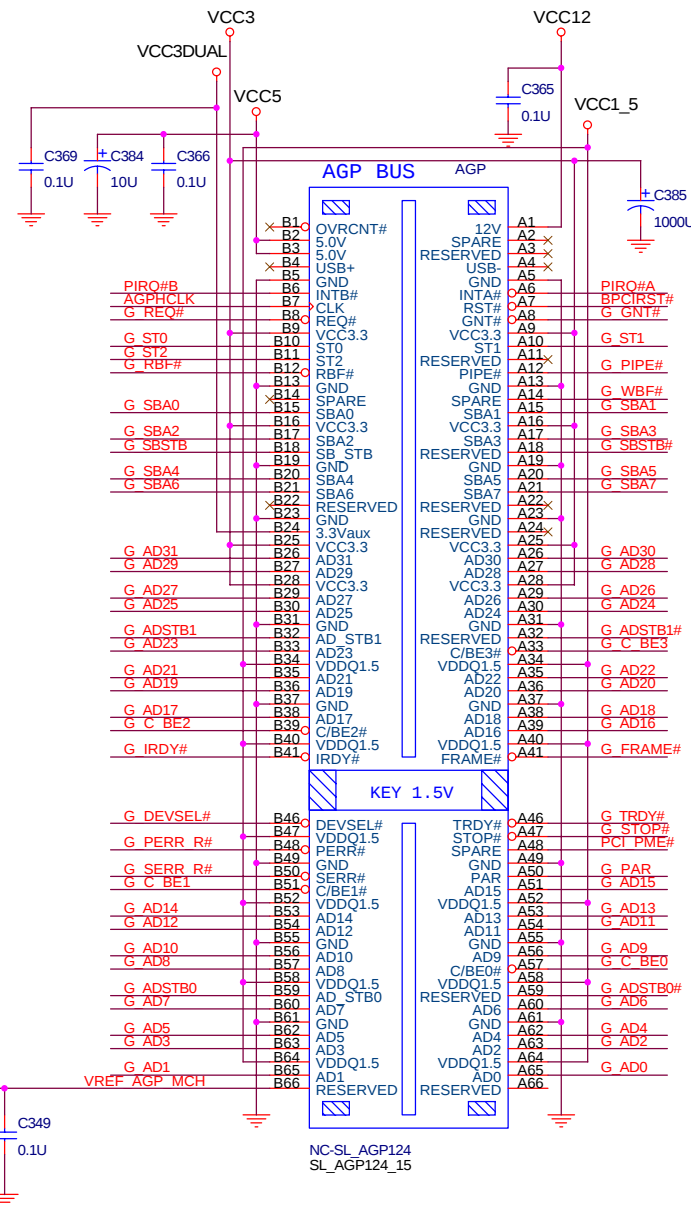
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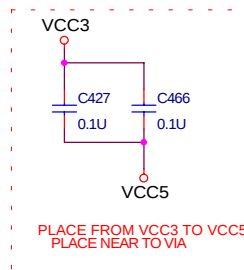
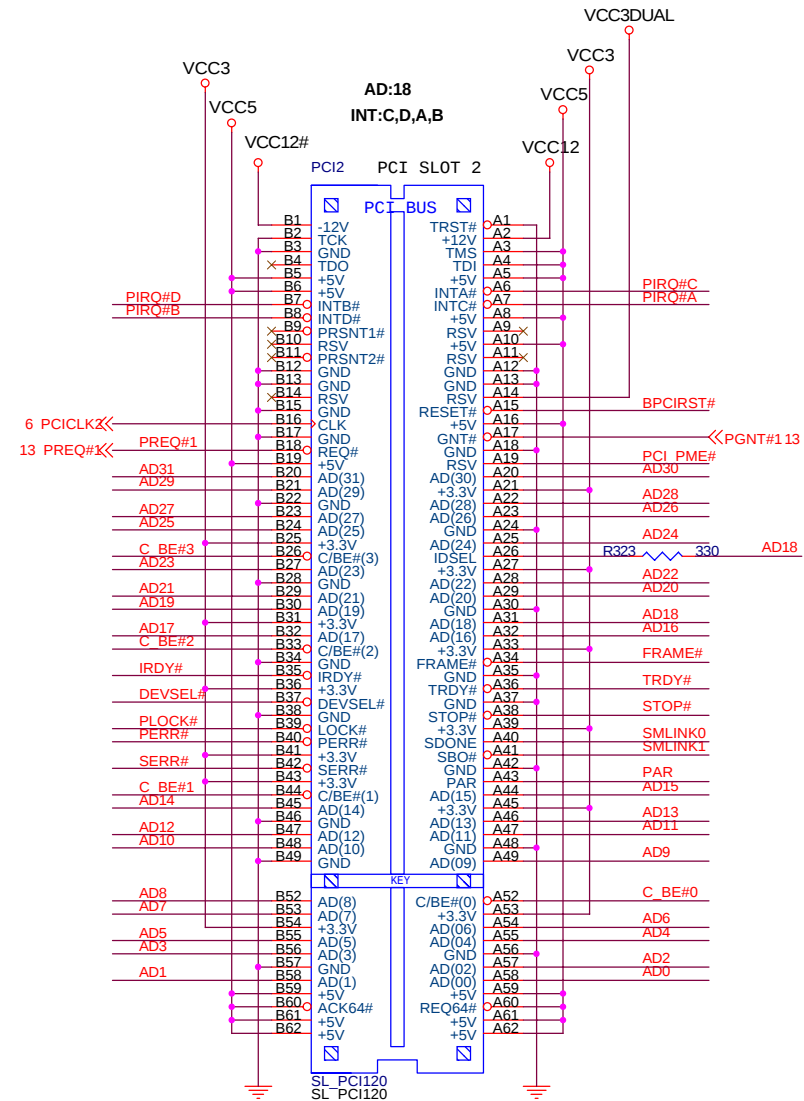
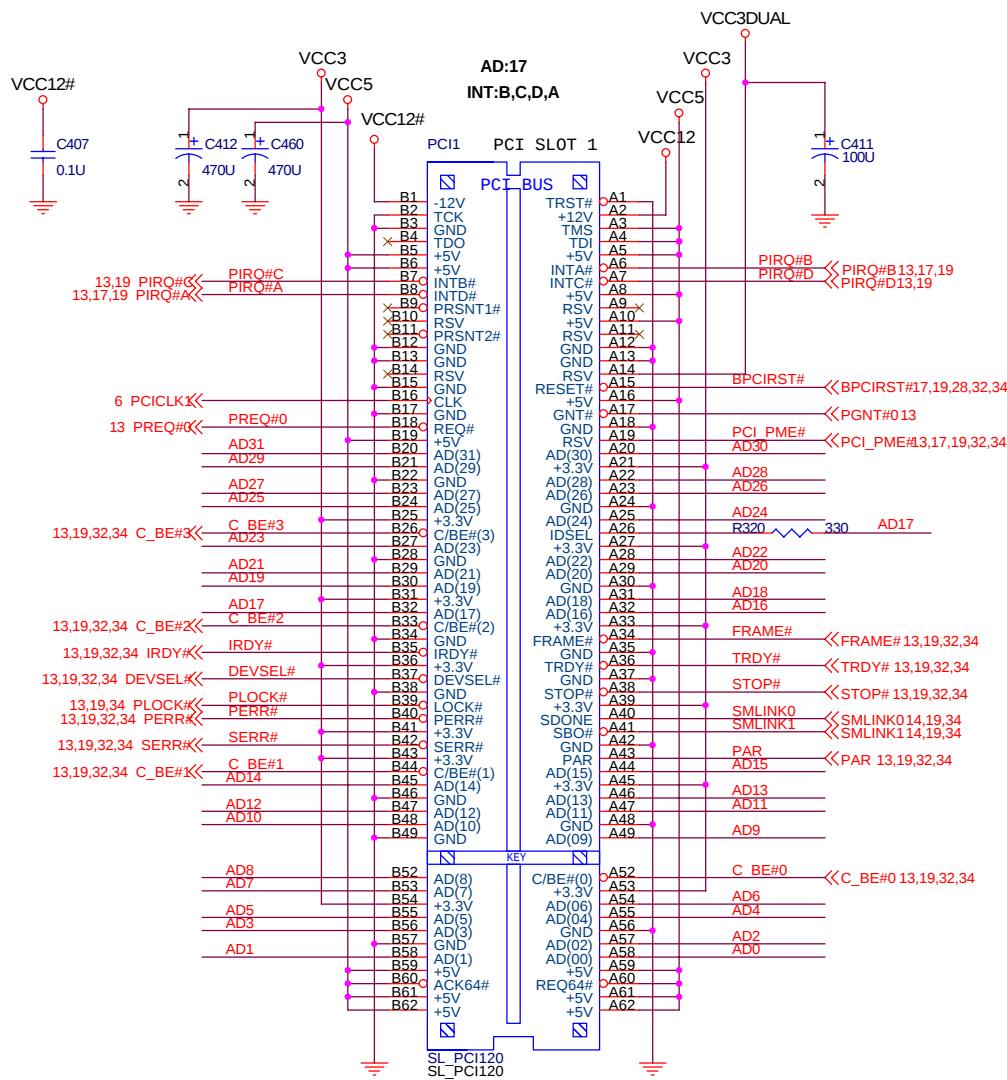
FirmWare Hub (FWH) Socket



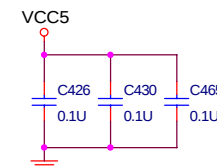
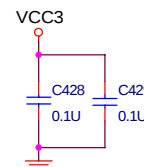
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FIRMWARE HUB (FWH) / IDE(ATA100)			
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8 G_AD[0..31] >> G_AD[0..31]
8 G_SBA[0..7] >> G_SBA[0..7]
8 G_C_BE0 >> G_C_BE0
8 G_C_BE1 >> G_C_BE1
8 G_C_BE2 >> G_C_BE2
8 G_C_BE3 >> G_C_BE3
8 G_FRAME# >> G_FRAME#
8 G_DEVSEL# >> G_DEVSEL#
8 G_IRDY# >> G_IRDY#
8 G_TRDY# >> G_TRDY#
8 G_STOP# >> G_STOP#
8 G_PAR# >> G_PAR#
8 G_REQ# >> G_REQ#
8 G_GNT# >> G_GNT#
8 G_PIPE# >> G_PIPE#
8 G_ADSTB0 >> G_ADSTB0
8 G_ADSTB0# >> G_ADSTB0#
8 G_ADSTB1 >> G_ADSTB1#
8 G_ADSTB1# >> G_ADSTB1#
8 G_SBSTB >> G_SBSTB#
8 G_SBSTB# >> G_SBSTB#
8 G_ST0 >> G_ST0
8 G_ST1 >> G_ST1
8 G_ST2 >> G_ST2
8 G_RBF# >> G_RBF#
8 G_WBF# >> G_WBF#
6 AGPHCLK >> AGPHCLK
13,18,19 PIRQ#A >> PIRQ#A
13,18,19 PIRQ#B >> PIRQ#B
18,19,28,32,34 BPCIRST# >> BPCIRST#
13,18,19,32,34 PCI_PME# >> PCI_PME#

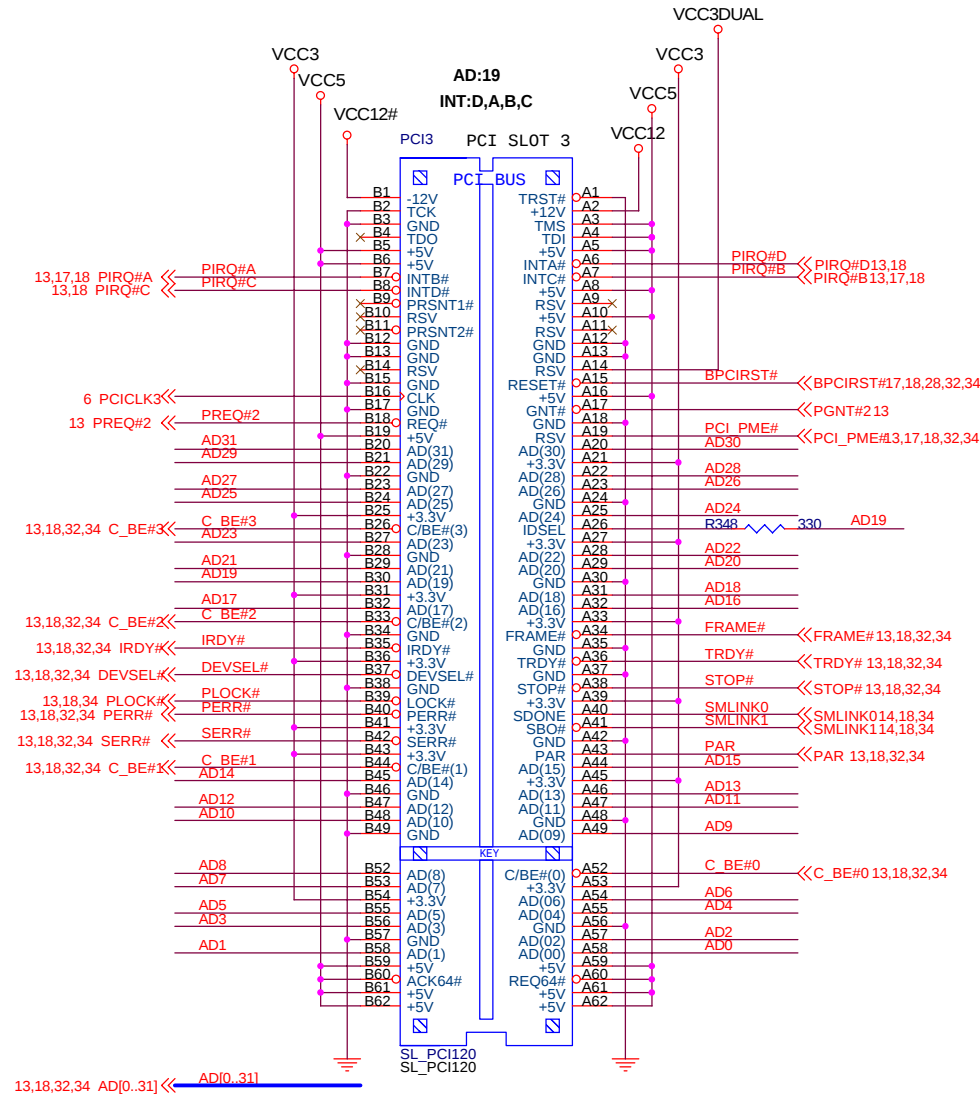


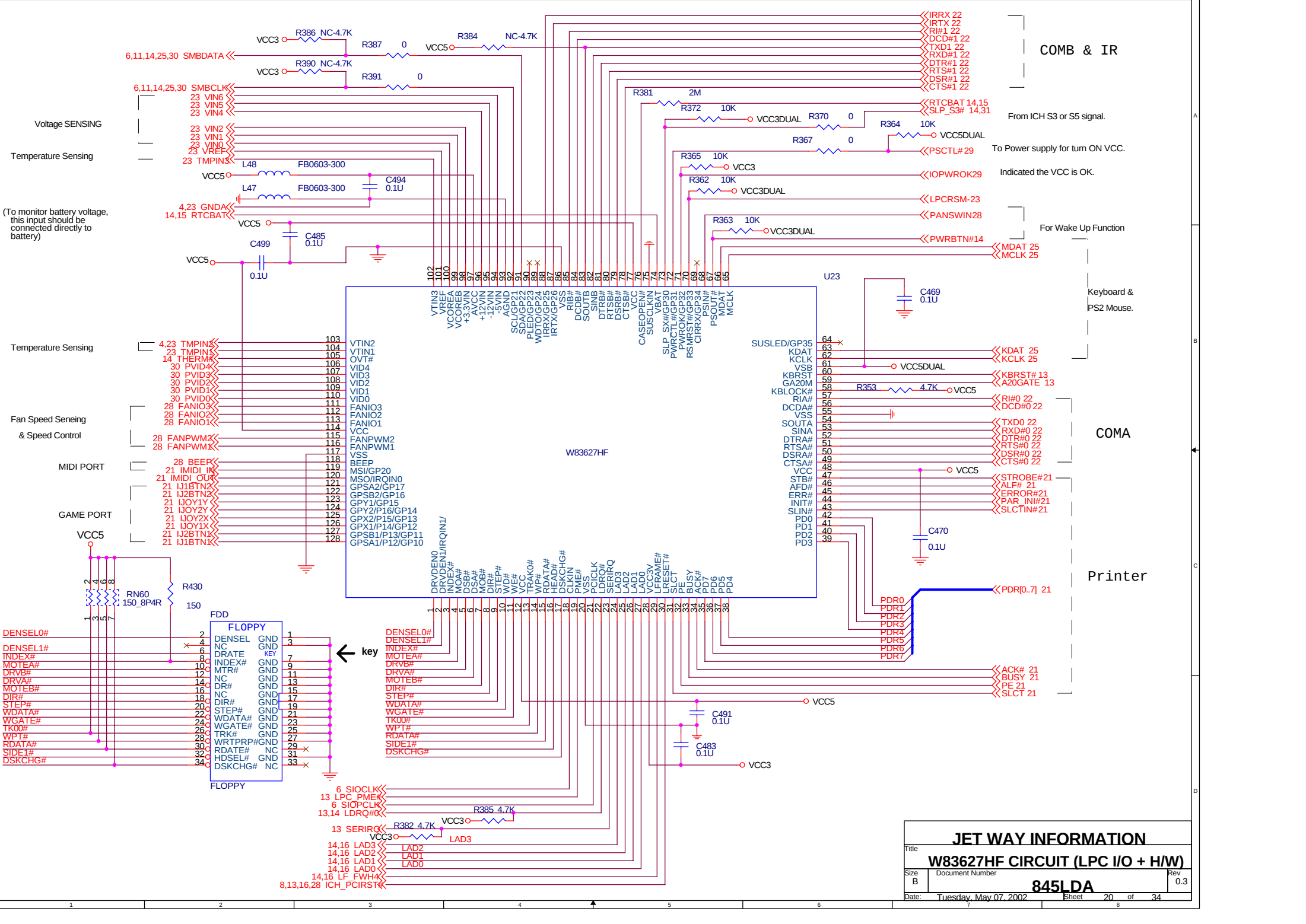


PLACE FROM VCC3 TO VCC5
PLACE NEAR TO VIA



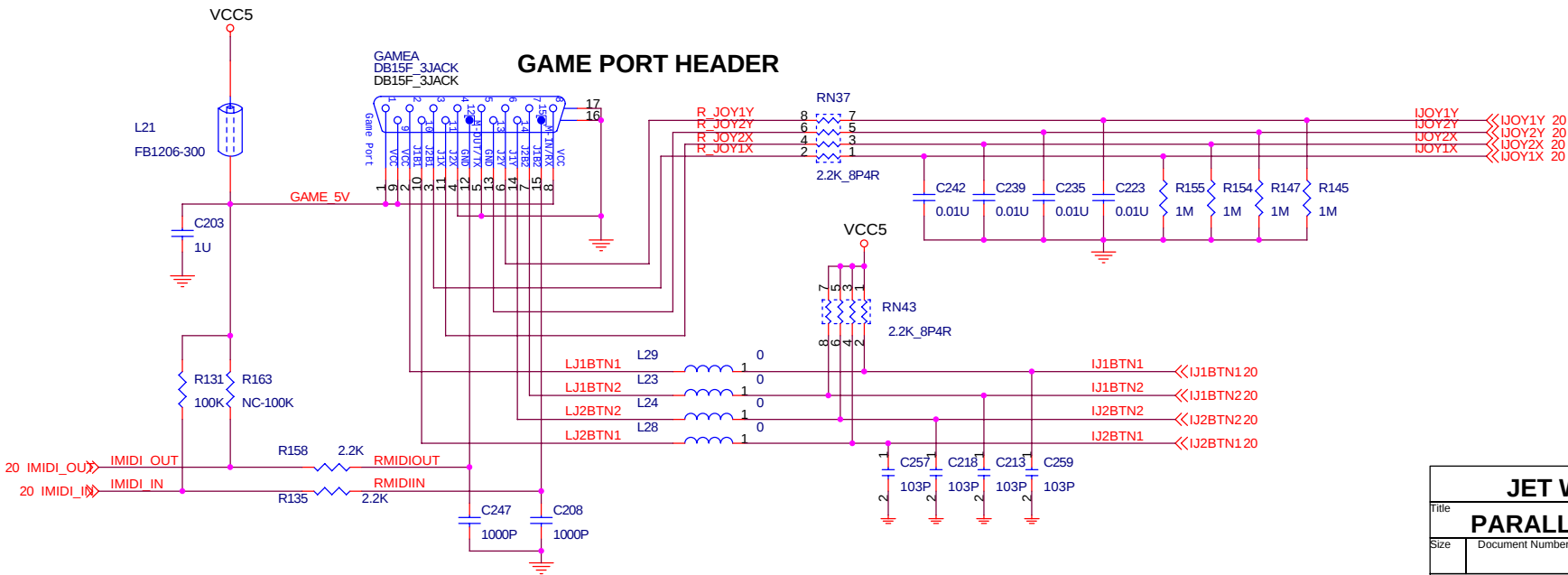
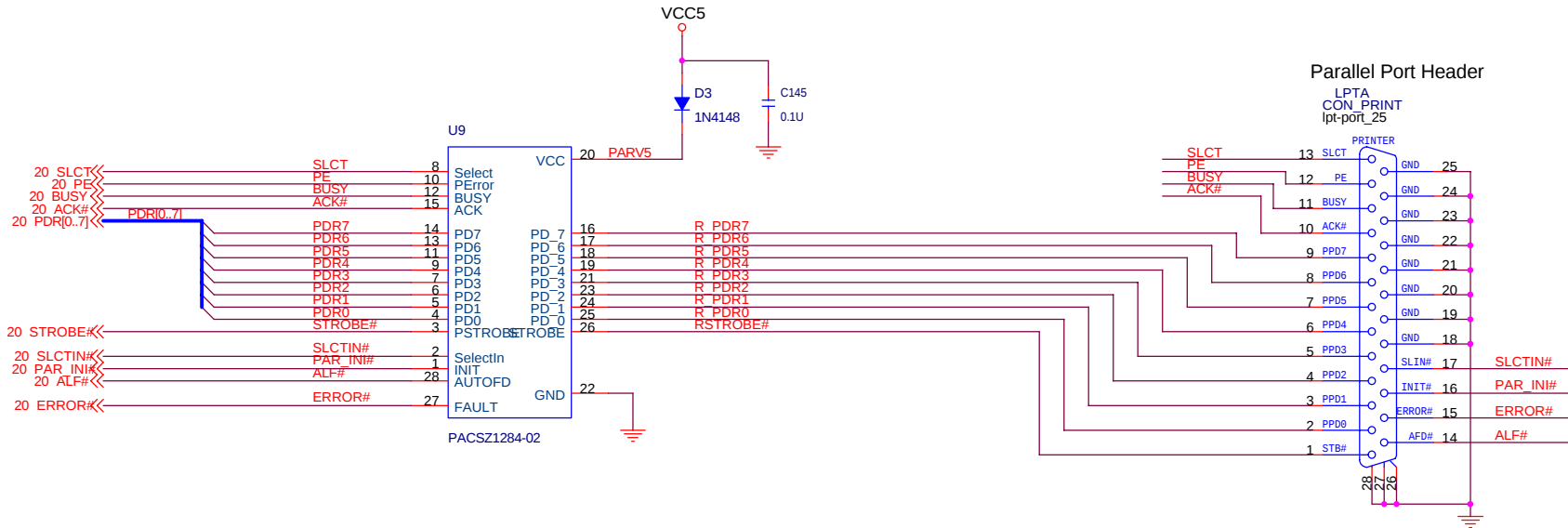
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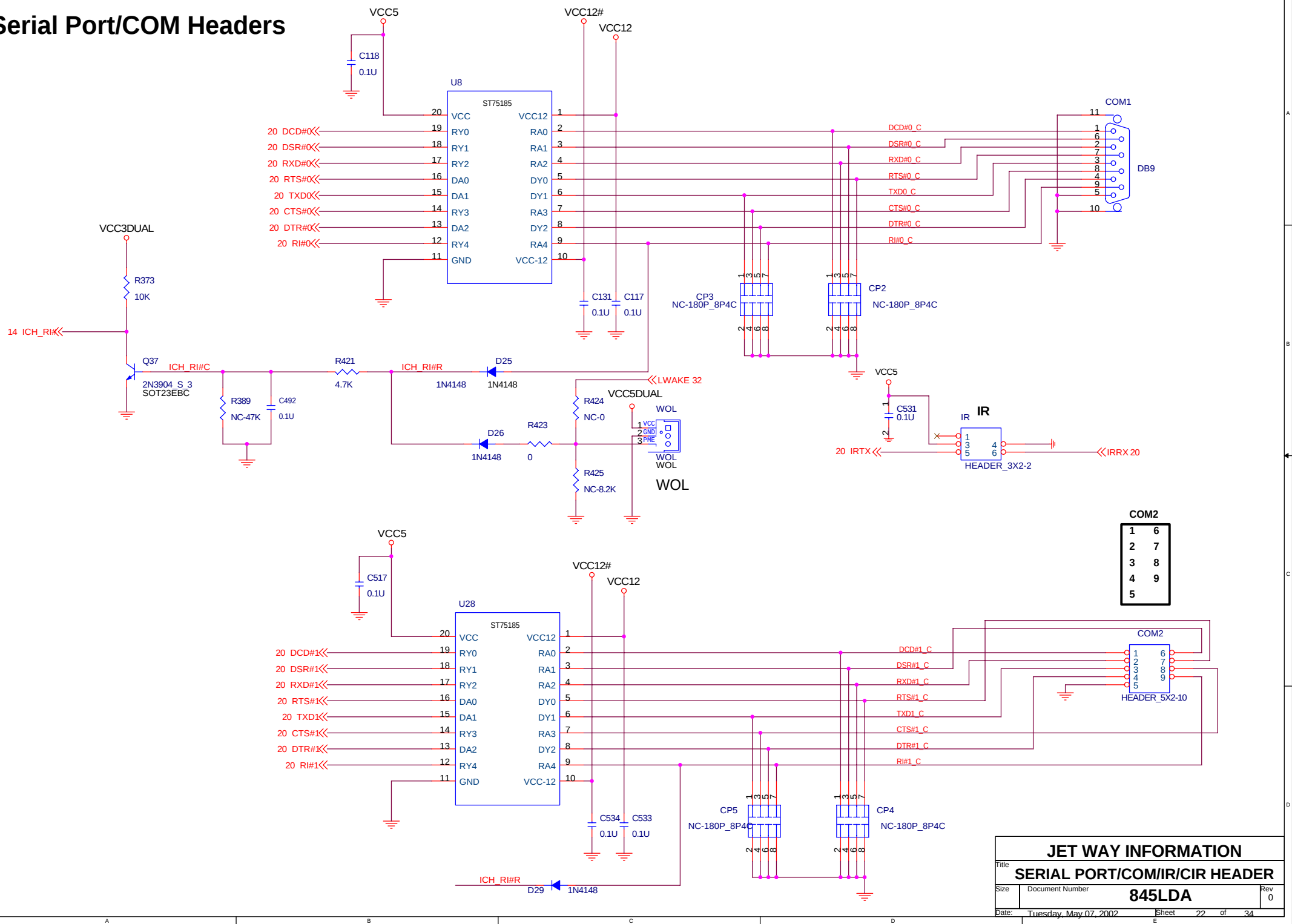


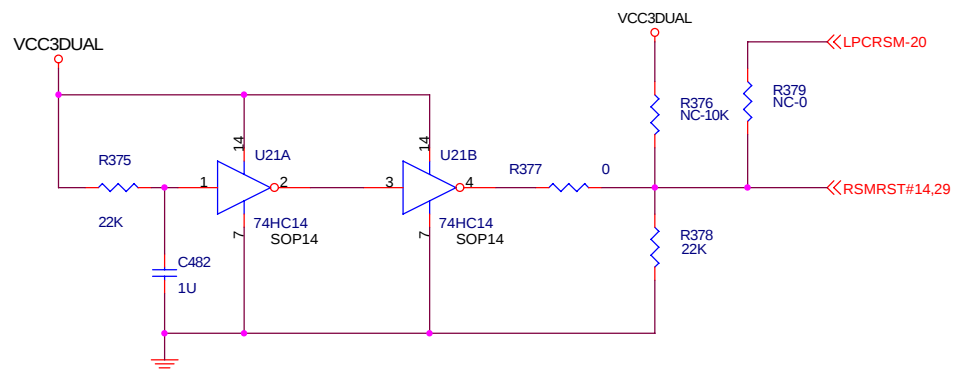
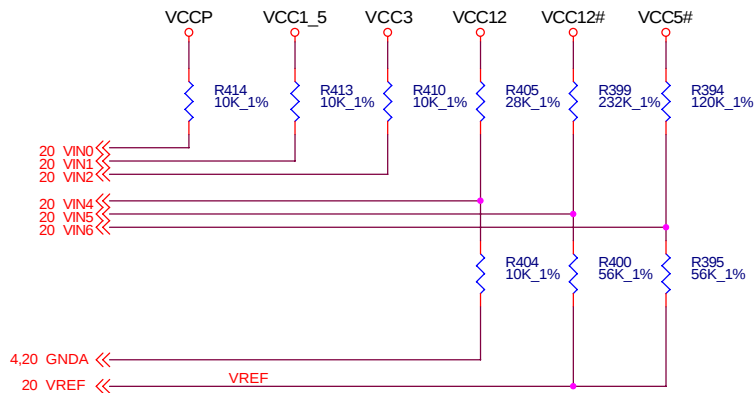
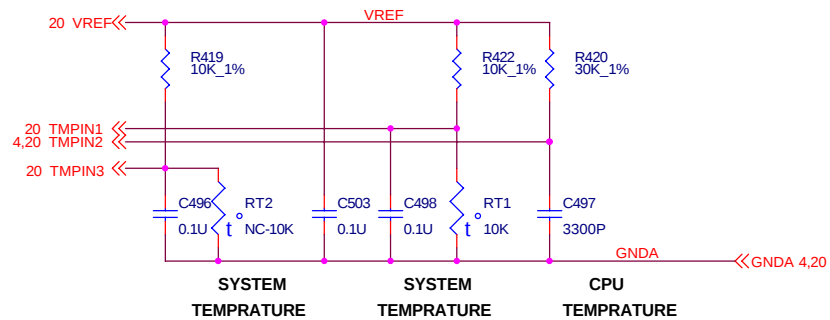
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W83627HF CIRCUIT (LPC I/O + H/W)			
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Parallel Port Header

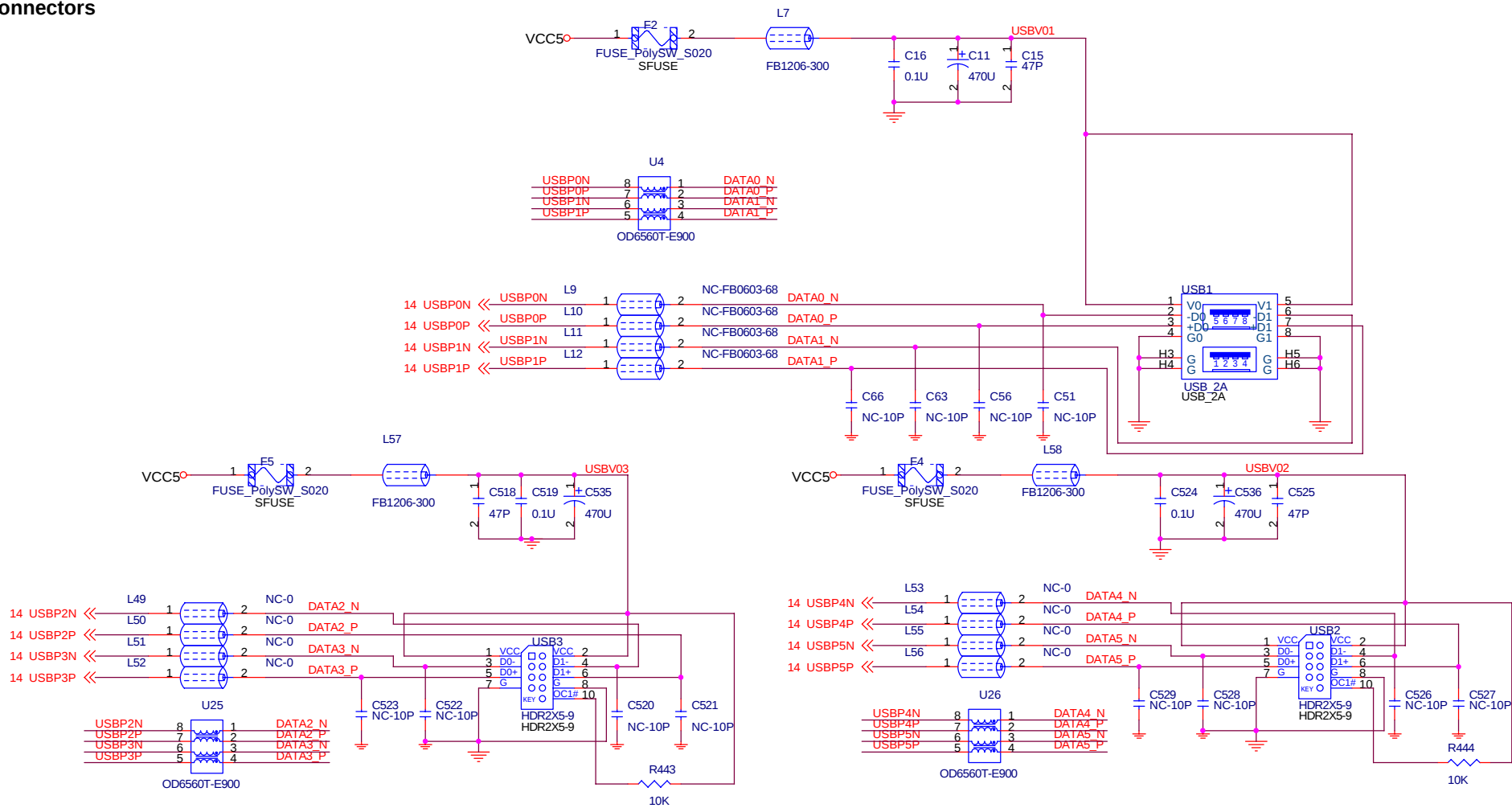


Serial Port/COM Headers

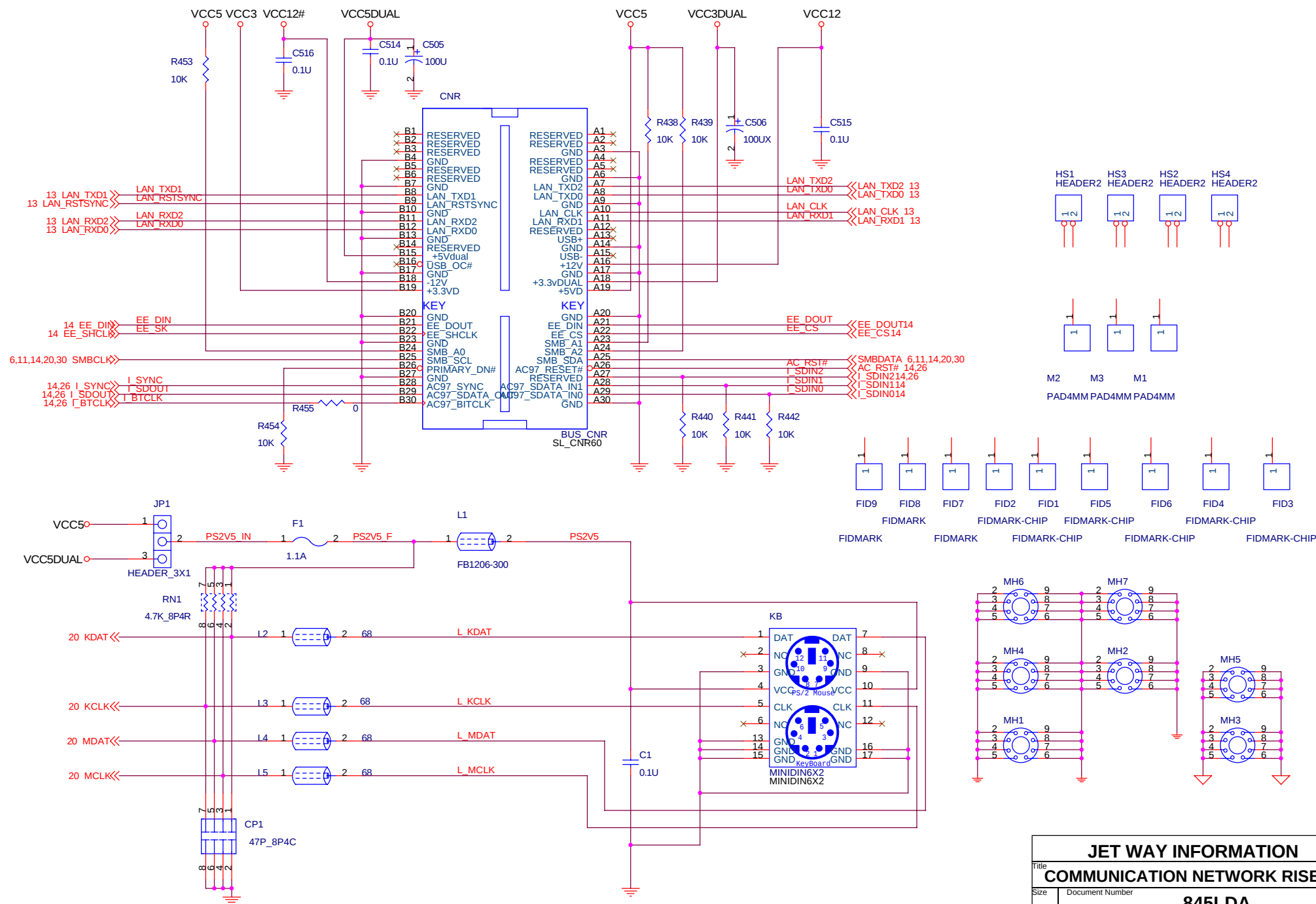


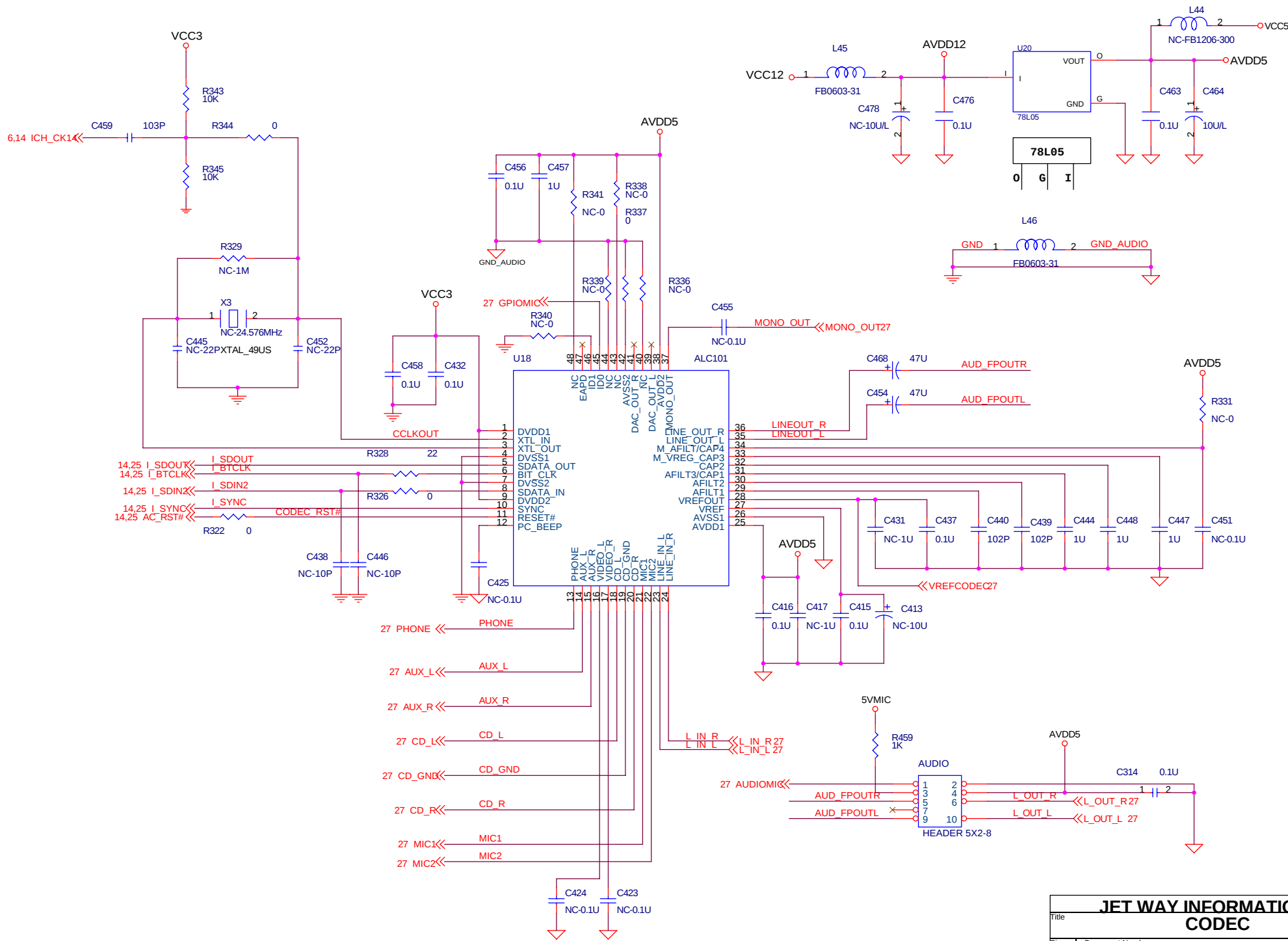


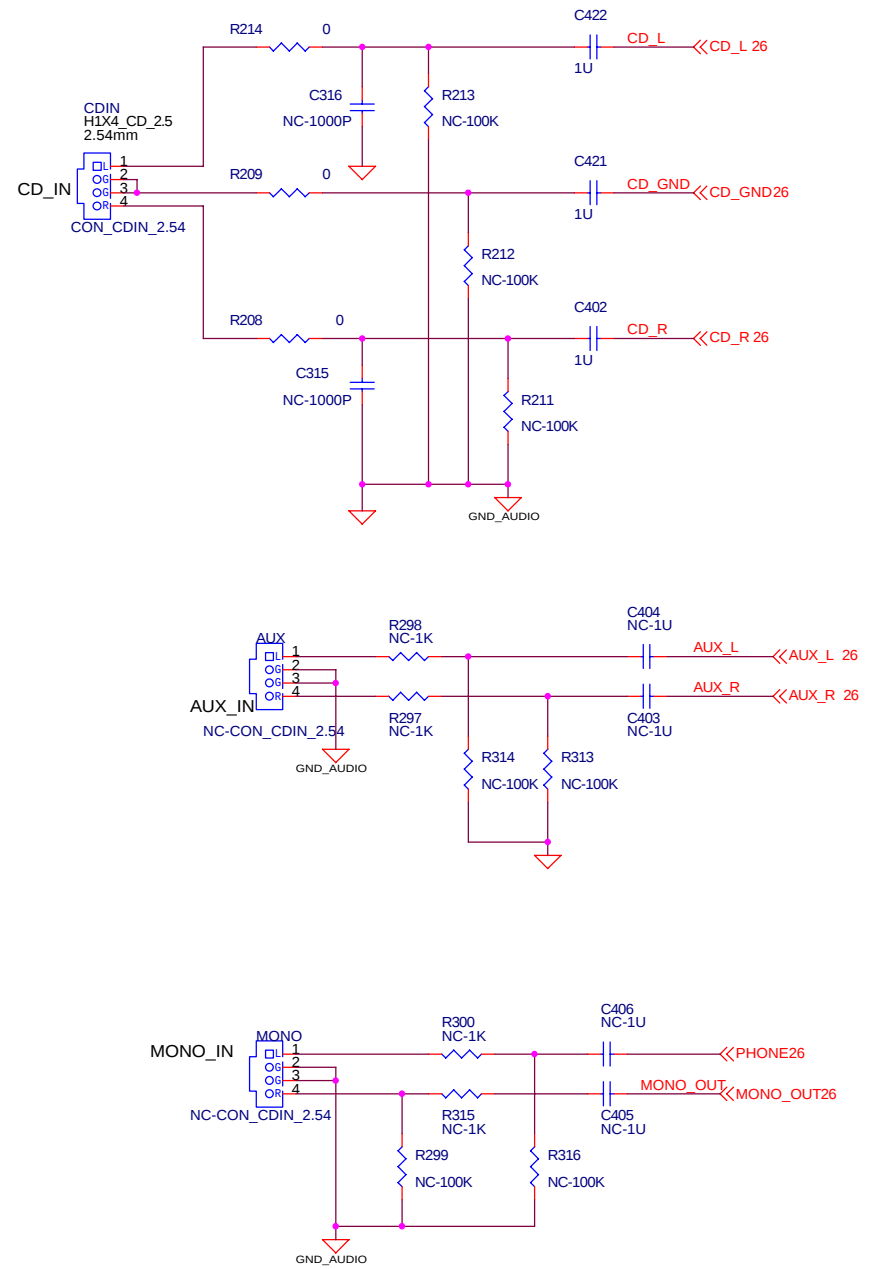
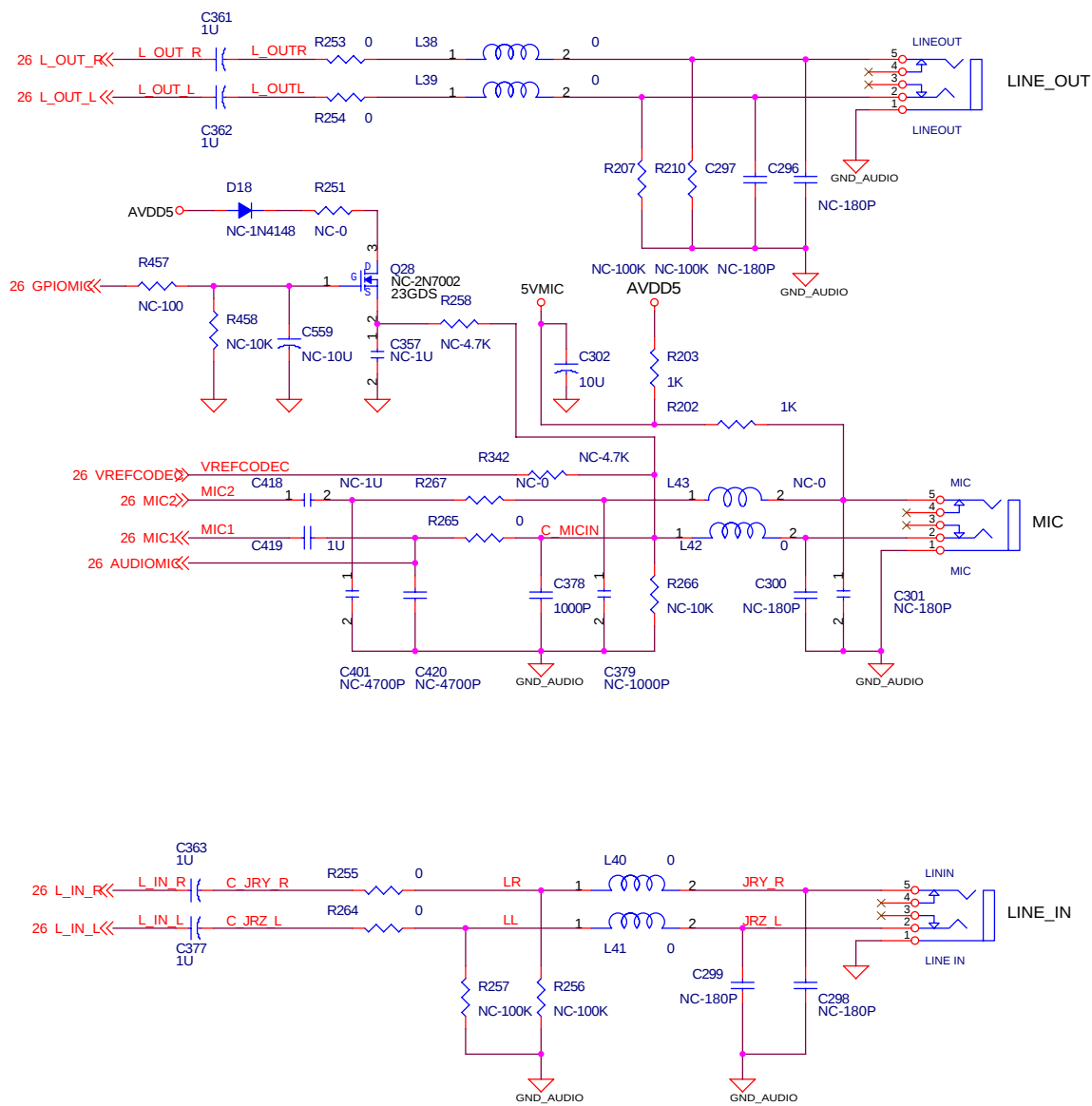
USB Connectors



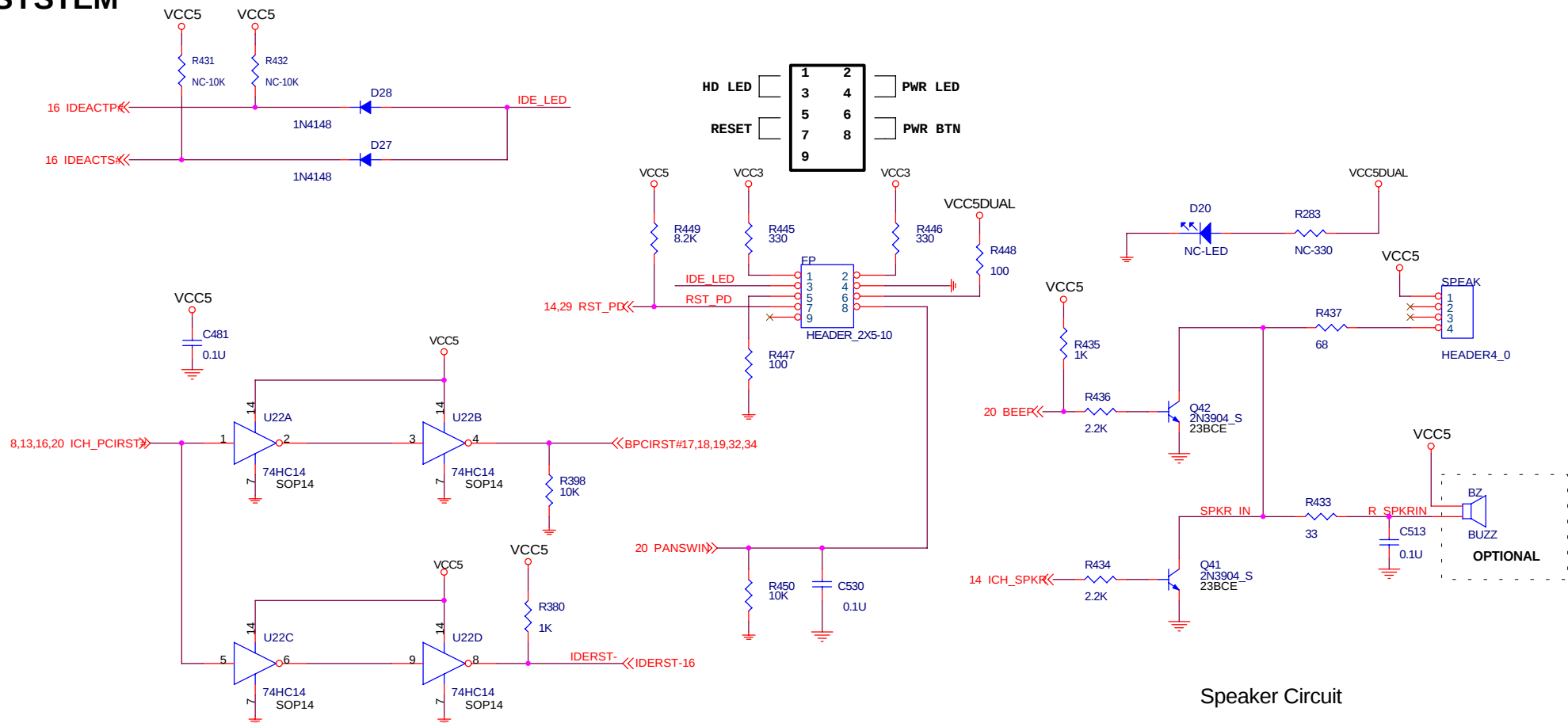
KB/MS CONNECT & CNR RISER



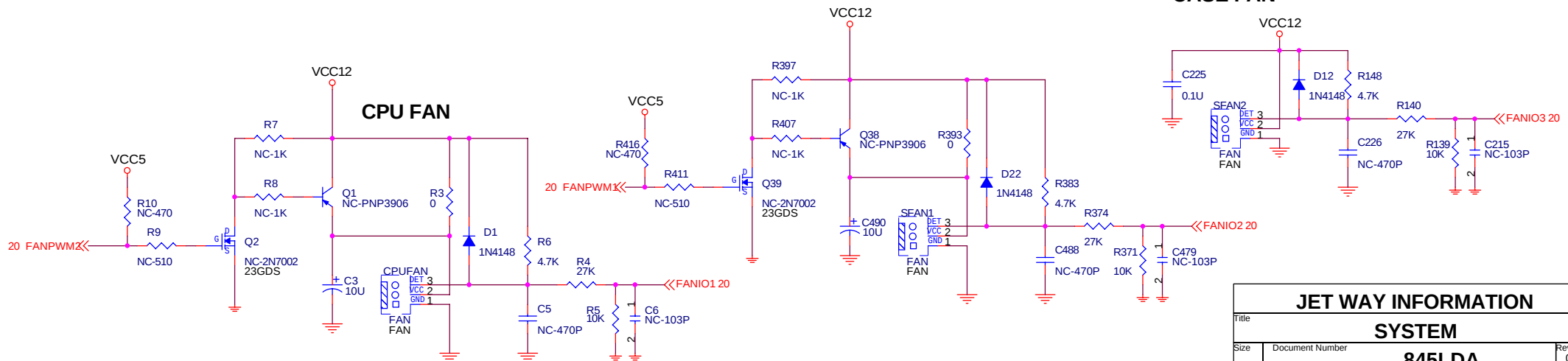




SYSTEM



SYSTEM FAN



JET WAY INFORMATION

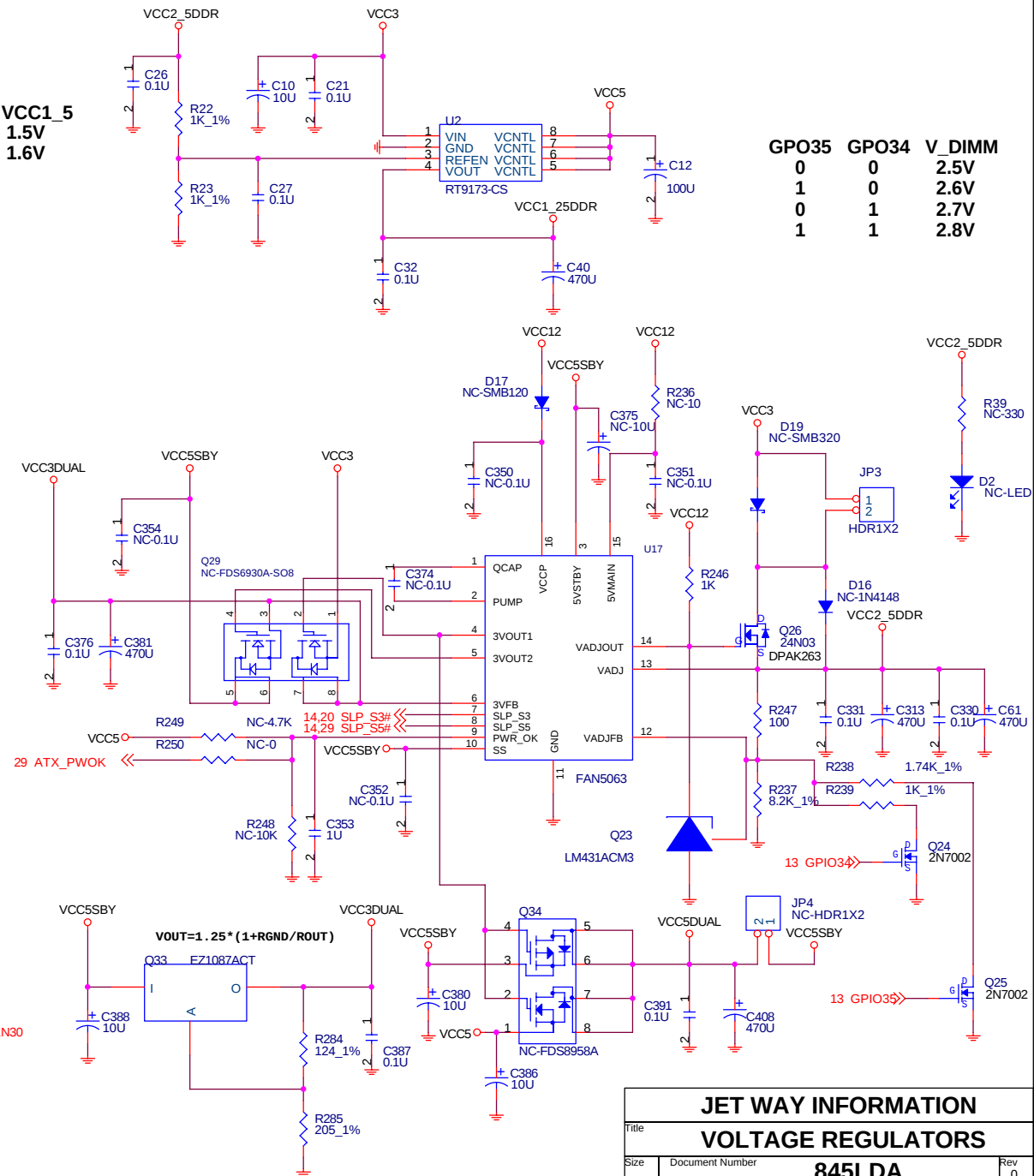
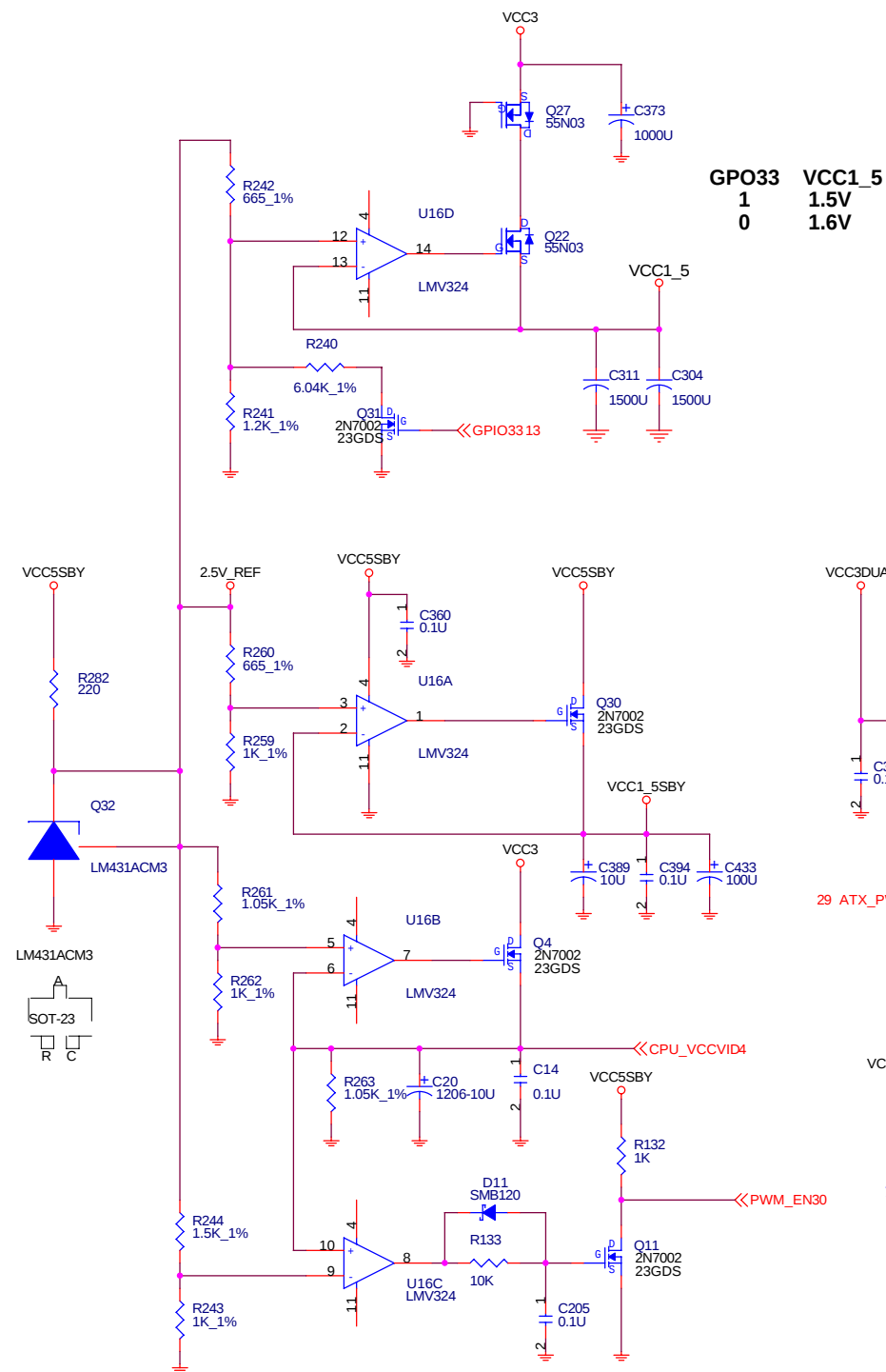
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Voltage Regulators



GPO35	GPO34	V_DIMM
0	0	2.5V
1	0	2.6V
0	1	2.7V
1	1	2.8V

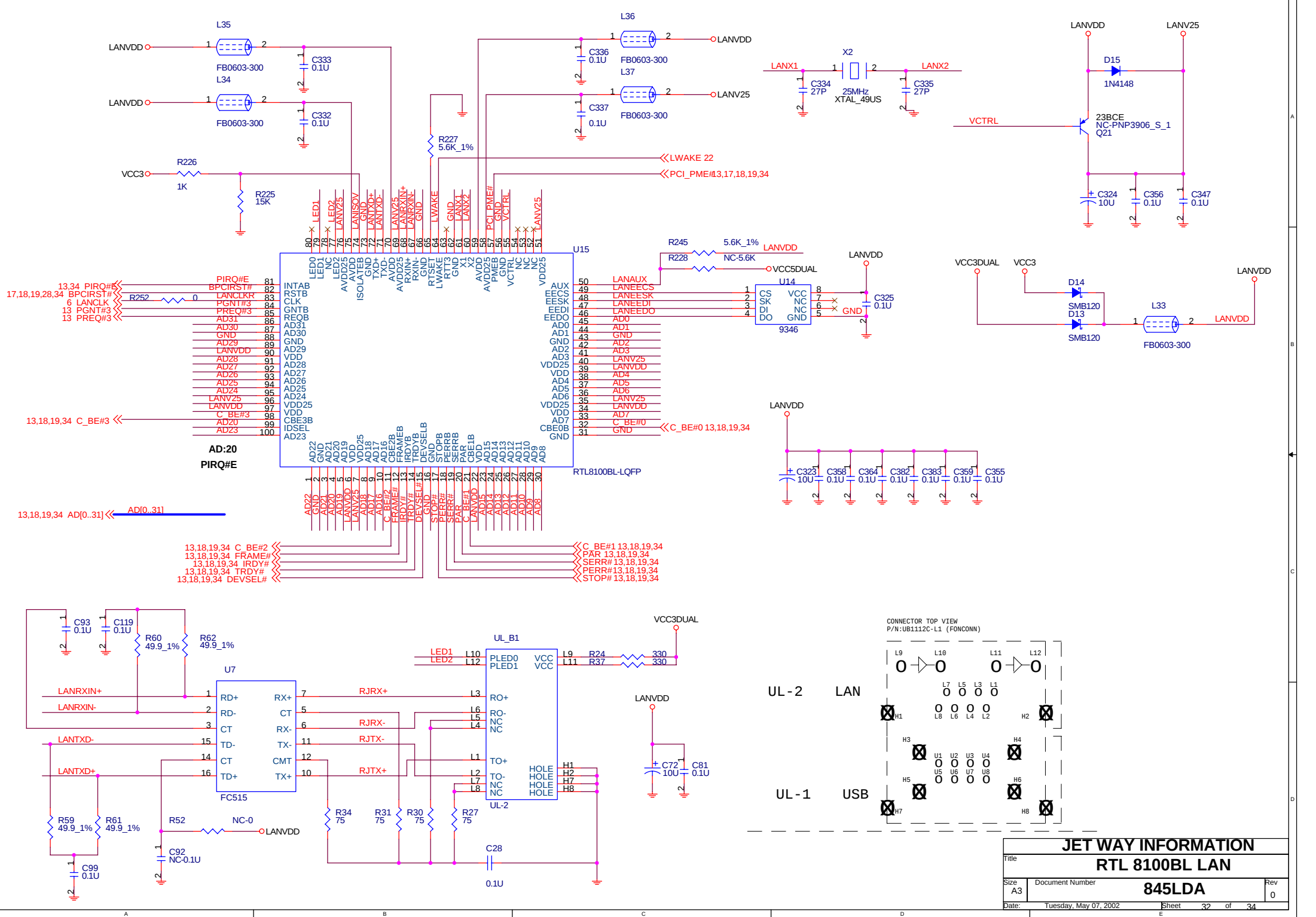
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VOLTAGE REGULATORS

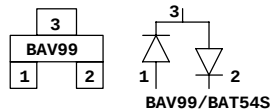
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POPULATE WHEN NON LTI CONNECTOR

