

JET WAY INFORMATION

MODEL : J - 694AS (SOCKET 370+VT82C694X+VT82C686B+AGP 4X+1 AMR)

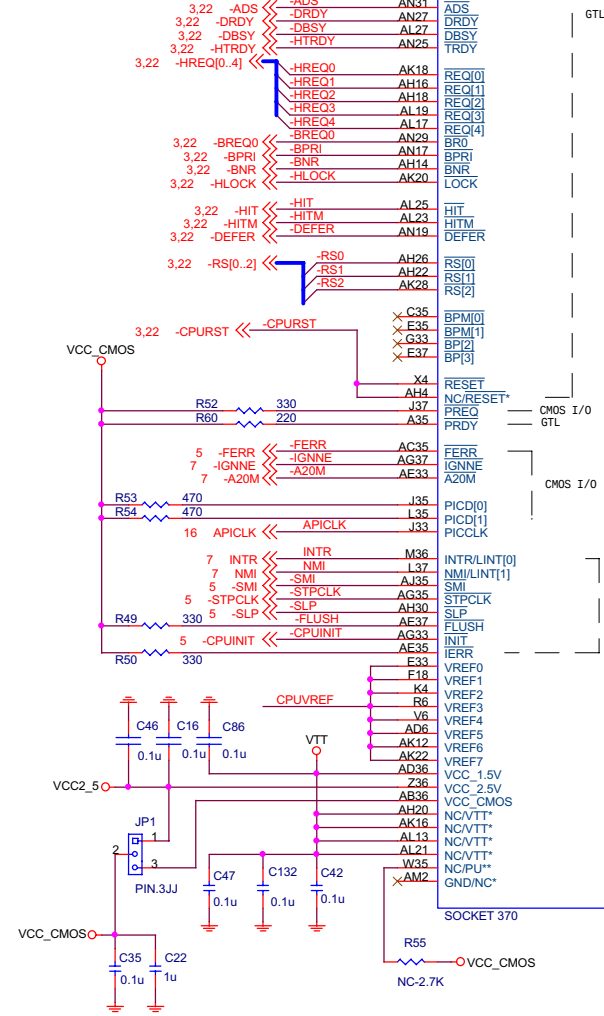
TITLE	SHEET
COVER SHEET	1
SOCKET 370 PROCESSOR	2
NORTH BRIDGE (VT82C694X)	3, 4
SOUTH BRIDGE (VT82C686B)	5, 6
USB2, 3 & FREQUENCY RATIO	7
SDRAM	8, 9
PCI SLOTS	10, 11, 12
AGP SLOT	13
ISA PULL UP RESISTANCE	14
IDE & PANEL	15
CLOCK SYNTHESIZER	16
BYPASS CAPACITORS	17
DC-DC CONVERTER	18
PRINTER / COM PORT	17
AUDIO CODEC & AUDIO PORT & JOSTICK PORT	20
AMR SLOT	21
ATX POWER CONNECTOR / VTT TERMINATOR / VCC3	22

LINK
2.SCH
3.SCH
4.SCH
5.SCH
6.SCH
7.SCH
8.SCH
9.SCH
10.SCH
11.SCH
12.SCH
13.SCH
14.SCH
15.SCH
16.SCH
17.SCH
18.SCH
19.SCH
20.SCH
21.SCH
22.SCH

JETWAY INFORMATION			
Title COVER SHEET			
Size	Document Number J-694AS		Rev 0.1
Date: Thursday, November 23, 2000	Sheet	1	of 22

SOCKET 370

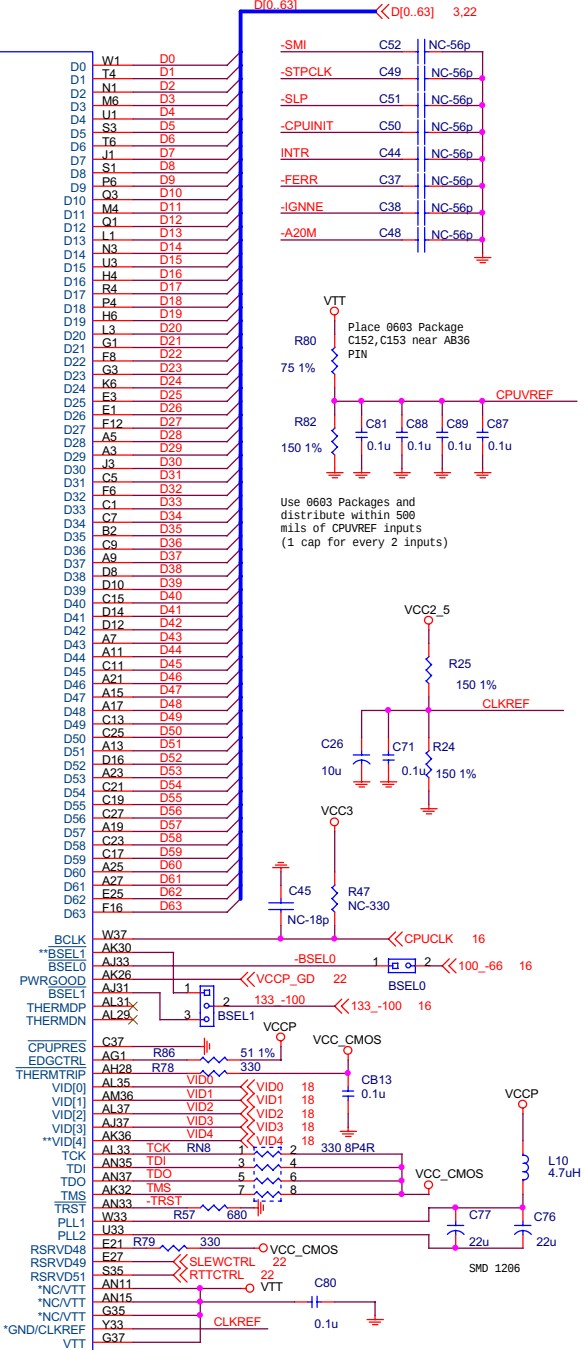
BSEL0 BSEL1	Jumper function
Short	Bus freq. auto detected by CPU.
Open	Test 66/100 MHz CPU run 100/133 MHz.
Short	Test 100 MHz CPU run 133 MHz.
Open	



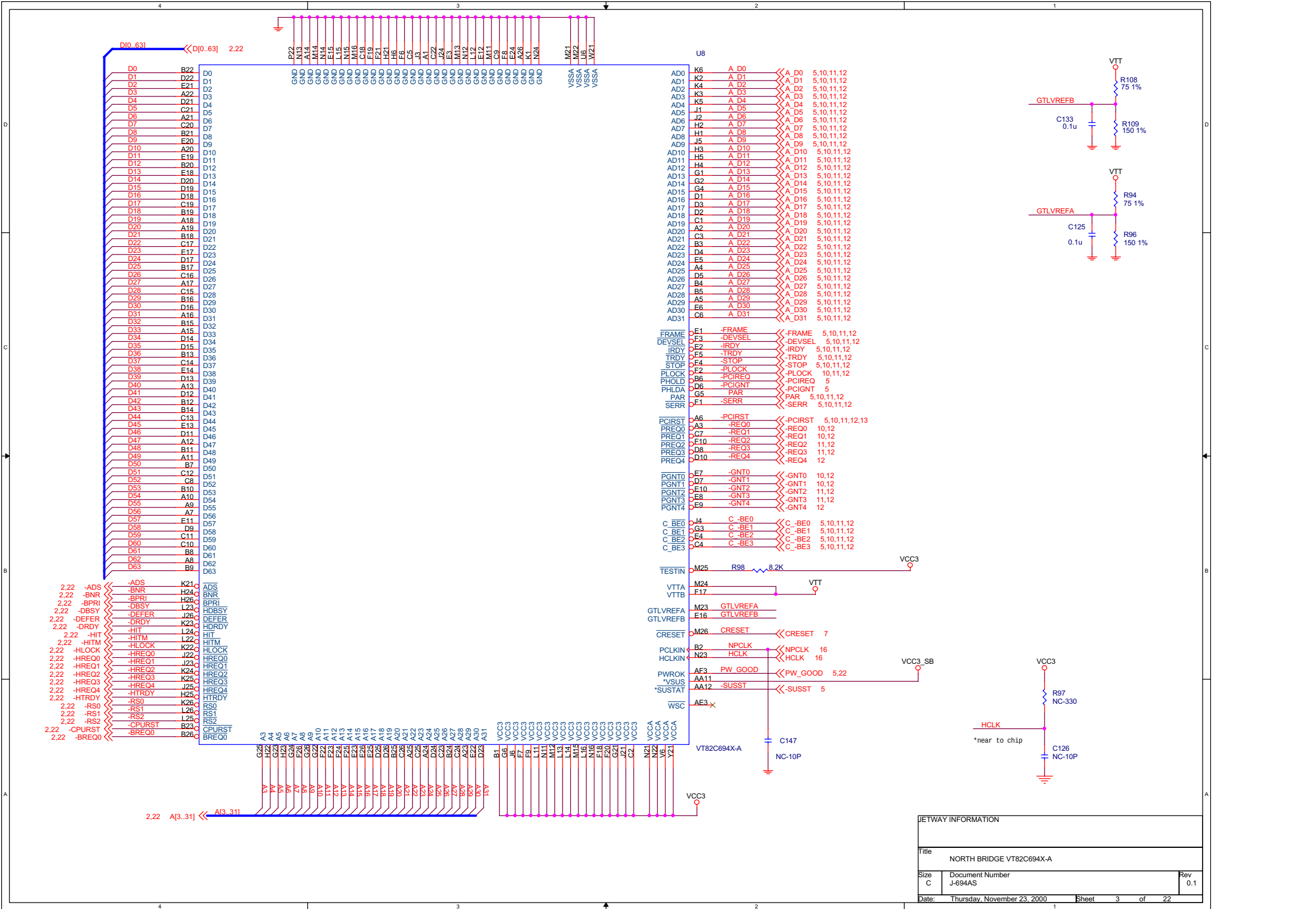
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A37	D34	AA5	F2	AD36
AB32	E7	AA37	F4	
AC33	E11	AB34	F14	
AD2	E15	AD32	F22	
AD34	F20	AE5	F30	
AF32	F24	AF2	F34	
AF36	F28	AF34	H36	
AG5	F32	AH24	H36	
AH34	F36	AH32	J5	
AJ3	H2	AJ35	K32	
AJ7	H34	AJ39	K34	
AJ11	K36	AJ13	M32	
AJ15	L5	AJ17	N5	
AJ19	M2	AJ21	P2	
AJ23	M34	AJ25	P34	
AJ27	P32	AJ29	R32	
AK4	P36	AK2	R36	
AL1	Q5	AK34	S5	
AL3	R34	AM4	T2	
AM6	T32	AM8	T34	
AM19	T36	AM12	V32	
AM14	U5	AM16	V36	
AM18	V2	AM20	M5	
AM22	V34	AM24	X34	
AM26	X32	AM28	Y35	
AM30	X36	AM32	Z32	
AM34	Y5	B6		
AN3	Y37	B10		
B4	Z2	B14		
B8	Z34	B18		
B12		B22		
B16		B26		
B20		B30		
B24		B34		
B28		C3		
B32		D6		
D2		D20		
D4		D24		
D18		D28		
D22		D32		
D26		D36		
E5		E9		
E13		E17		

*:Intel Old/New
**:cyrix

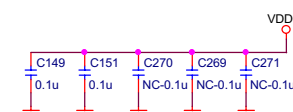
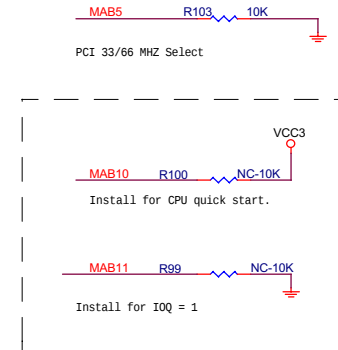
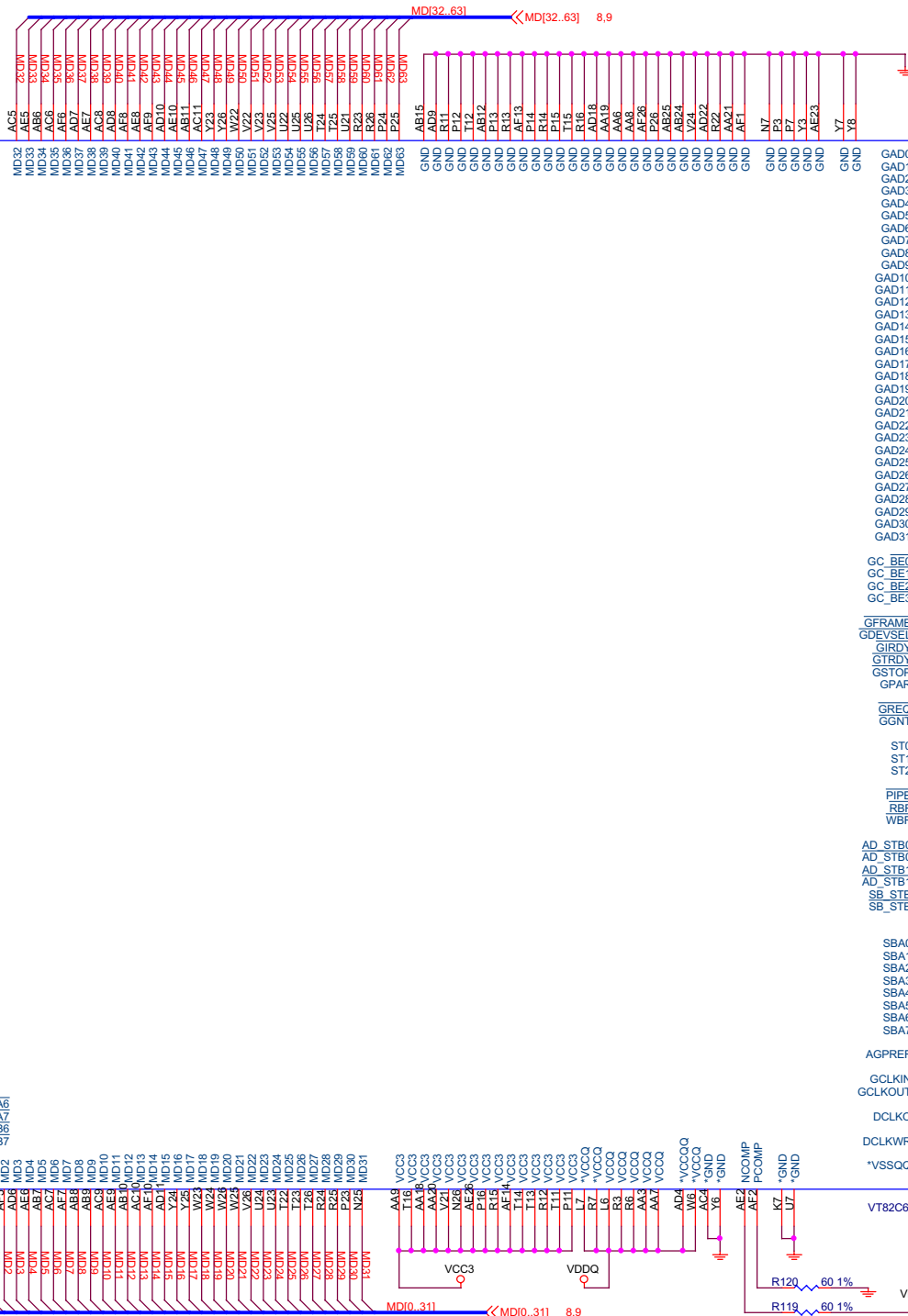
BSEL0, BSEL1:
Cyrix:Input
Intel:Output



JET WAY INFORMATION		
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Size	Document Number	Rev
B	J-694AS	0.1
Date:	Thursday, November 23, 2000	Sheet 2 of 22

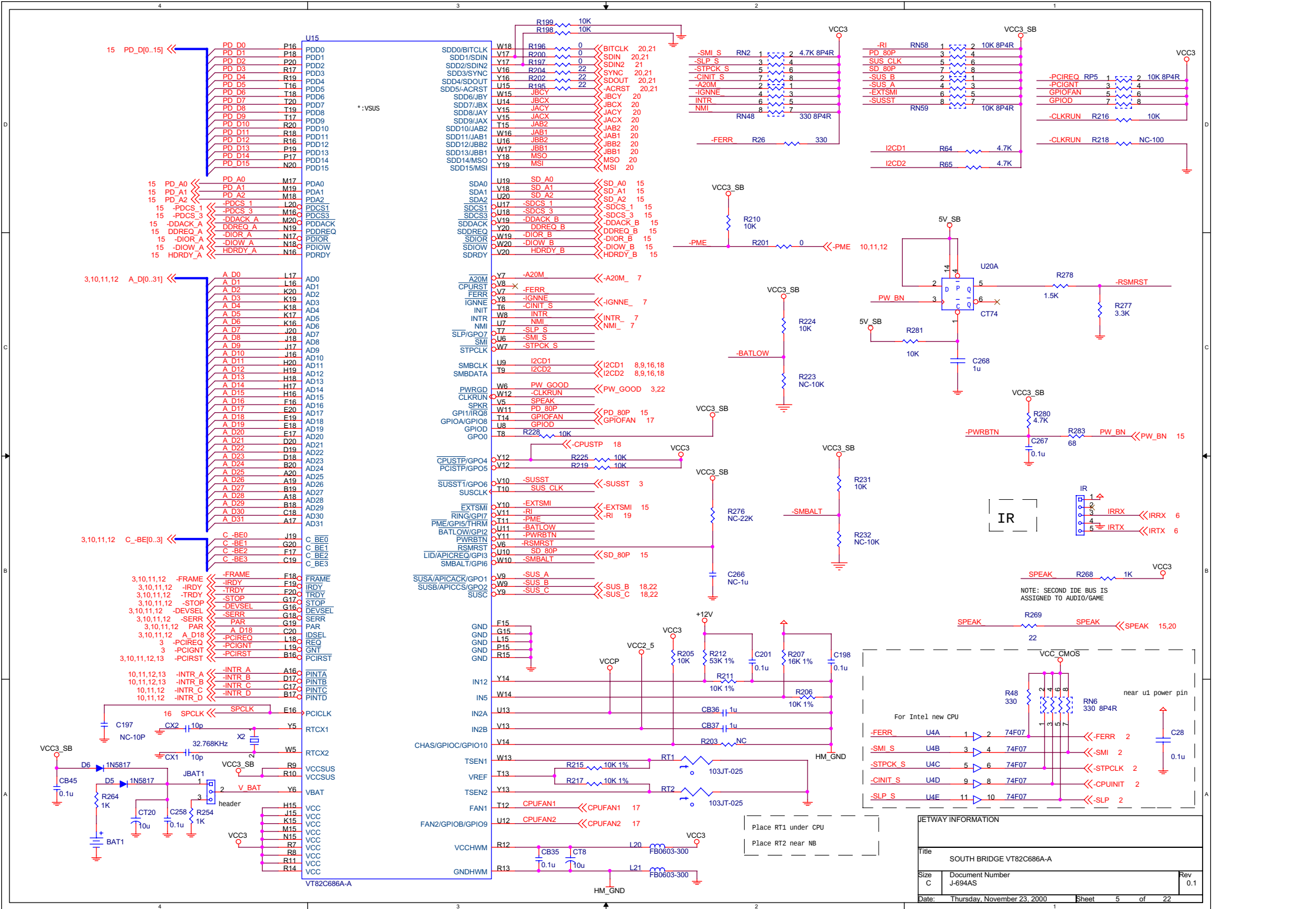


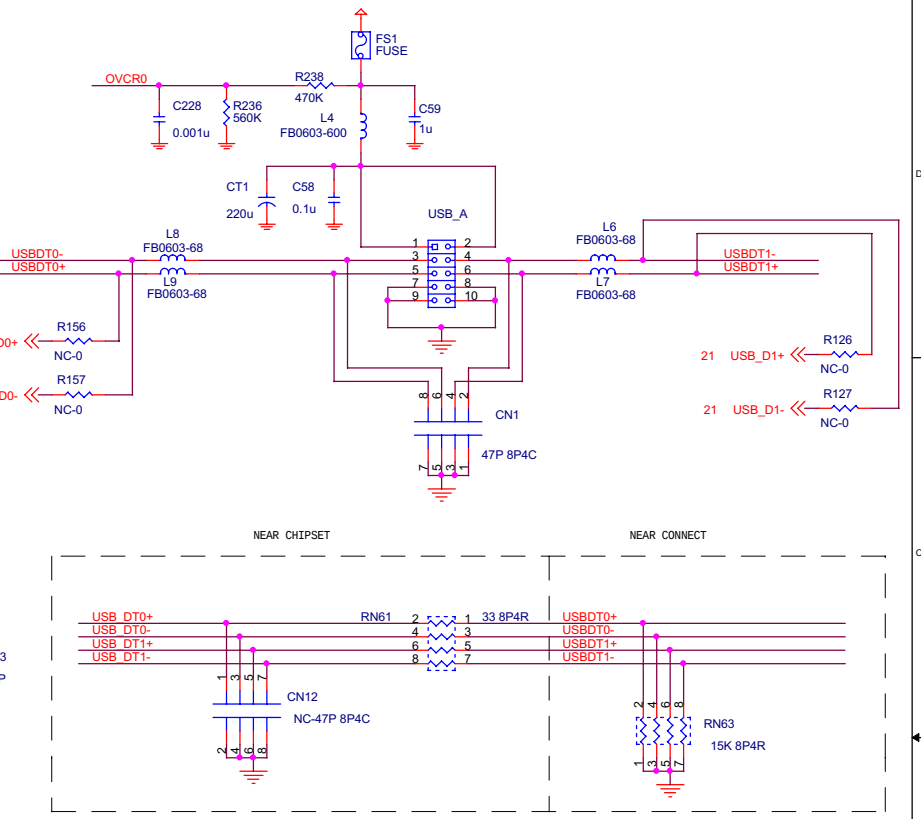
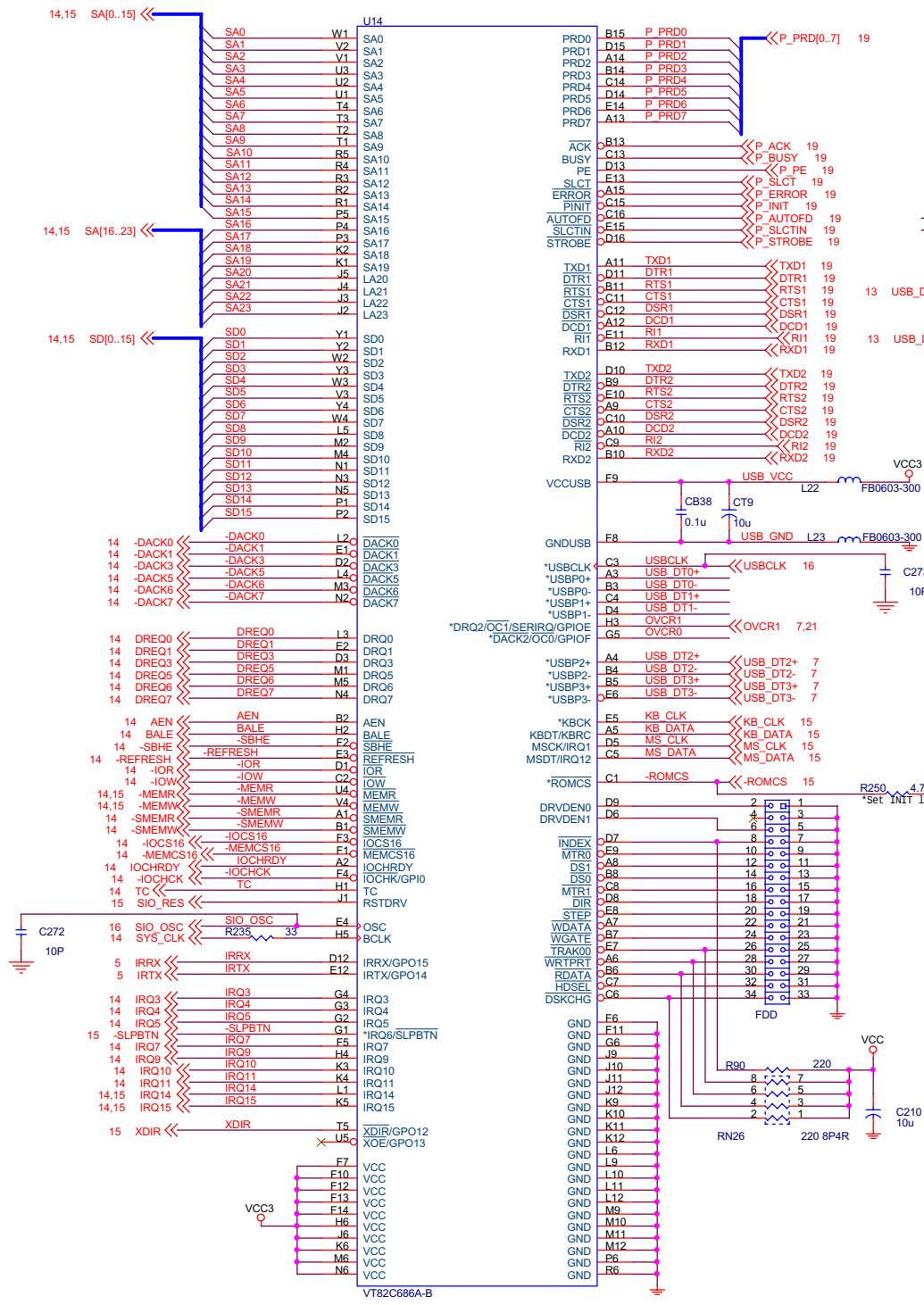
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8 MAA1 MAA1 AB16 MAA1
8 MAA2 MAA2 AE17 MAA2
8 MAA3 MAA3 AC17 MAA3
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8 MAA5 MAA5 AE19 MAA5
8 MAA6 MAA6 AF19 MAA6
8 MAA7 MAA7 AC18 MAA7
8 MAA8 MAA8 AE20 MAA8
8 MAA9 MAA9 AD20 MAA9
8 MAA10 MAA10 AE21 MAA10
8 MAA11 MAA11 AC21 MAA11
8 MAA12 MAA12 AE22 MAA12
8 MAA13 MAA13 AB22 MAA13
8 MAA14 MAA14
9 MAB0 MAB0 AD16 MAB0
9 MAB1 MAB1 AC16 MAB1
9 MAB2 MAB2 AD17 MAB2
9 MAB3 MAB3 AB17 MAB3
9 MAB4 MAB4 AE18 MAB4
9 MAB5 MAB5 AD19 MAB5
9 MAB6 MAB6 AB18 MAB6
9 MAB7 MAB7 AB19 MAB7
9 MAB8 MAB8 AE20 MAB8
9 MAB9 MAB9 AC20 MAB9
9 MAB10 MAB10 AB20 MAB10
9 MAB11 MAB11 AE21 MAB11
9 MAB12 MAB12 AD21 MAB12
9 MAB13 MAB13 AE22 MAB13
9 MAB14 MAB14
8 -CSA0 -CSA0 AB14 CSA0
8 -CSA1 -CSA1 AE15 CSA1
8 -CSA2 -CSA2 AC15 CSA2
8 -CSA3 -CSA3 AD15 CSA3
8 -CSA4 -CSA4 AE16 CSA4
8 -CSA5 -CSA5
8 -CSB0 -CSB0 AD24 CSB0
8 -CSB1 -CSB1 AE25 CSB1
8 -CSB2 -CSB2 AC24 CSB2
8 -CSB3 -CSB3 AD26 CSB3
8 -CSB4 -CSB4 AE27 CSB4
8 -CSB5 -CSB5 AB23 CSB5
8.9 -DQMA0 -DQMA0 AD13 DQMA0
8 -DQMA1 -DQMA1 AC13 DQMA1
8.9 -DQMA2 -DQMA2 AC25 DQMA2
8.9 -DQMA3 -DQMA3 AB26 DQMA3
8.9 -DQMA4 -DQMA4 AE14 DQMA4
8 -DQMA5 -DQMA5 AC14 DQMA5
8.9 -DQMA6 -DQMA6 AA24 DQMA6
8.9 -DQMA7 -DQMA7 AD24 DQMA7
8 -DQMB1 -DQMB1 AE13 DQMB1
8 -DQMB5 -DQMB5 AD14 DQMB5
9 -SRASA -SRASA AF16 SRASA
9 -SRASB -SRASB AA17 SRASB
8 -SCASA -SCASA AF12 SCASA
9 -SCASB -SCASB AB13 SCASB
8 -SWEA -SWEA AE12 WEA
9 -SWEB -SWEB AC12 WEB
8.9 MPD0 MPD0 AE11 MECC0
8.9 MPD1 MPD1 AA10 MECC1
8.9 MPD2 MPD2 AA23 MECC2
8.9 MPD3 MPD3 AA26 MECC3
8.9 MPD4 MPD4 AF11 MECC4
8.9 MPD5 MPD5 AD12 MECC5
8.9 MPD6 MPD6 AA25 MECC6
8.9 MPD7 MPD7 Y22 MECC7
8 CKE0 CKE0 AC22 CKE0
8 CKE1 CKE1 AF23 CKE1
8 CKE2 CKE2 AE24 CKE2/CSA6
8 CKE3 CKE3 AD23 CKE3/CSA7
9 CKE4 CKE4 AC23 CKE4/CSB6
9 CKE5 CKE5 AF24 CKE5/CSB7



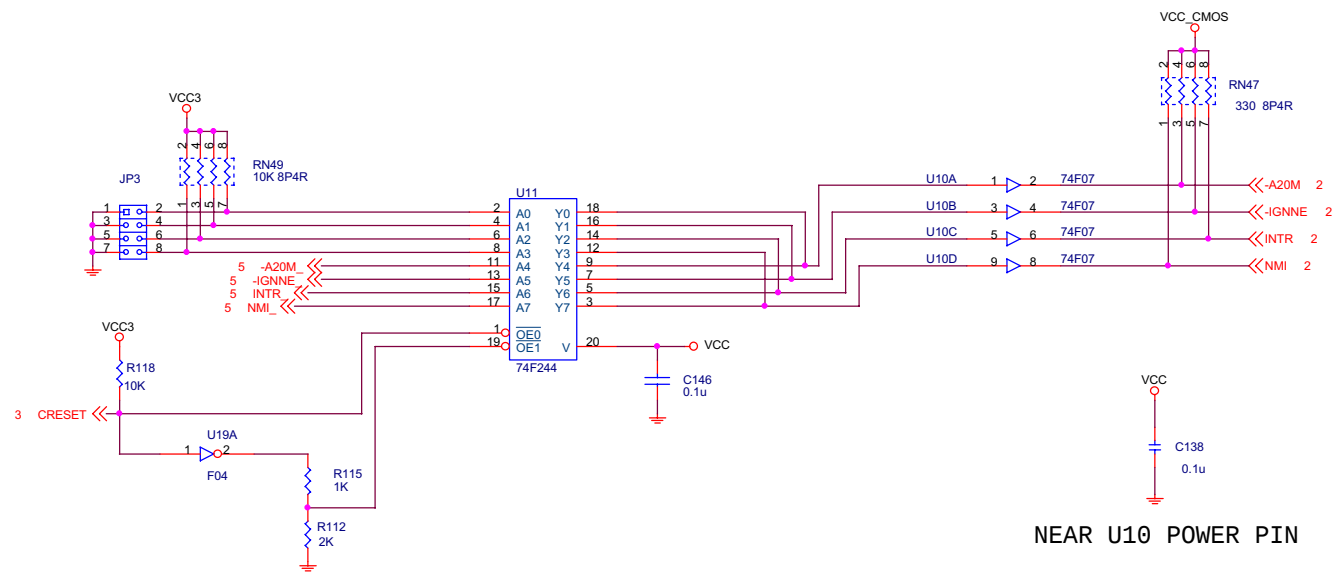
Trace note:
1. The length of GCLK should be as short as possible.
2. The length of GCLKIN should equal to the length of GCLKO * 3".
3. The width of AGPREF is 30 mils or wider.

JETWAY INFORMATION.			
Title NORTH BRIDGE VT82C694X-B			
Size C	Document Number J-694AS	Rev 0.1	
Date: Thursday, November 23, 2000	Sheet 4	of 22	



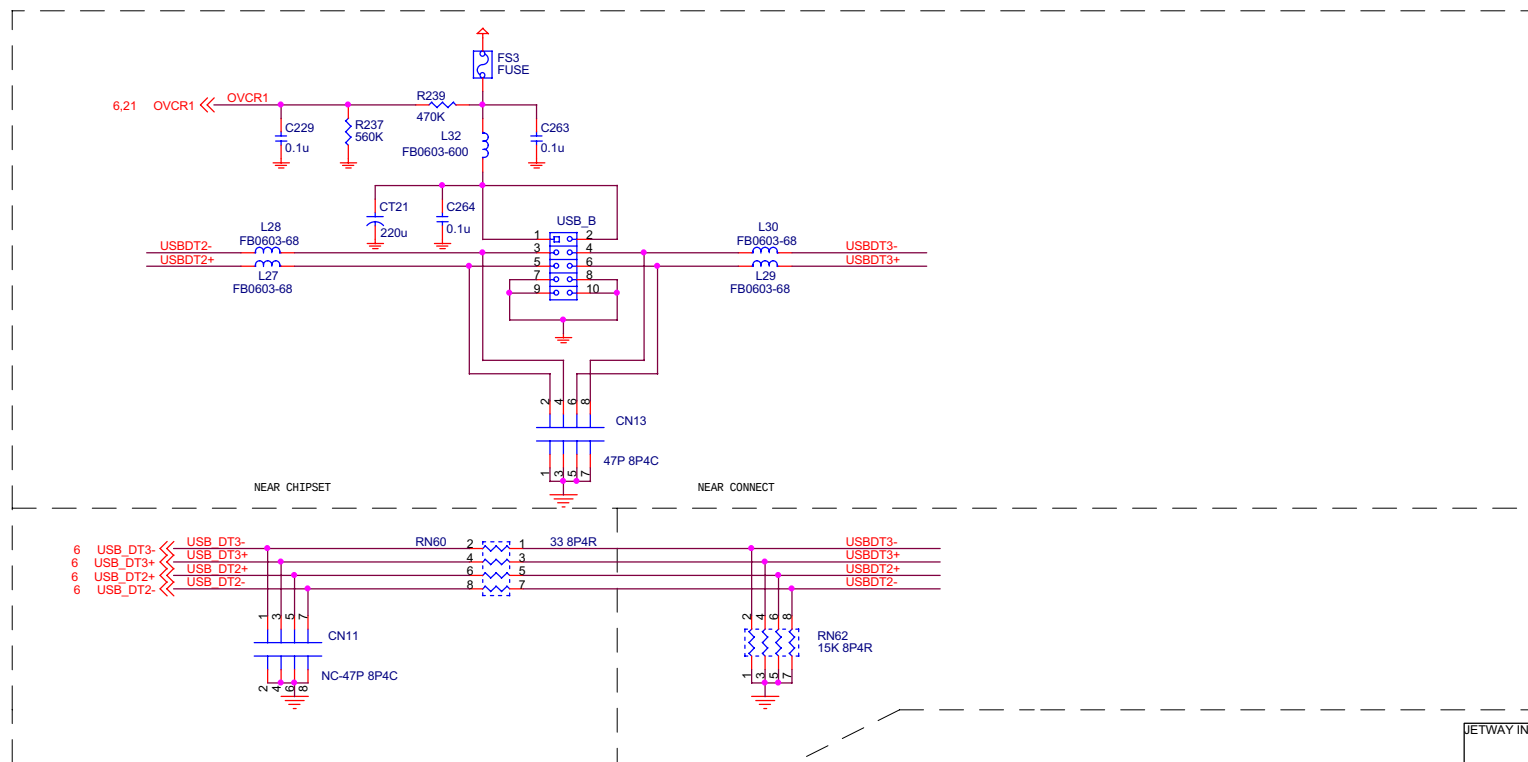


JETWAY INFORMATION			
Title			
SOUTH BRIDGE VT82C686A-B			
Size	Document Number	Rev	
C	J-694AS	0.1	
Date:	Thursday, November 23, 2000	Sheet	6 of 22



NEAR U10 POWER PIN

Freq/Ratio	1	2	3	4
3	ON	OFF	ON	ON
3.5	ON	OFF	OFF	ON
4	OFF	ON	ON	ON
4.5	OFF	ON	OFF	ON
5	OFF	OFF	ON	ON
5.5	OFF	OFF	OFF	ON
6	ON	ON	ON	OFF
6.5	ON	ON	OFF	OFF
7	ON	OFF	ON	OFF
7.5	ON	OFF	OFF	OFF
8	OFF	ON	ON	OFF



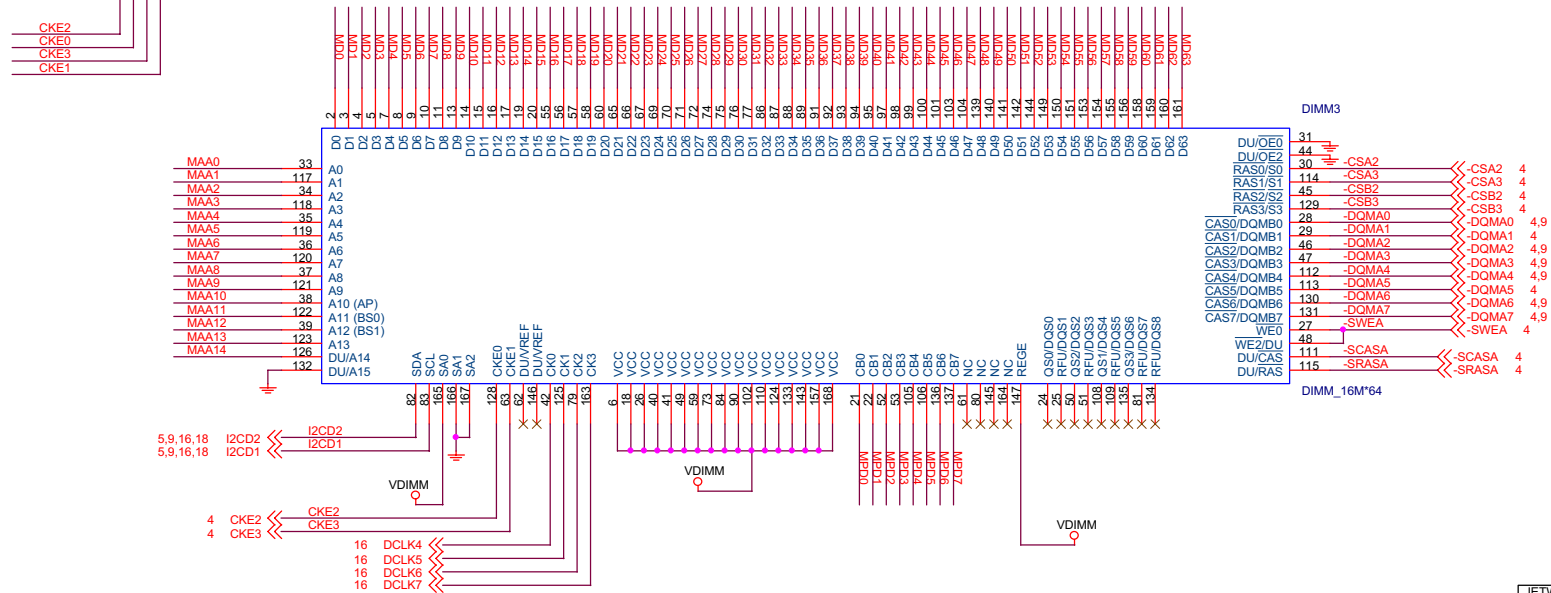
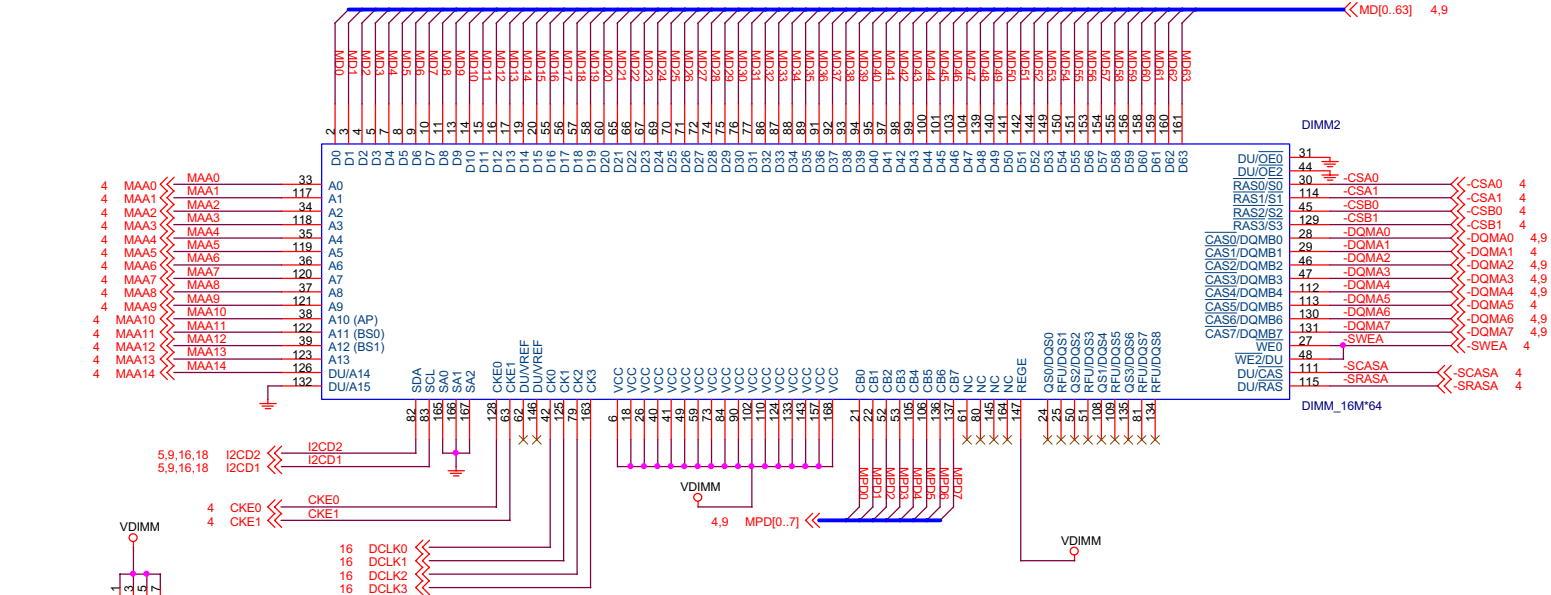
JETWAY INFORMATION

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USB/FREQ. RATIO

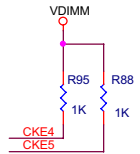
Size C Document Number
J-694AS

Rev
0.1

Date: Thursday, November 23, 2000 Sheet 7 of 22



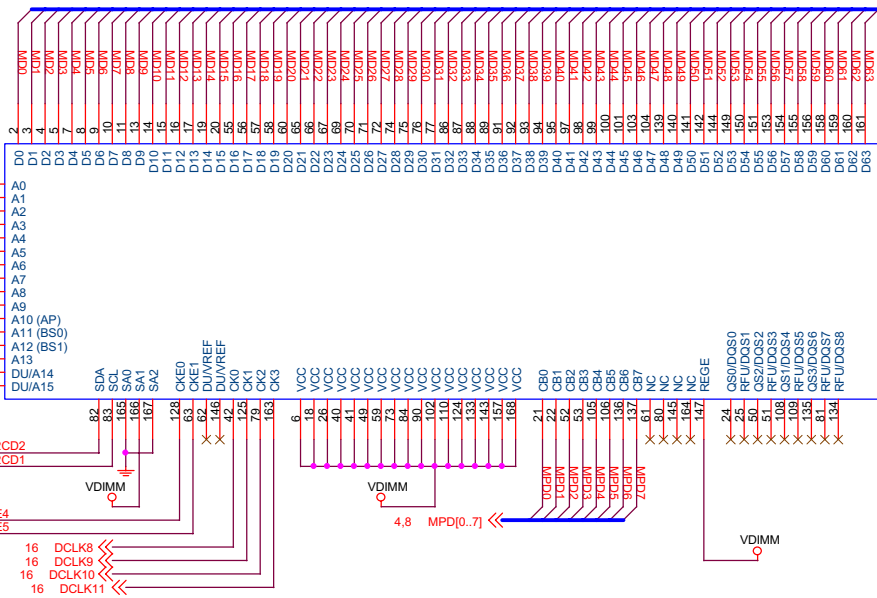
JETWAY INFORMATION			
Title			
SDRAM			
Size	Document Number		
C	J-694AS		
Date:			Rev
Thursday, November 23, 2000			0.1
Sheet		of	
1		22	



MAB0 MAB1 MAB2 MAB3 MAB4 MAB5 MAB6 MAB7 MAB8 MAB9 MAB10 MAB11 MAB12 MAB13 MAB14

I2CD2 I2CD1

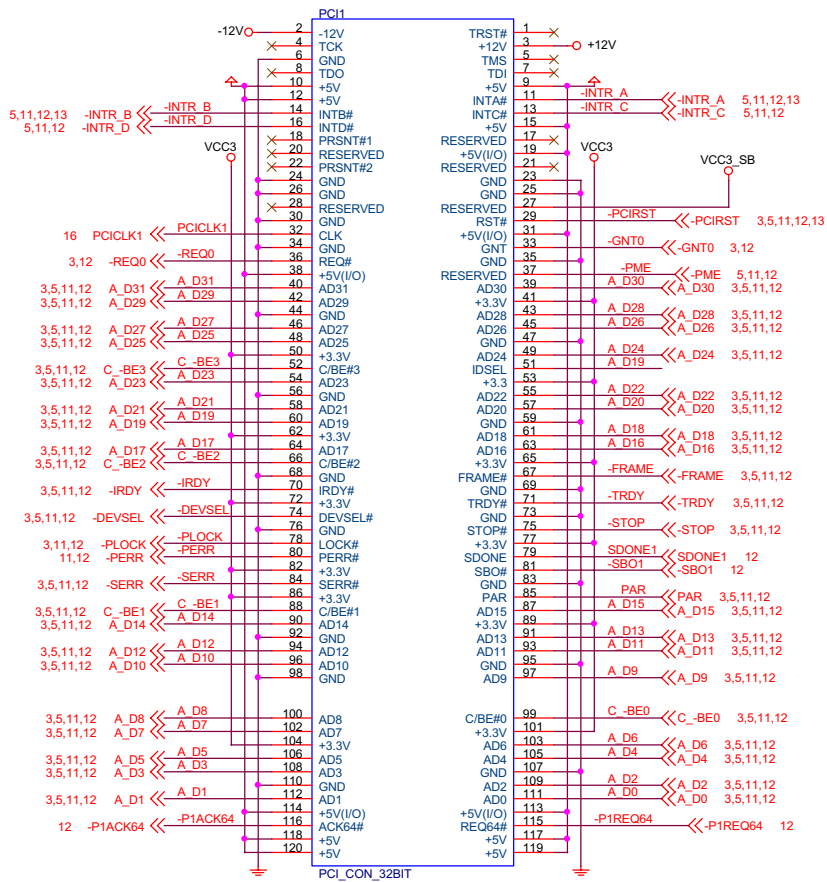
CKE4 CKE5



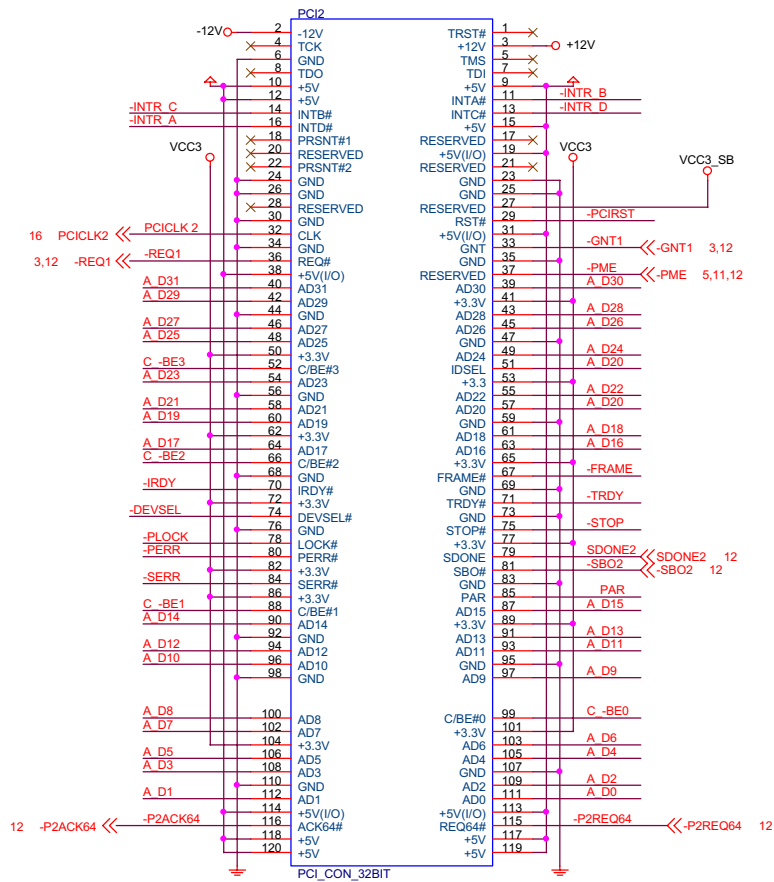
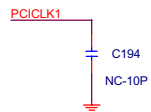
DIMM1

DIMM_16M*64

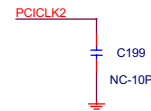
JETWAY INFORMATION			
Title SDRAM			
Size C	Document Number J-694AS		Rev 0.1
Date:	Thursday, November 23, 2000	Sheet 9 of 22	



AD:19
A,B,C,D



AD:20
B,C,D,A



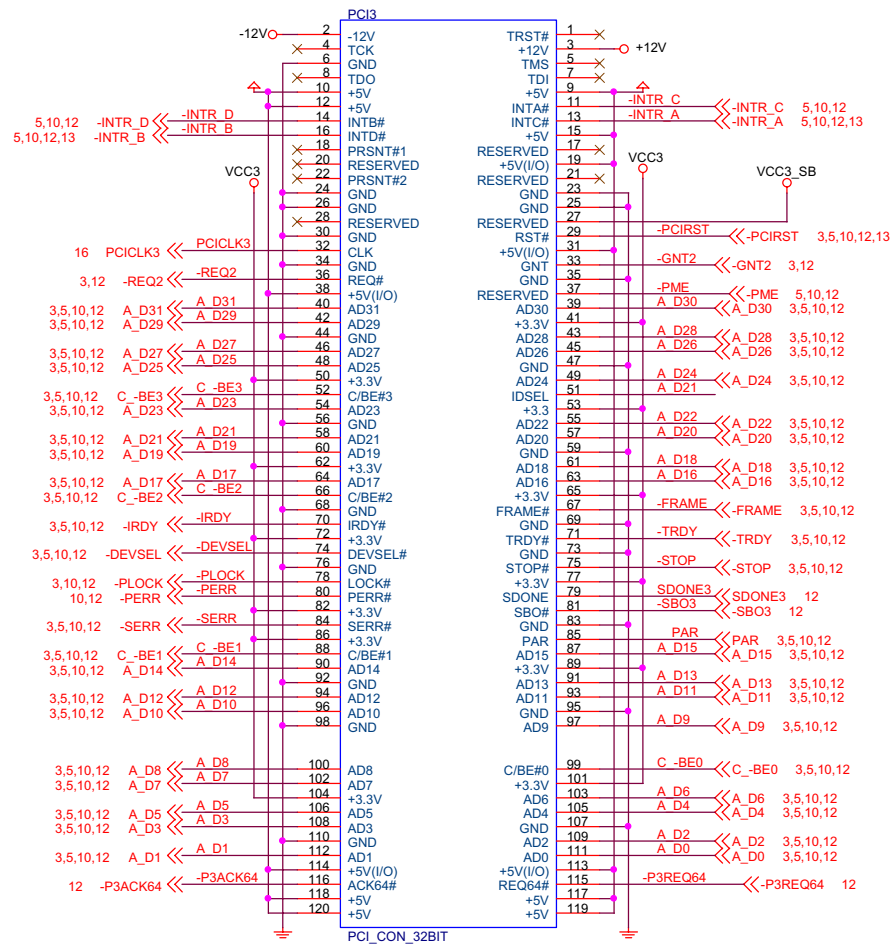
JETWAY INFORMATION

Title
PCI SLOT

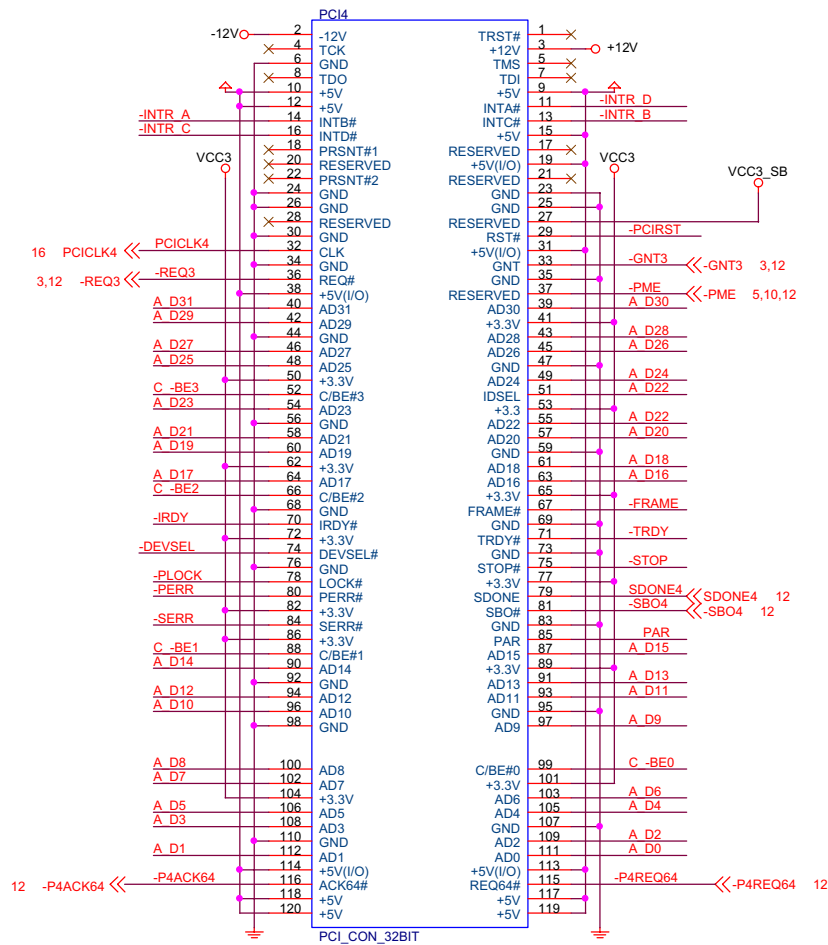
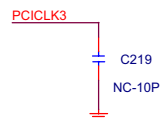
Size Document Number
J-694AS

Rev
0.1

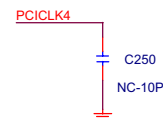
Date: Thursday, November 23, 2000 Sheet 10 of 22



AD: 21
C, D, A, B

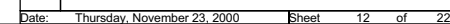


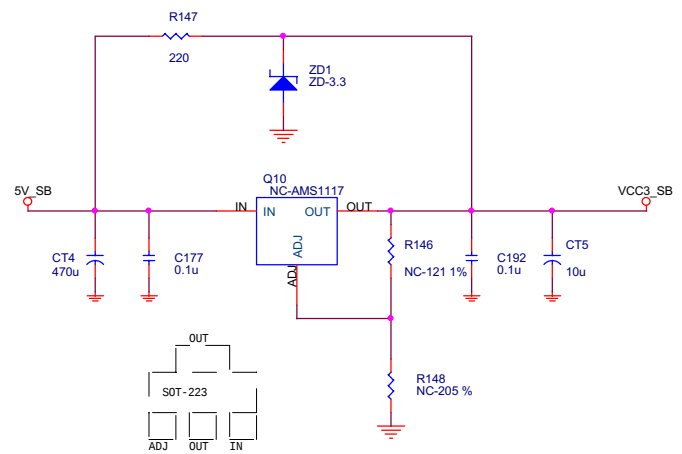
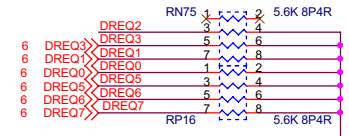
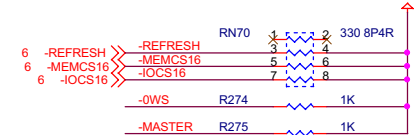
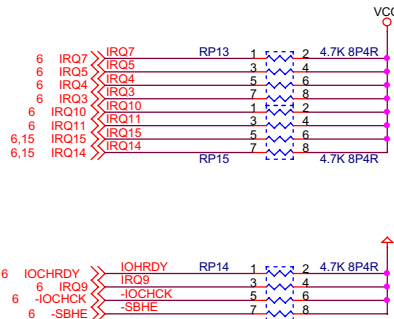
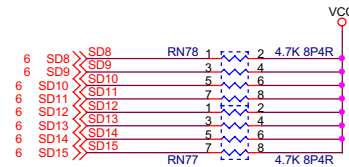
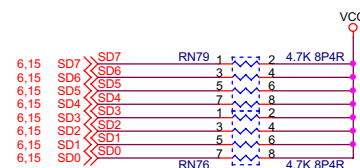
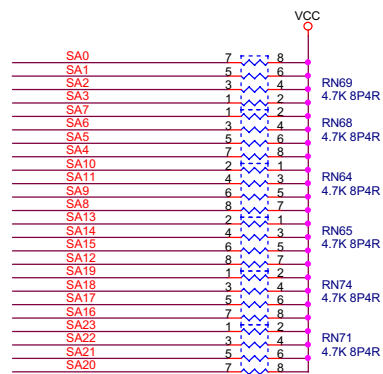
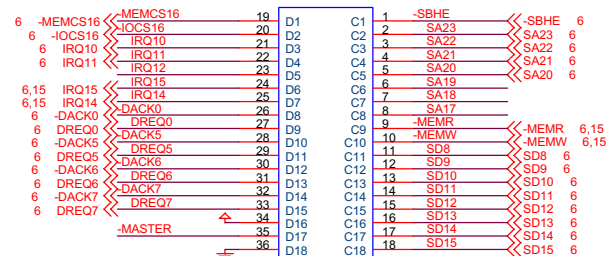
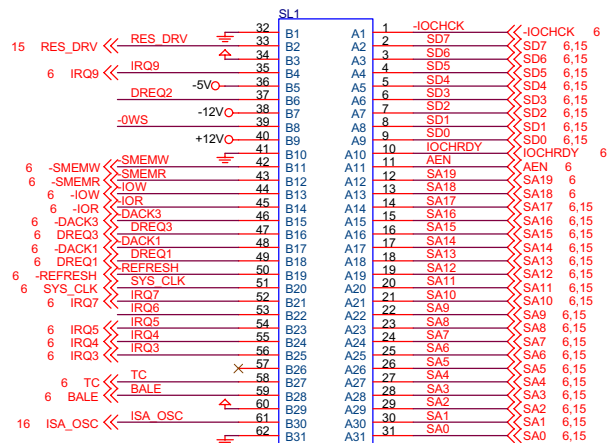
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D, A, B, C



JETWAY INFORMATION

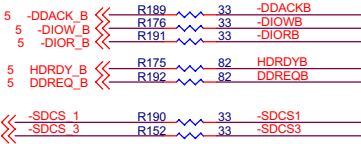
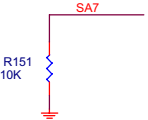
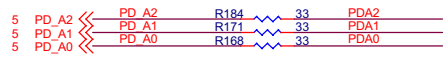
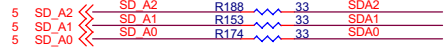
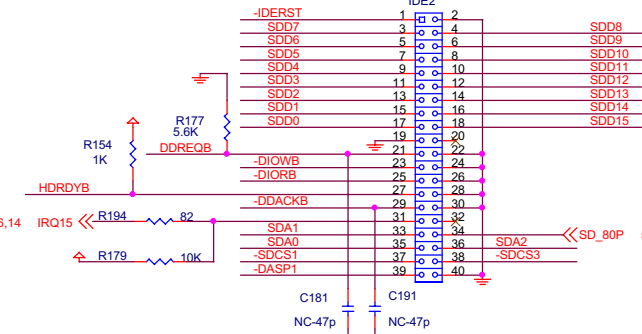
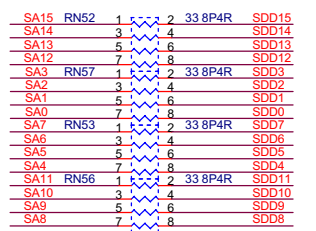
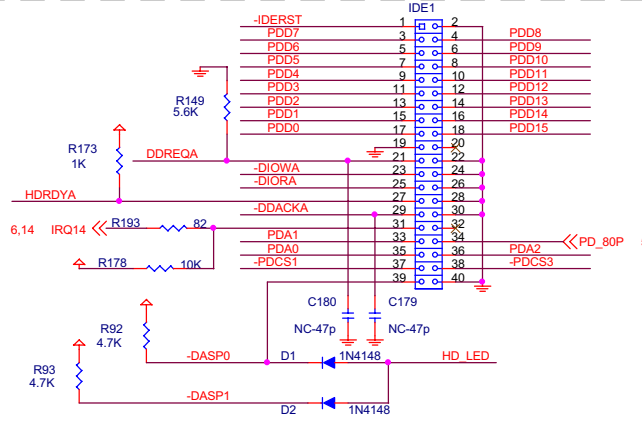
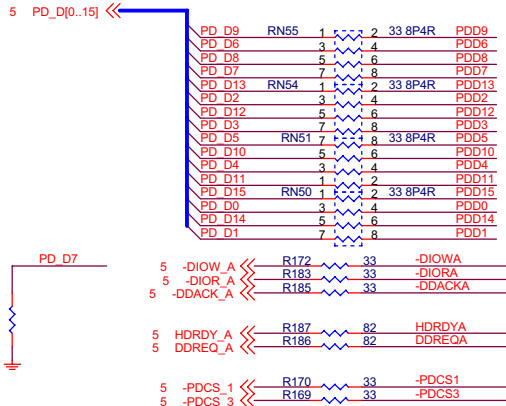
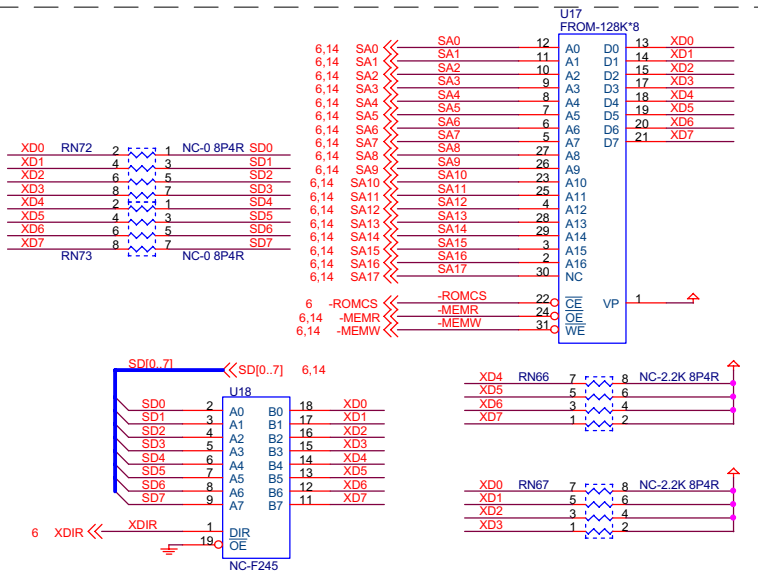
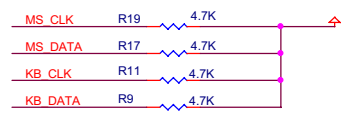
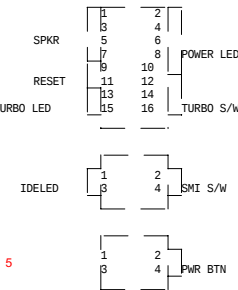
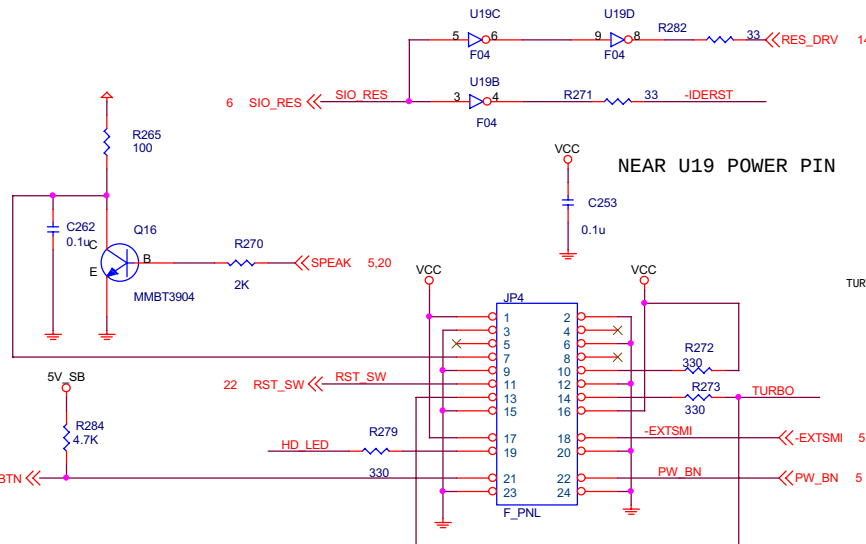
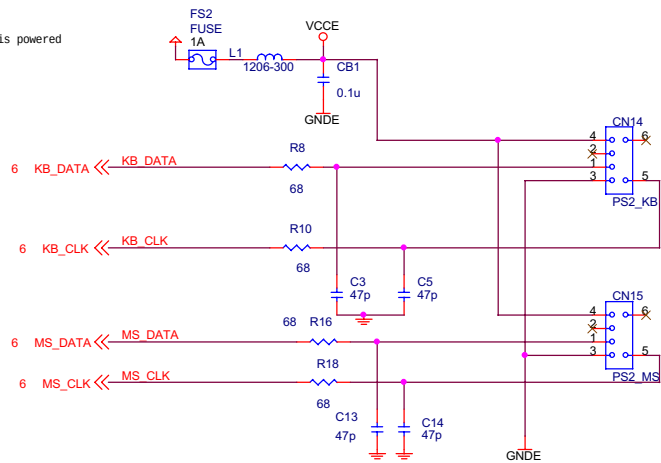
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PCI SLOT		
Size	Document Number	Rev
	J-694AS	0.1
Date:	Thursday, November 23, 2000	Sheet 11 of 22





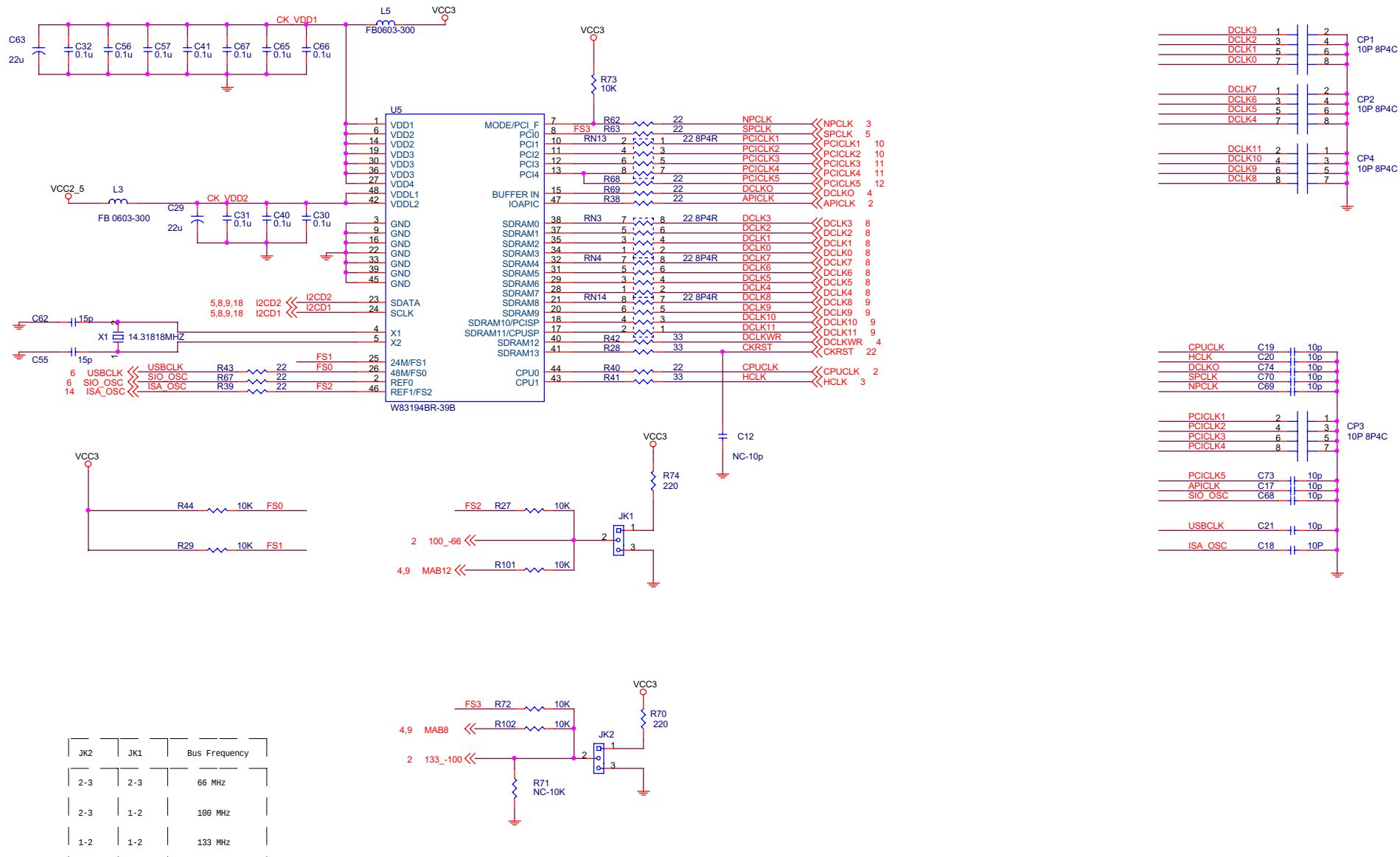
JETWAY INFORMATION			
Title			
ISA PULL UP RESISTANCE			
Size	Document Number		Rev
C	J-694AS		0.1
Date:	Thursday, November 23, 2000	Sheet	14 of 22

NOTE:F125 is powered by VCC3_SB



JETWAY INFORMATION

Title			PERIPHERAL
Size	Document Number	Rev	
C	J-694AS	0.1	
Date:	Thursday, November 23, 2000	Sheet	15 of 22

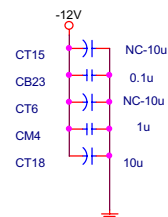
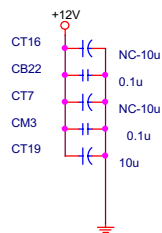
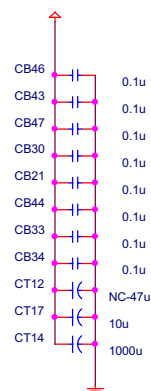
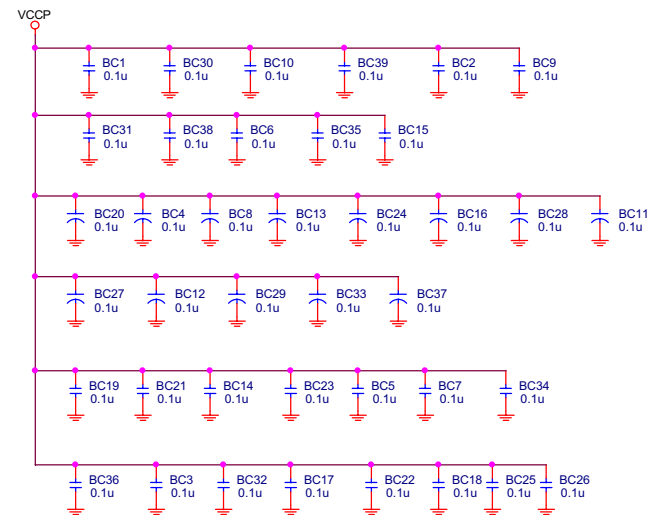
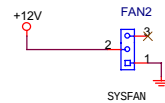
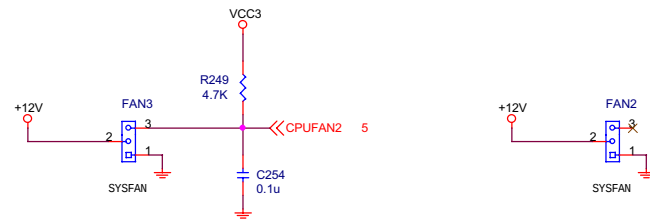
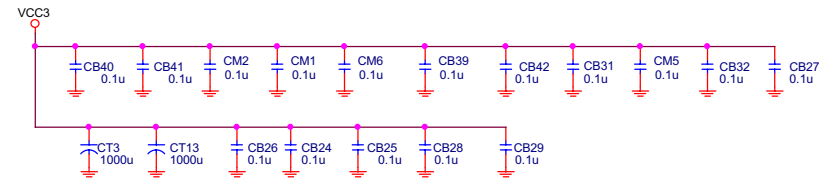
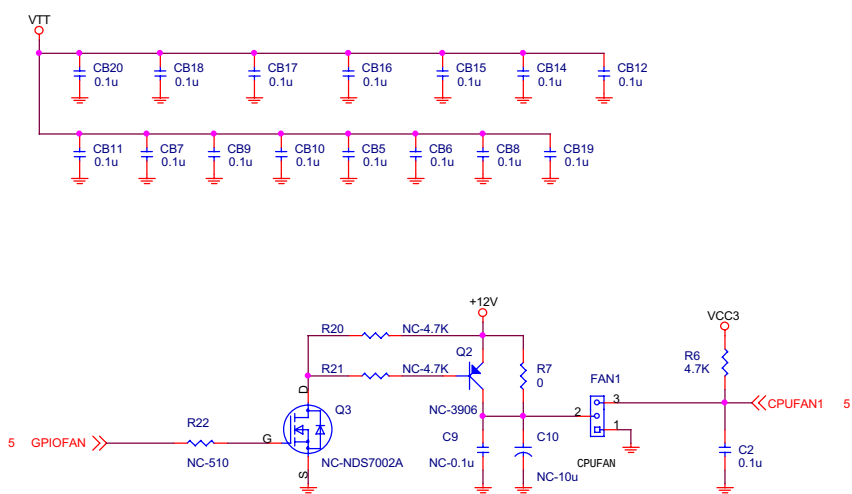


JETWAY INFORMATION.

Title
CLOCK SYNTHESIZER

Size C Document Number J-694AS Rev 0.1

Date: Thursday, November 23, 2000 Sheet 16 of 22



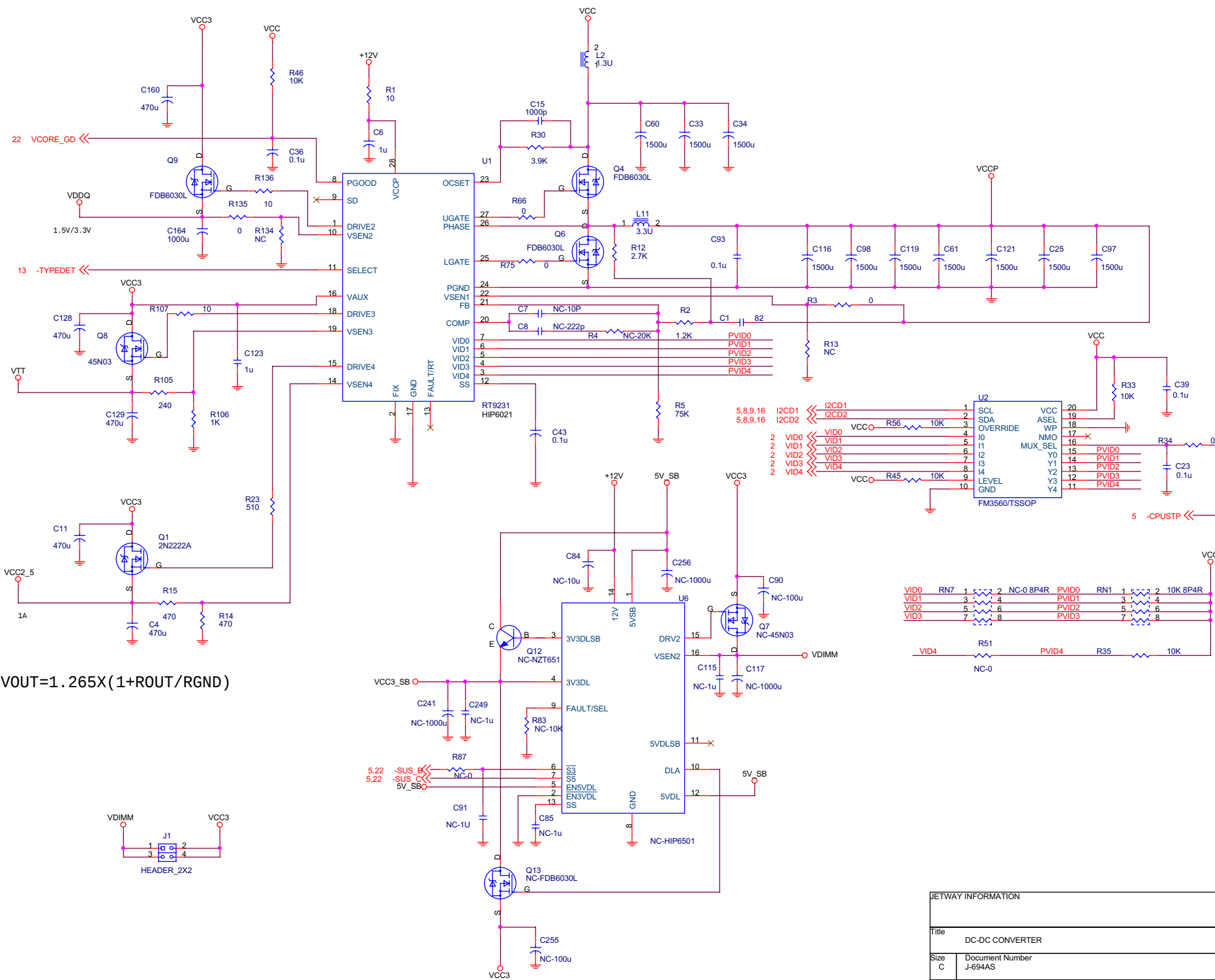
JETWAY INFORMATION

Title
POWER CONNECTOR / BYPASS CAPACITOR

Size C Document Number
J-694AS

Rev
0.1

Date: Thursday, November 23, 2000 Sheet 17 of 22



JETWAY INFORMATION

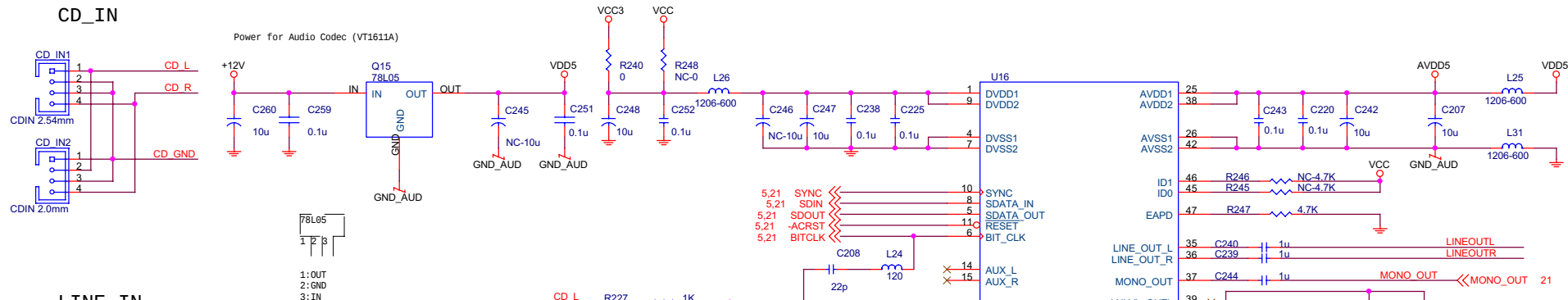
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Size C Document Number
J-694AS

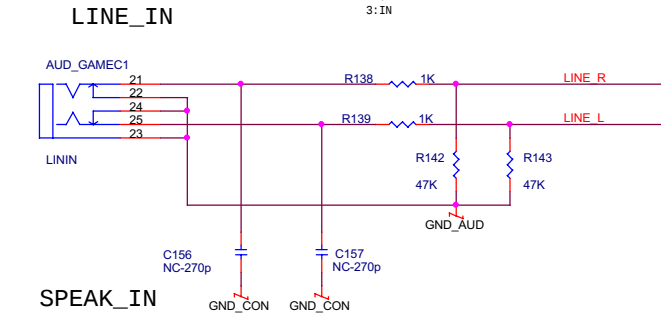
Rev
0.1

Date: Thursday, November 23, 2000 Sheet 18 of 22

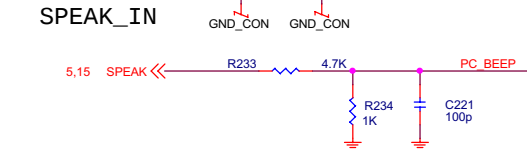
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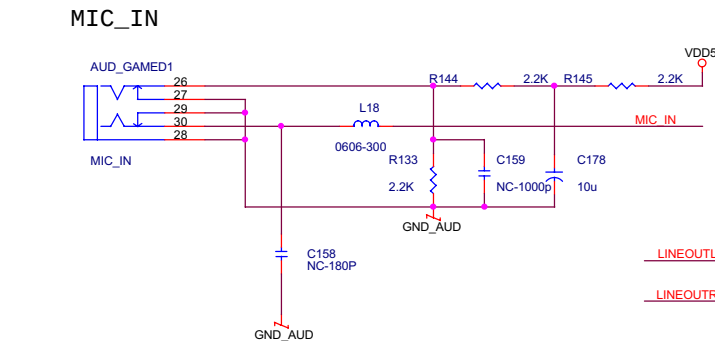
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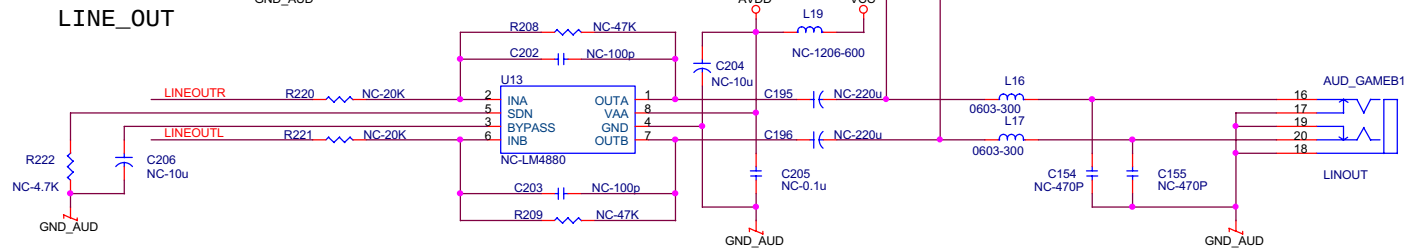
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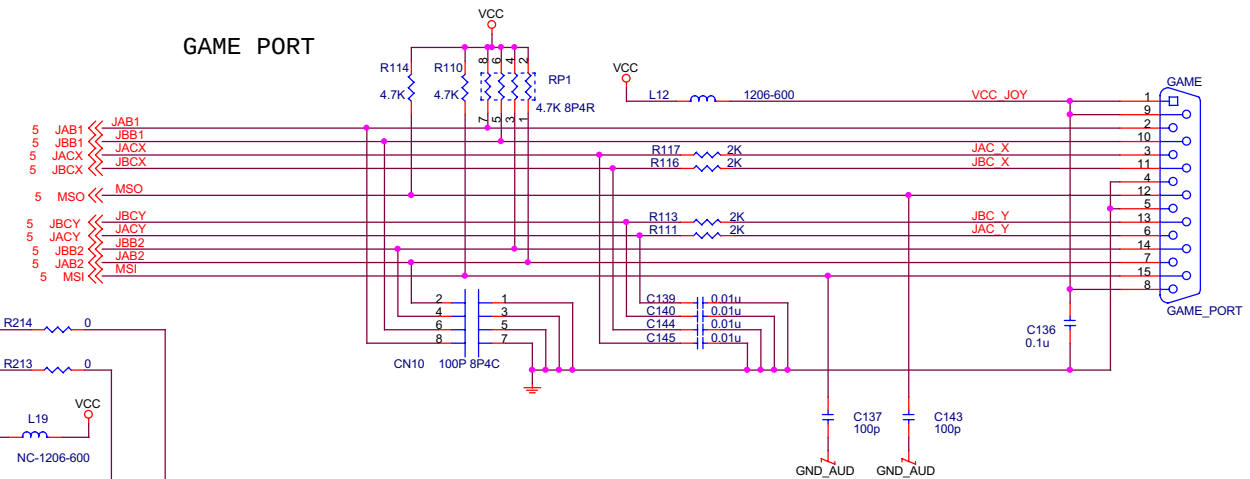
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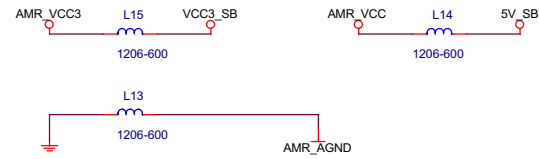
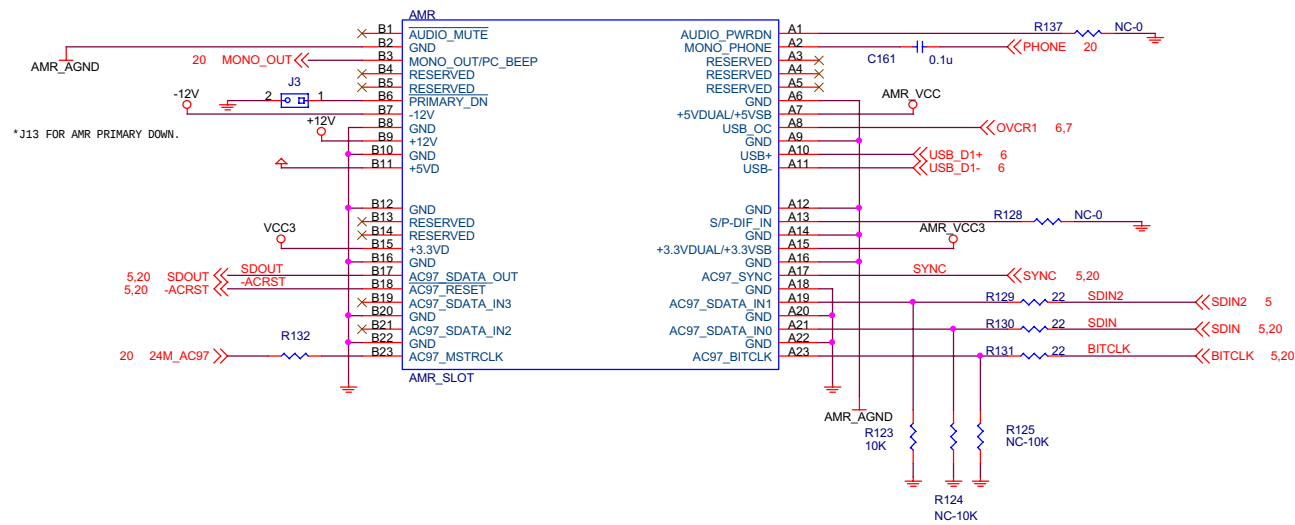
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GAME PORT



JET WAY INFORMATION			
Title AC'97 CODEC & AUDIO/GAME PORT			
Size C	Document Number J-694AS		Rev 0.1
Date:	Thursday, November 23, 2000	Sheet 20 of 22	



JETWAY INFORMATION			
Title			
ARM SLOT			
Size	Document Number		Rev
C	J-694AS		0.1
Date:	Thursday, November 23, 2000	Sheet	21 of 22

