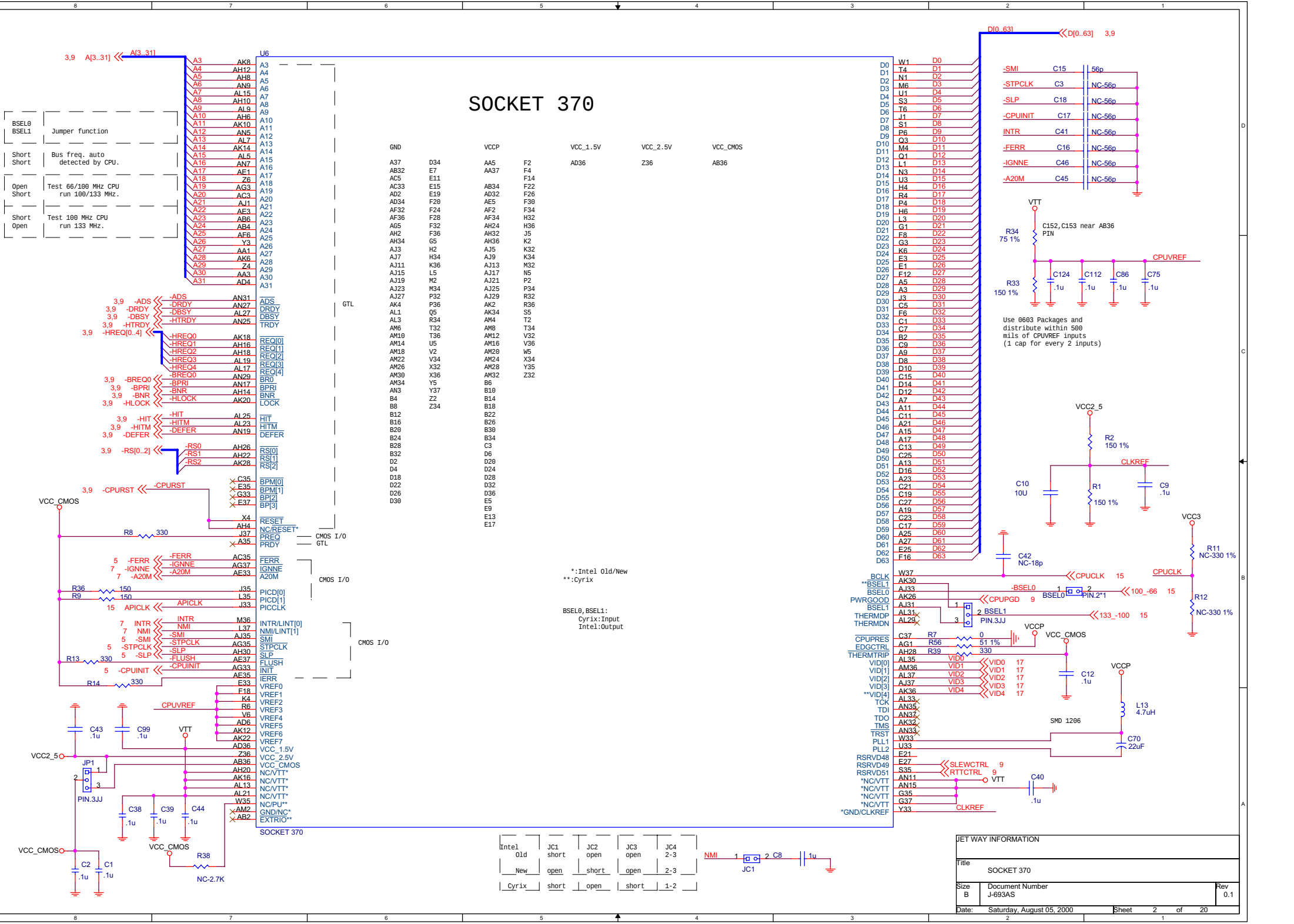


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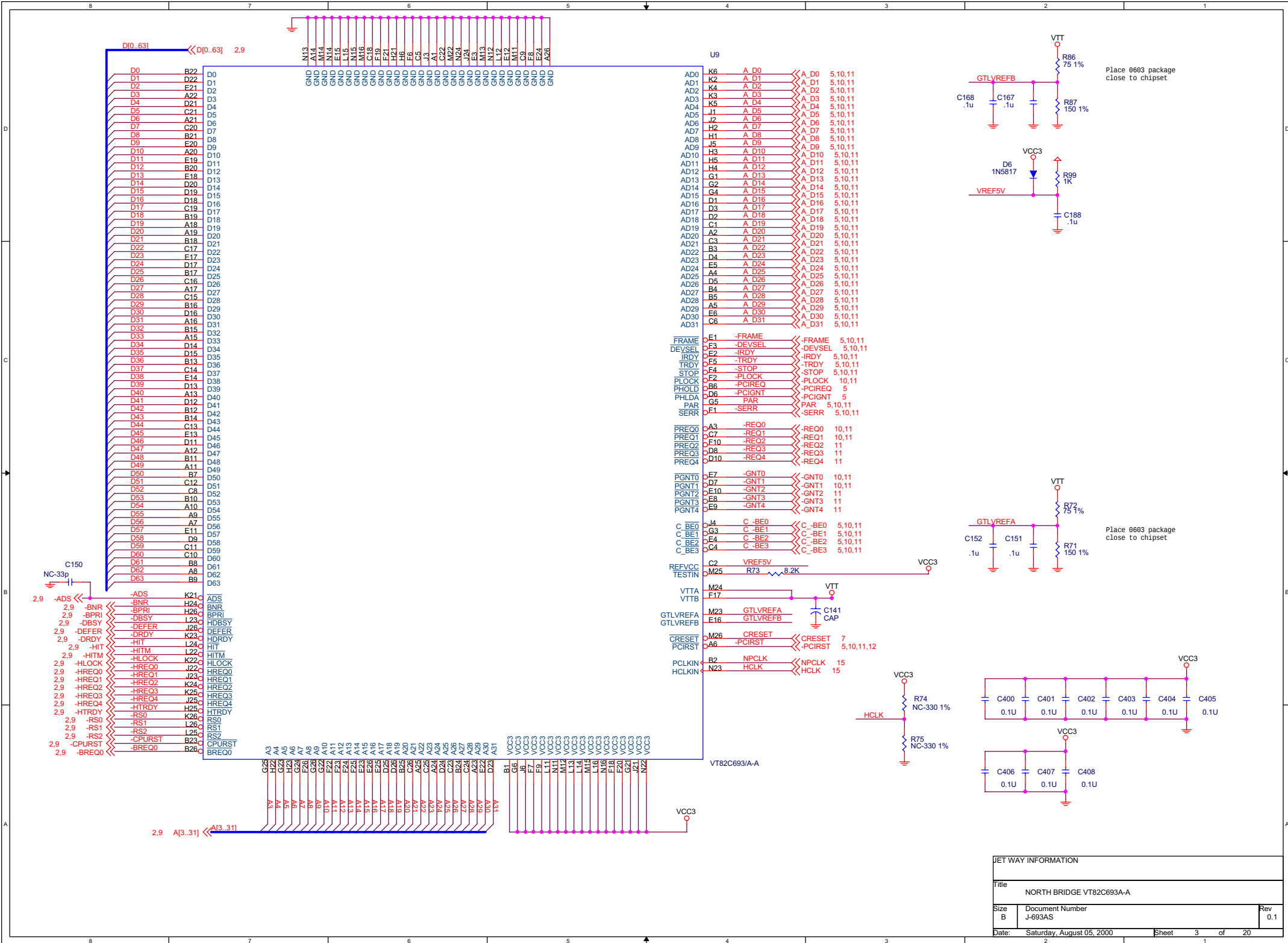
SOCKET 370 PROCESSOR	2
NORTH BRIDGE (VT82C693A)	3, 4
SOUTH BRIDGE (VT82C686A)	5, 6
USB2,3 & FREQUENCY RATIO	7
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ATX POWER CONNECT / VTT TERMINATOR / VCC3	9
PCI SLOT 1 & 2	10
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CLOCK SYNTHESIZER	15
FAN & BYPASS CAPACITOR	16
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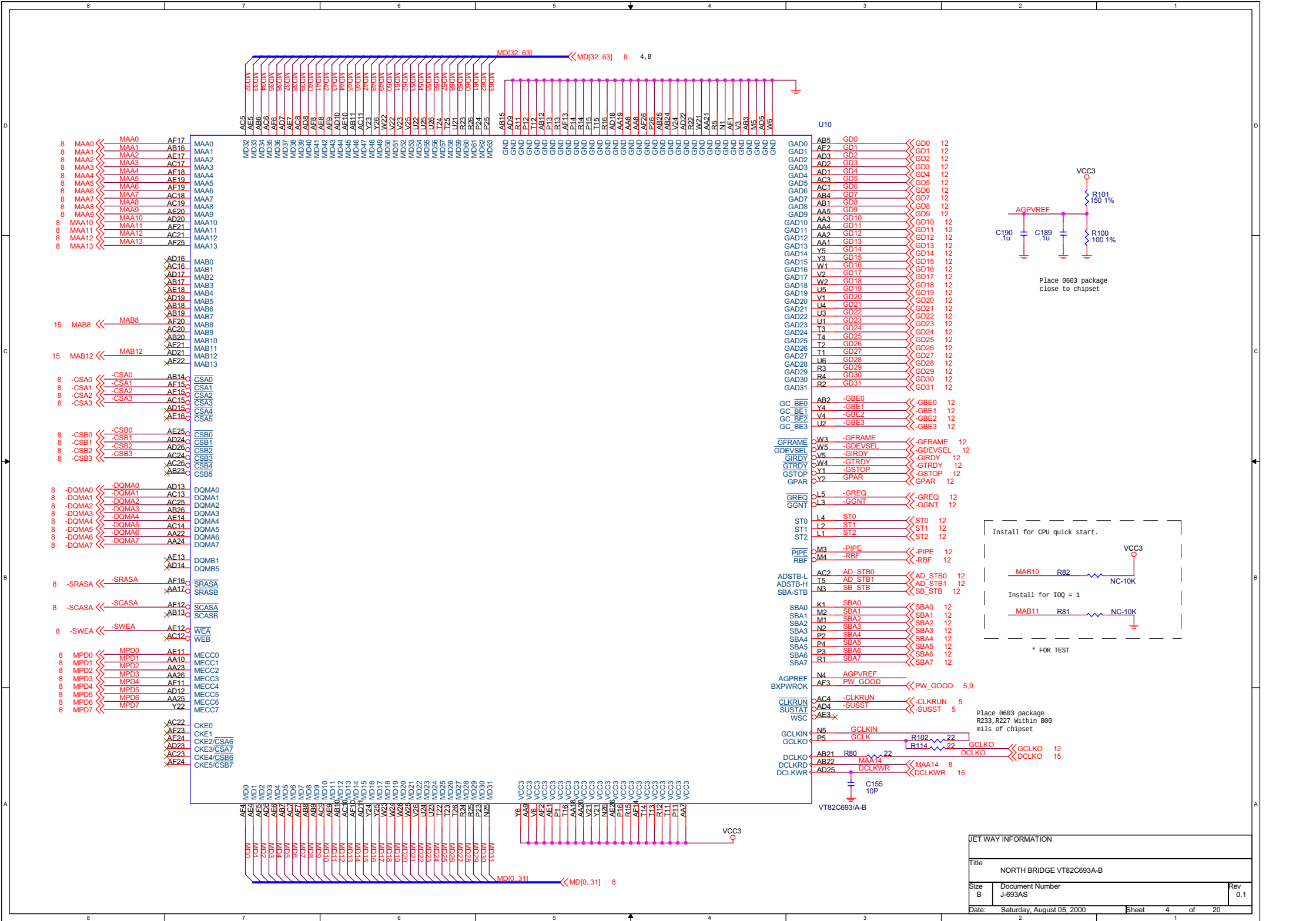
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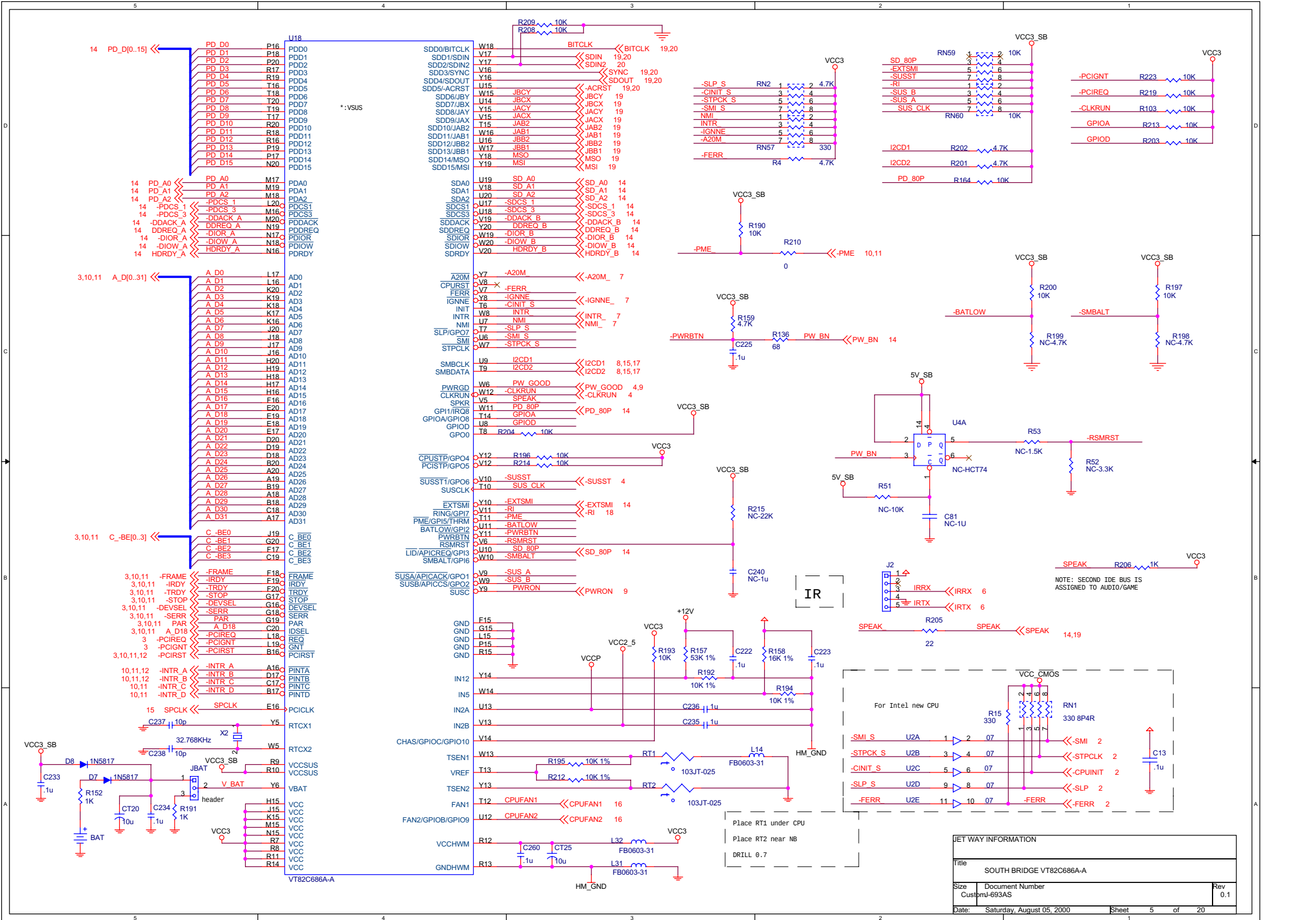


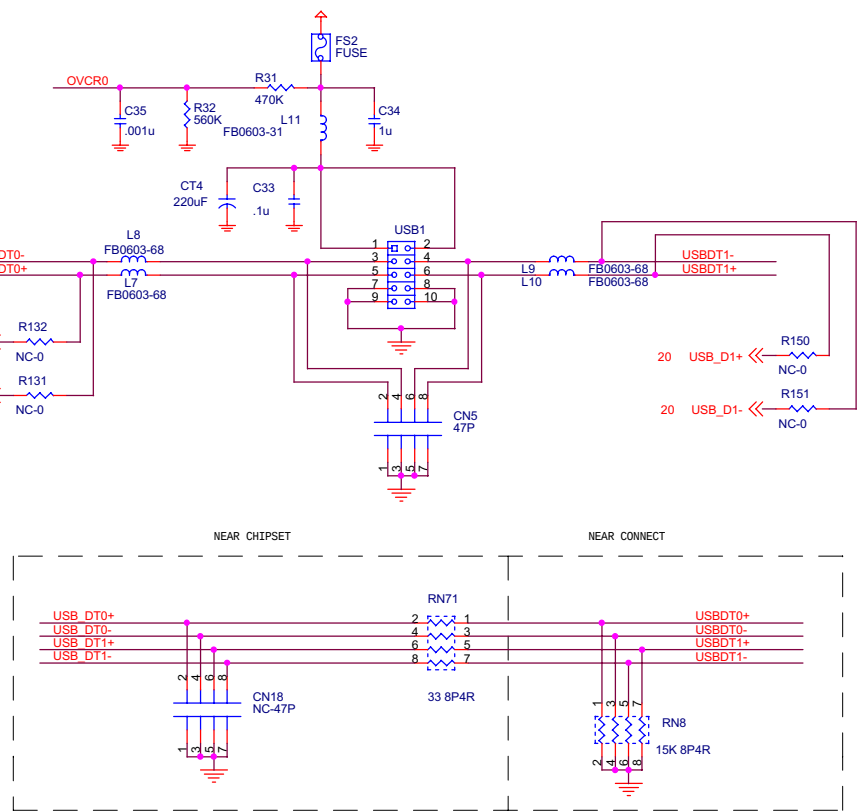
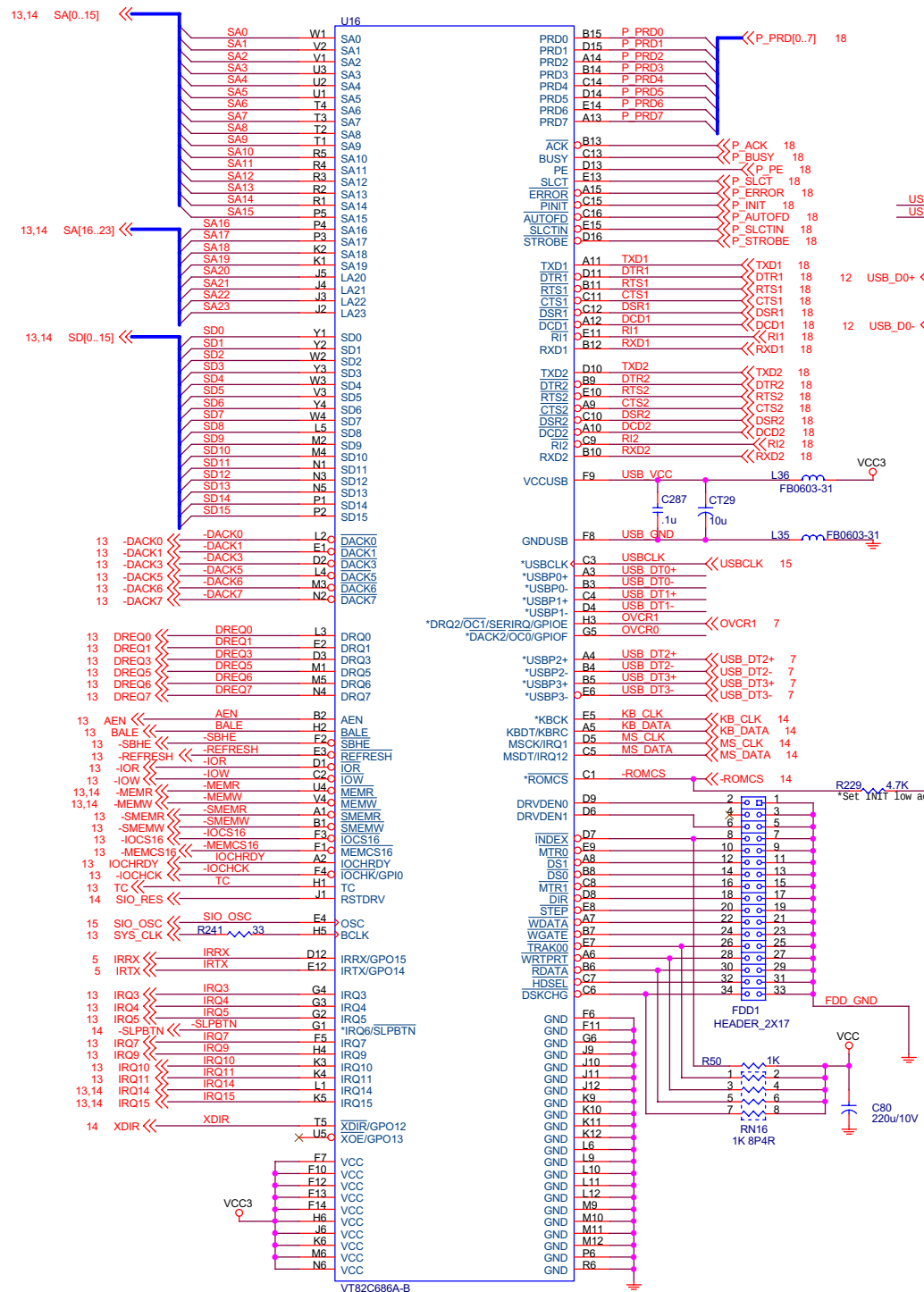
SOCKET 370

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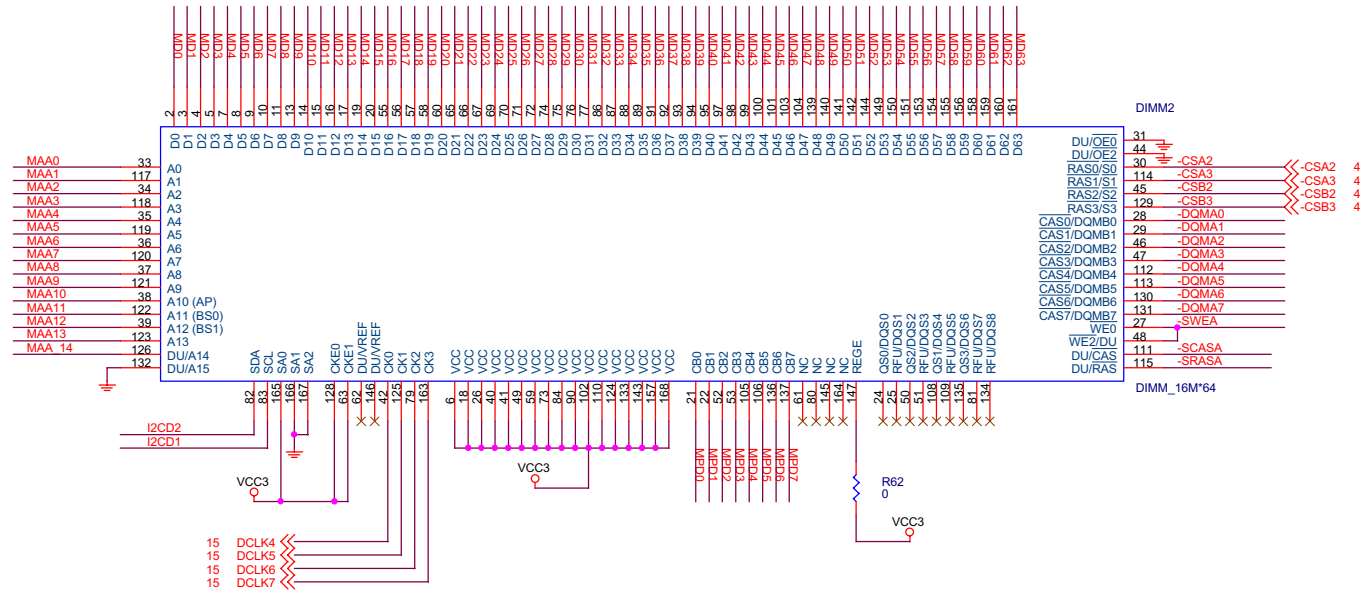
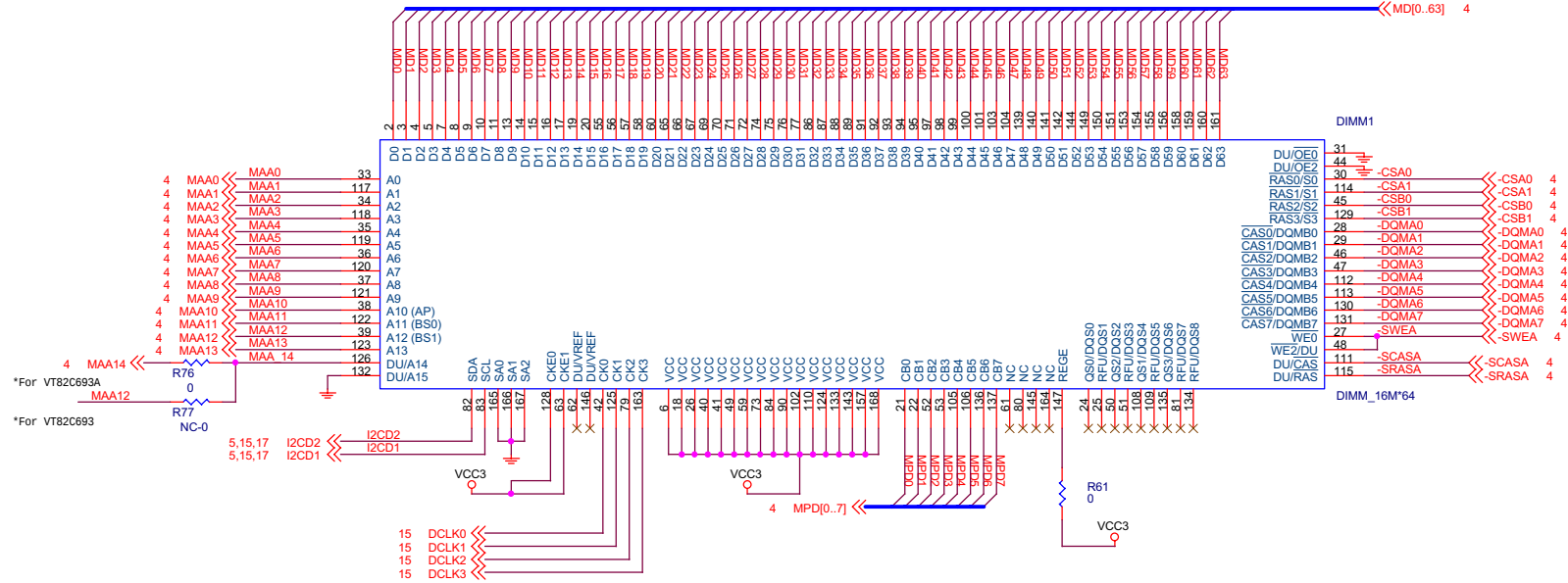




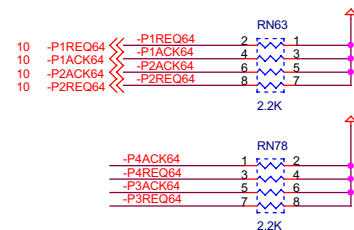
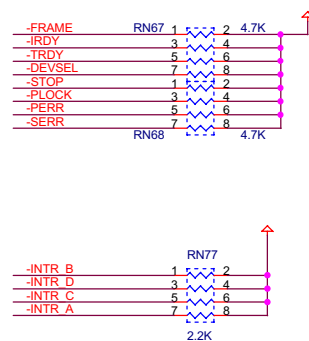
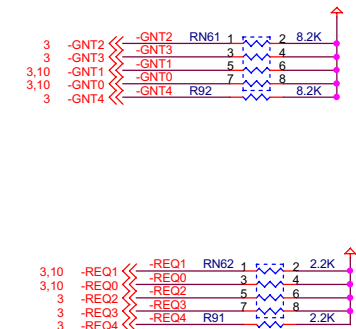
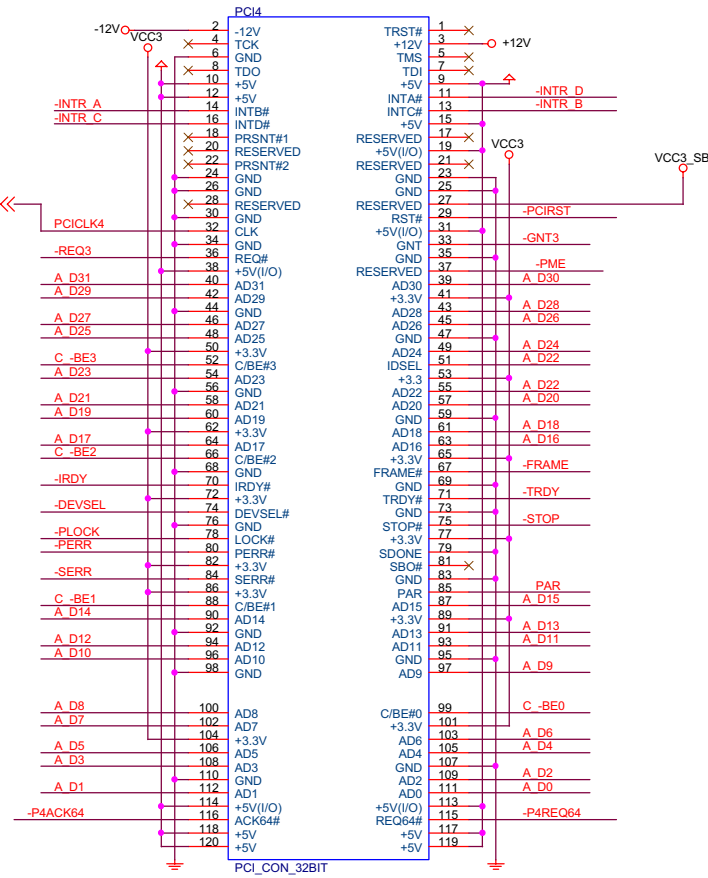
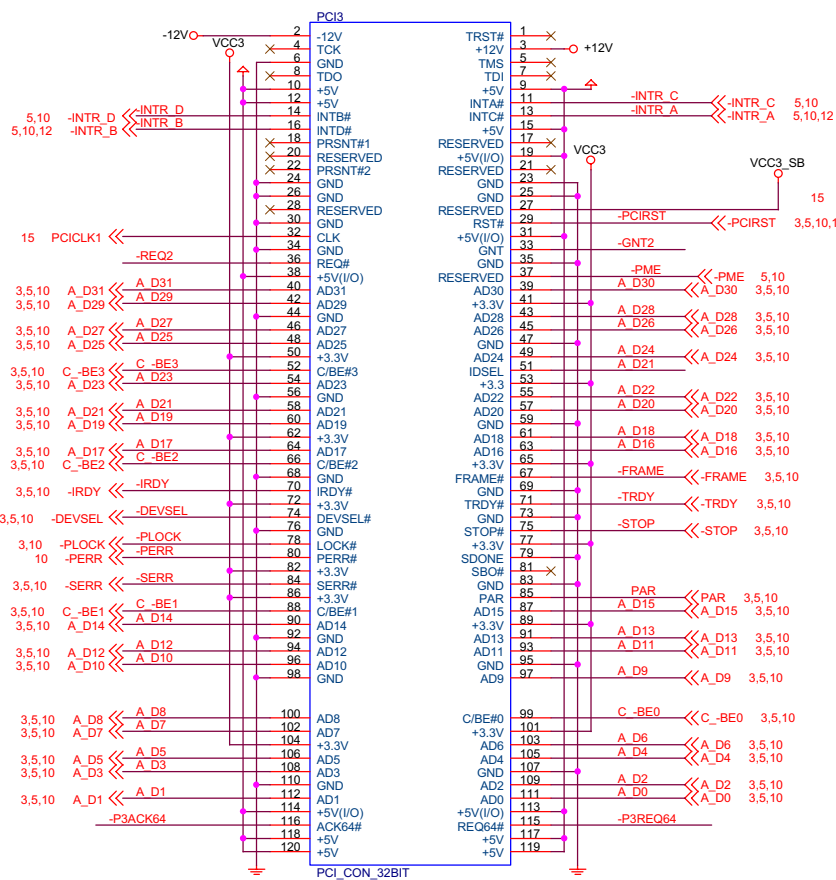




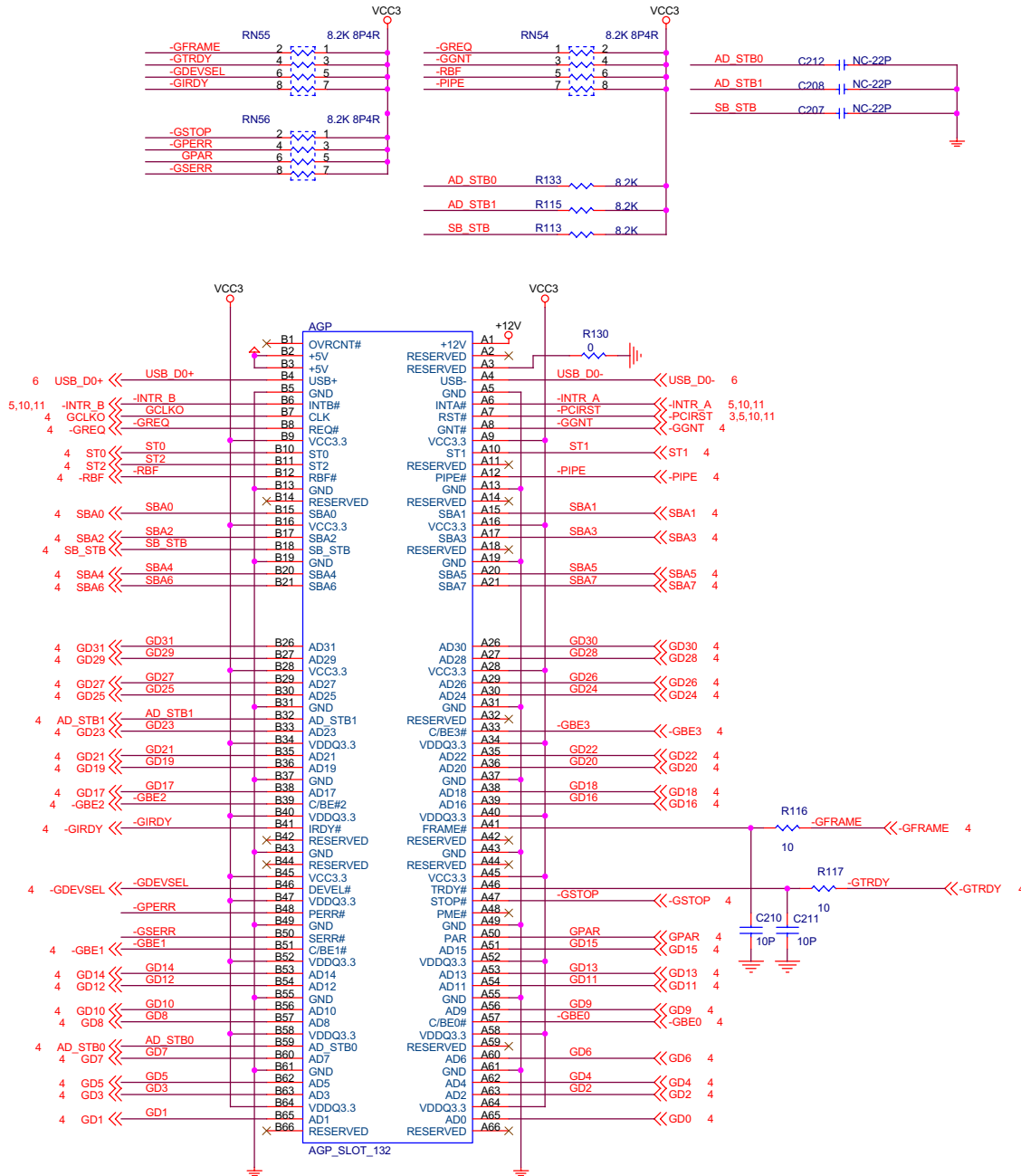
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SOUTH BRIDGE VT82C686A-B		
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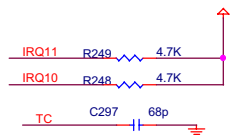
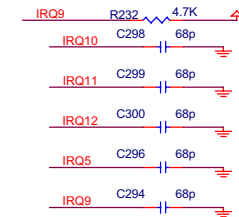
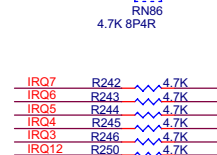
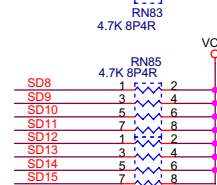
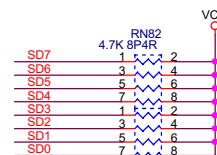
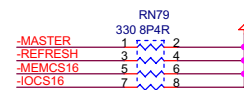
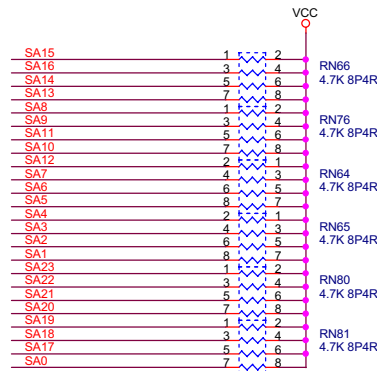
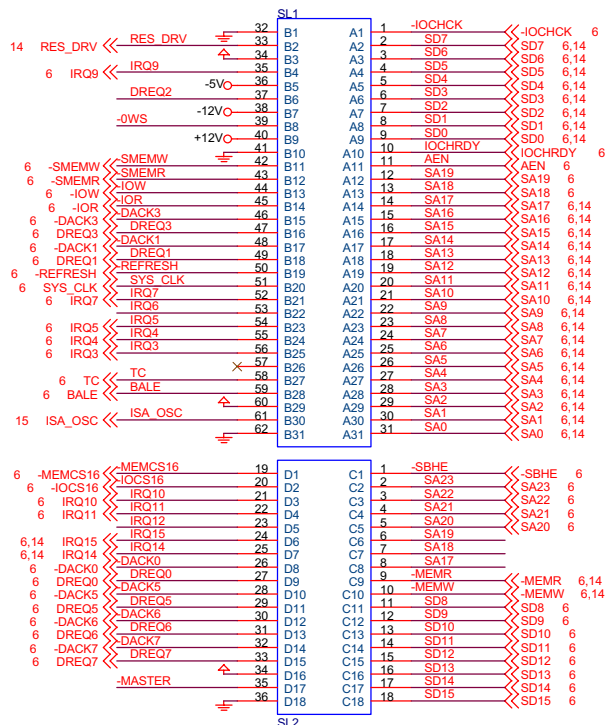
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AGP SLOT			
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ISA SLOT			
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NOTE: F125 is powered by VCC3_SB

FS1 FUSE 1A

L1

C6 .1uF

VCCCE

6 KB_DATA << KB_DATA

L3 FB0603-120

6 KB_CLK << KB_CLK

L4 FB0603-120

6 MS_DATA << MS_DATA

L5 FB0603-120

6 MS_CLK << MS_CLK

L6 FB0603-120

CN1

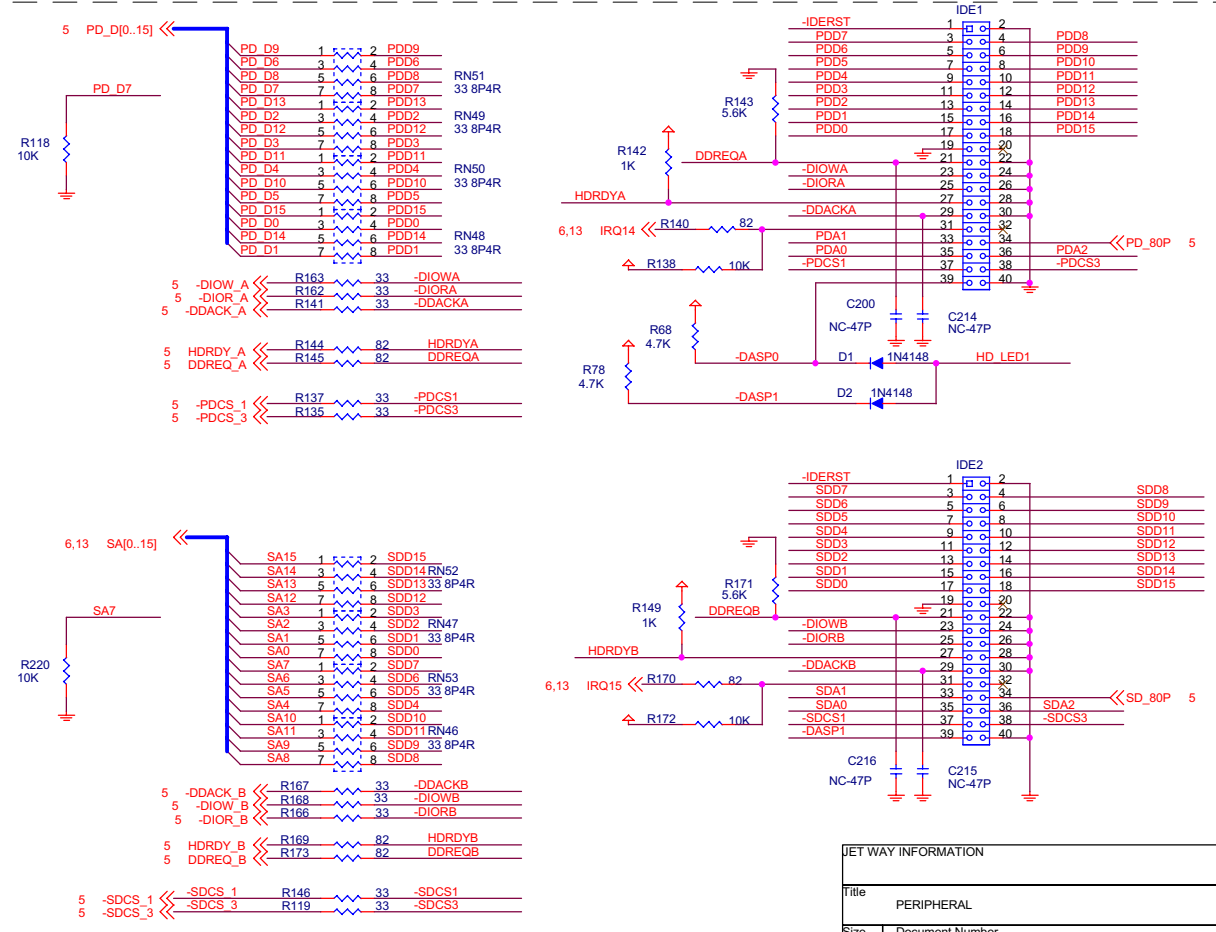
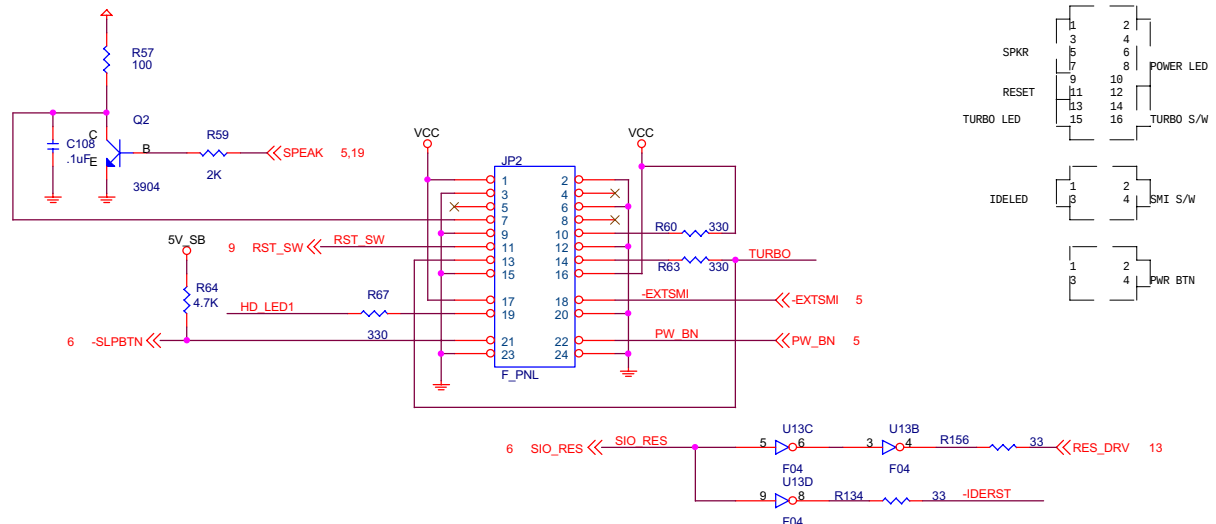
PS2_KB

CN2

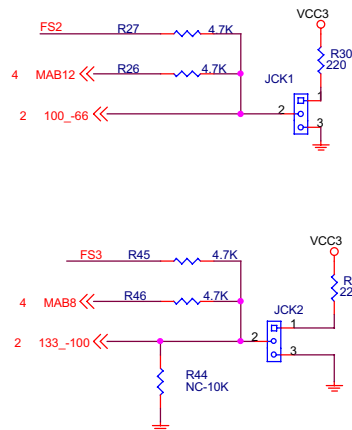
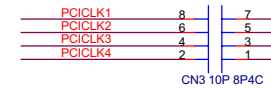
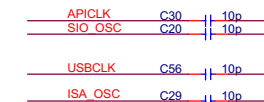
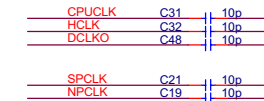
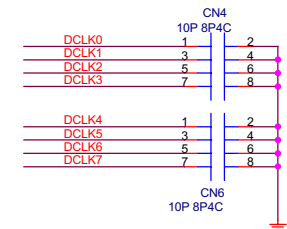
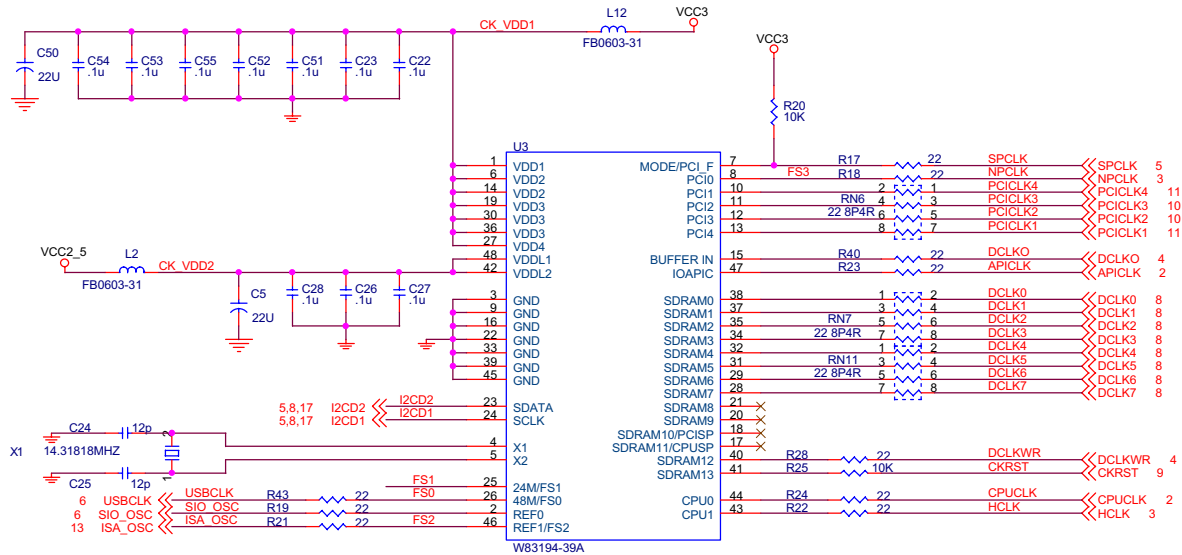
47P 8P4C

RN3

4.7K 8P4R



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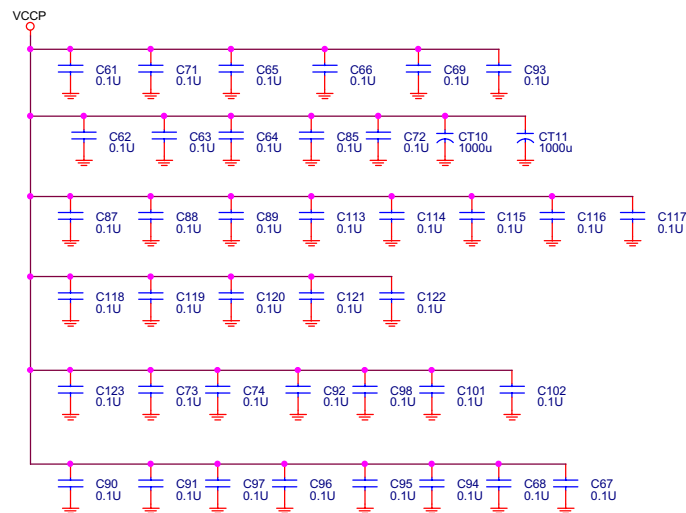
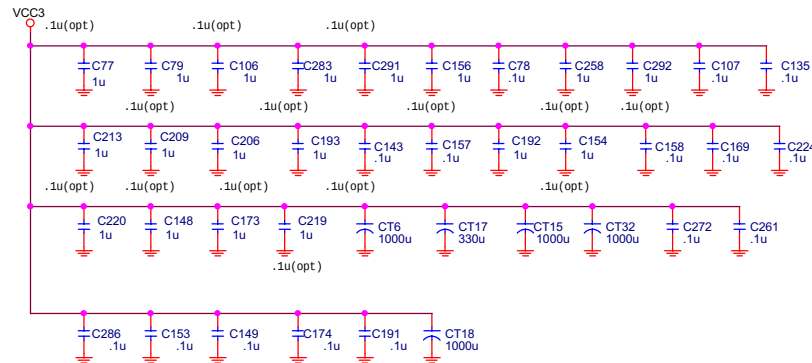
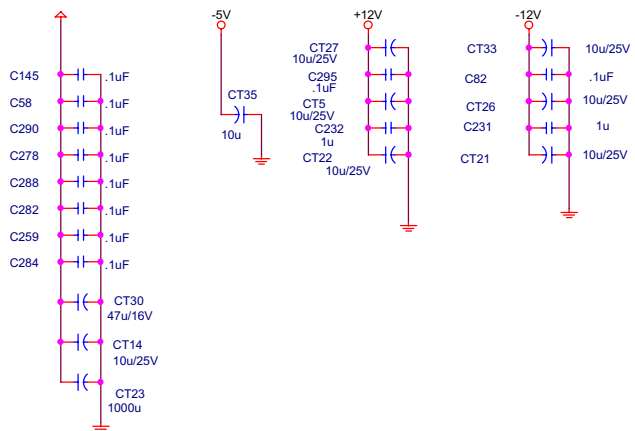
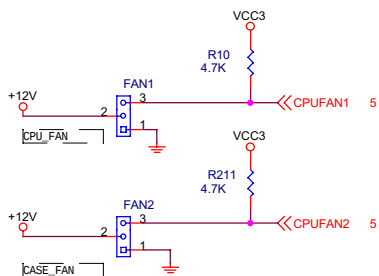
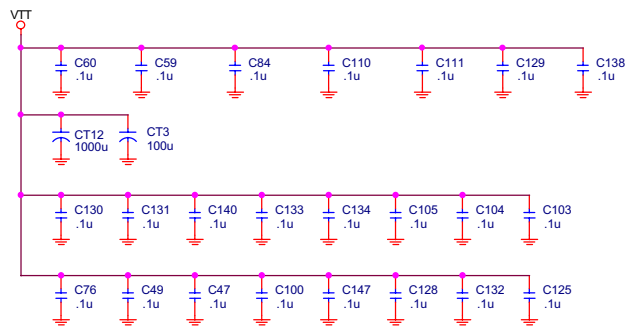


JCK2	JCK1	Bus Frequency
2-3	2-3	66 MHz
2-3	1-2	100 MHz
1-2	1-2	133 MHz

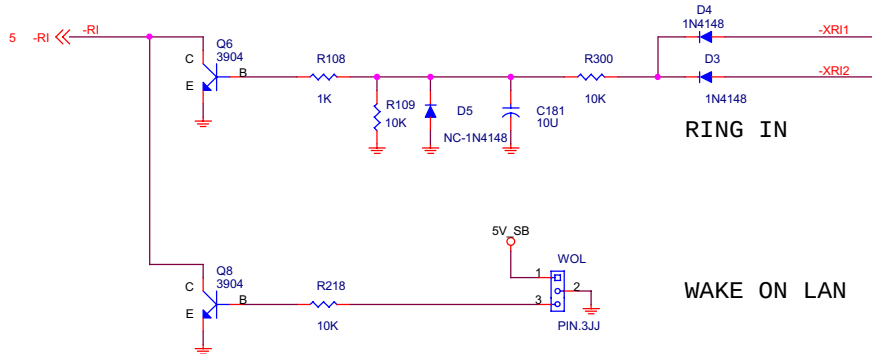
	FS0	FS1	FS2	FS3
66 MHz	1	1	0	0
100 MHz	1	1	1	0
133 MHz	1	1	1	1

*:For Cyrix CPU, the BSEL0 and BSEL1 are input pins.

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CLOCK SYNTHESIZER			
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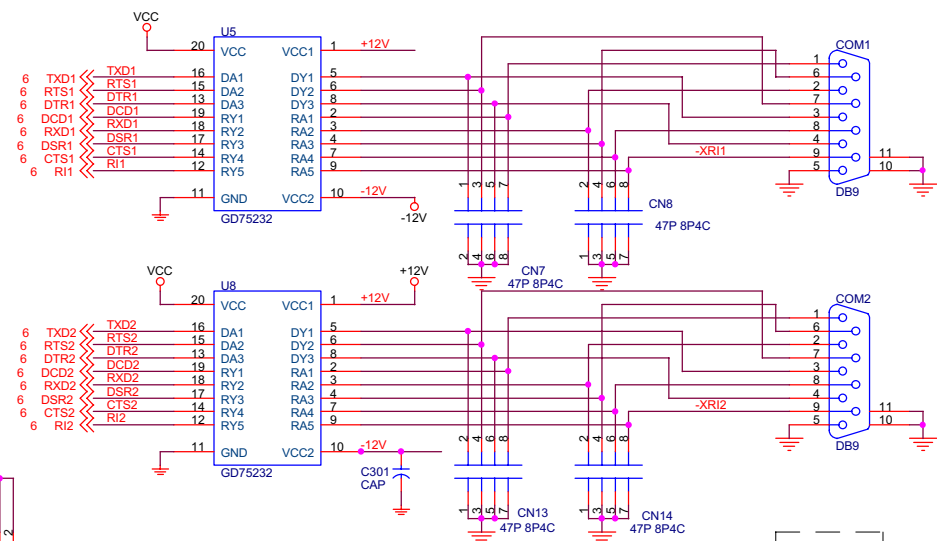


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BYPASS CAPACITOR			
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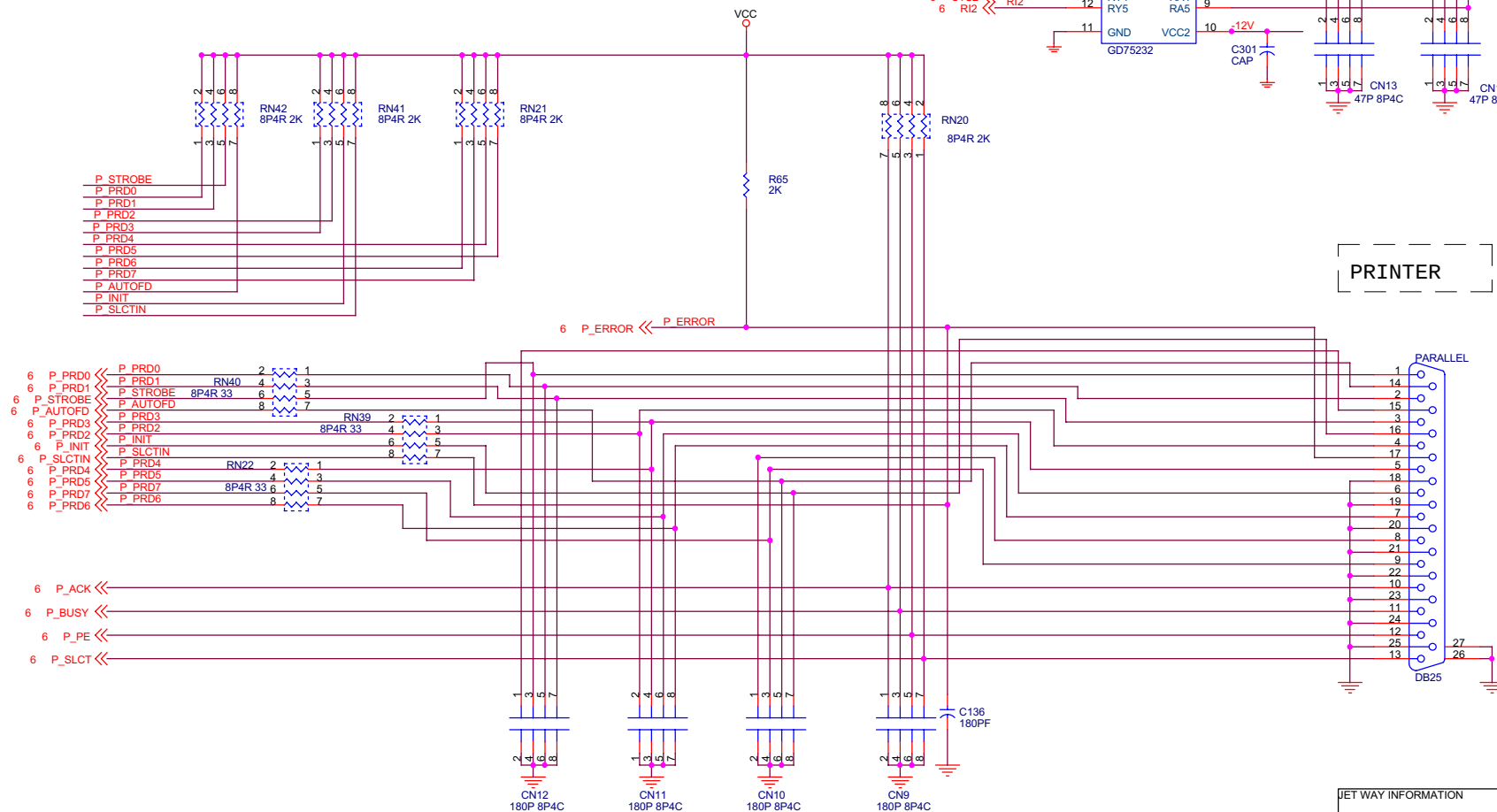


Place WOL connector near PCI slots

COM1, COM2 AND IR with Bipolar drivers and receivers

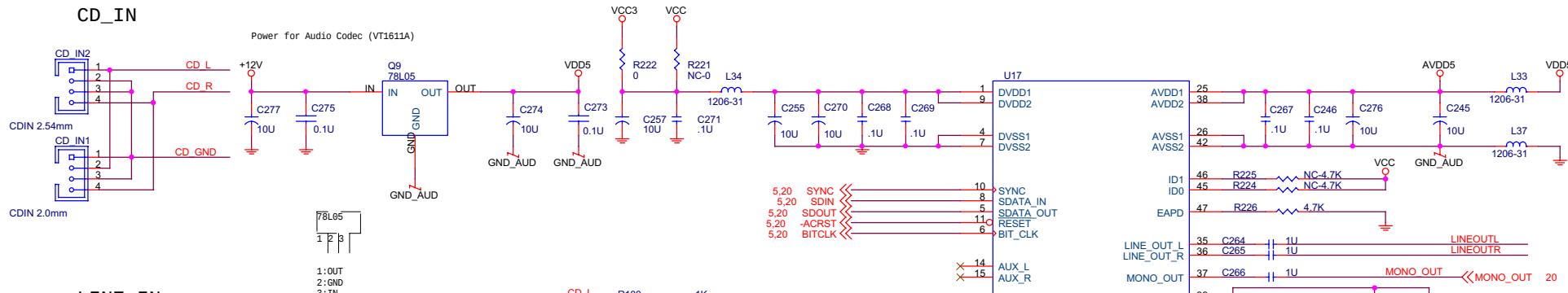


PRINTER

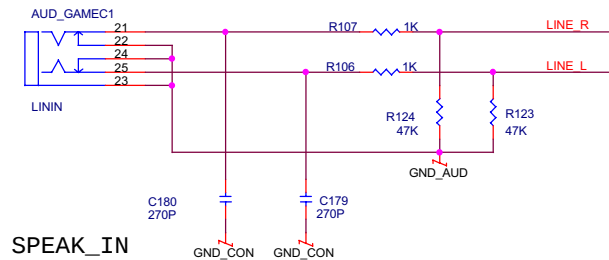


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PRINTER / COM PORT		
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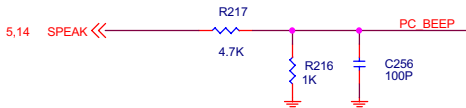
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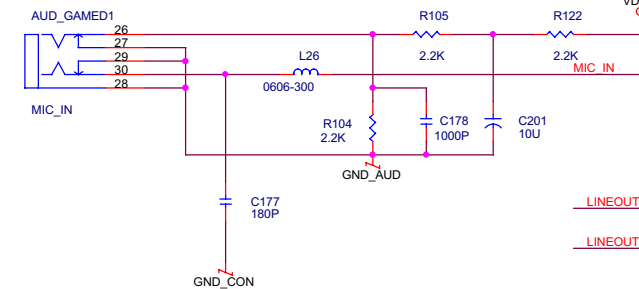
LINE_IN



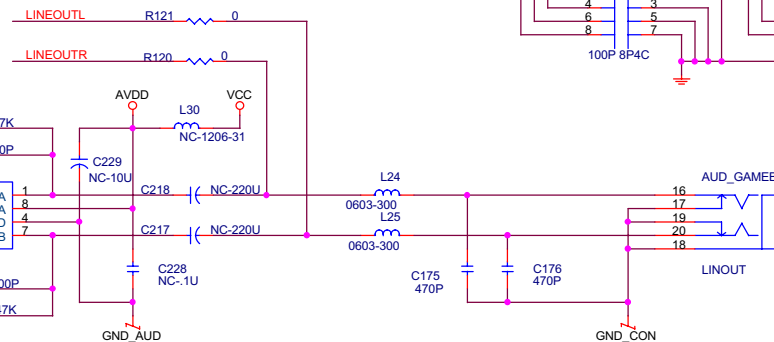
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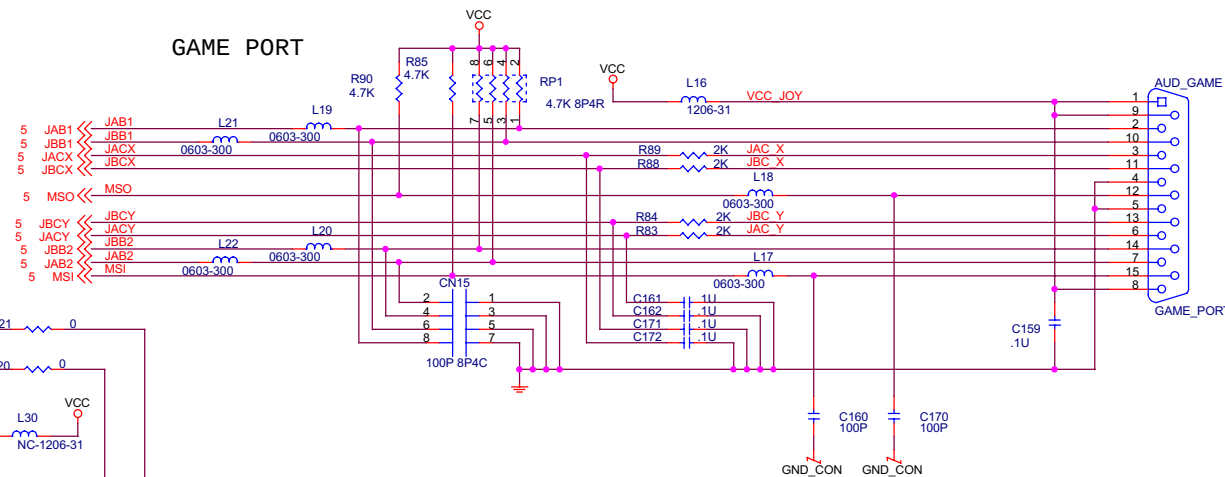
MIC_IN



LINE_OUT

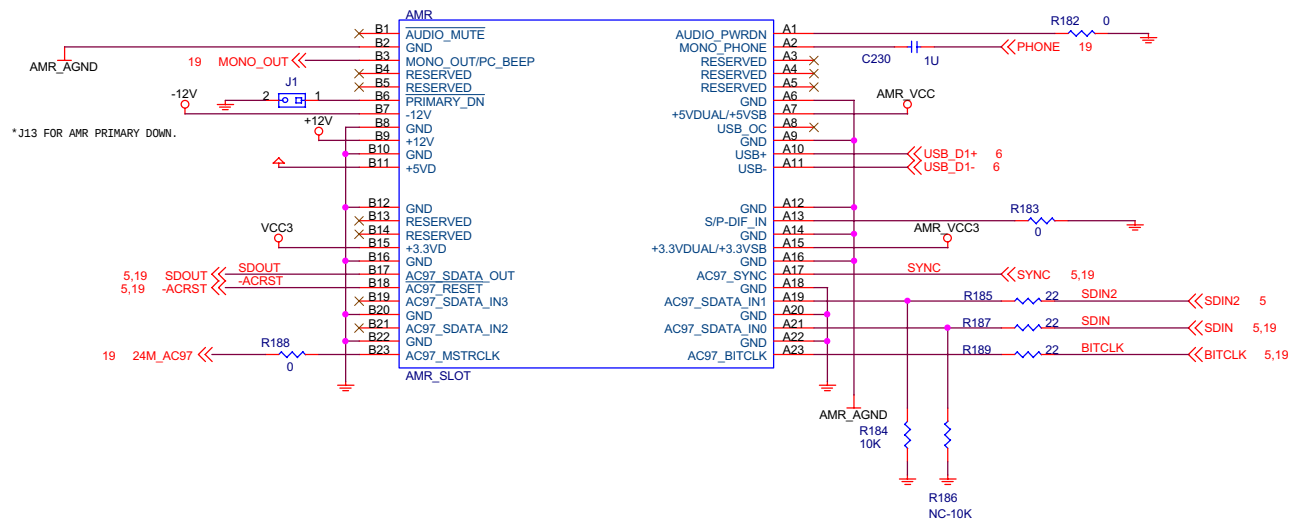


GAME PORT



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ARM SLOT			
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