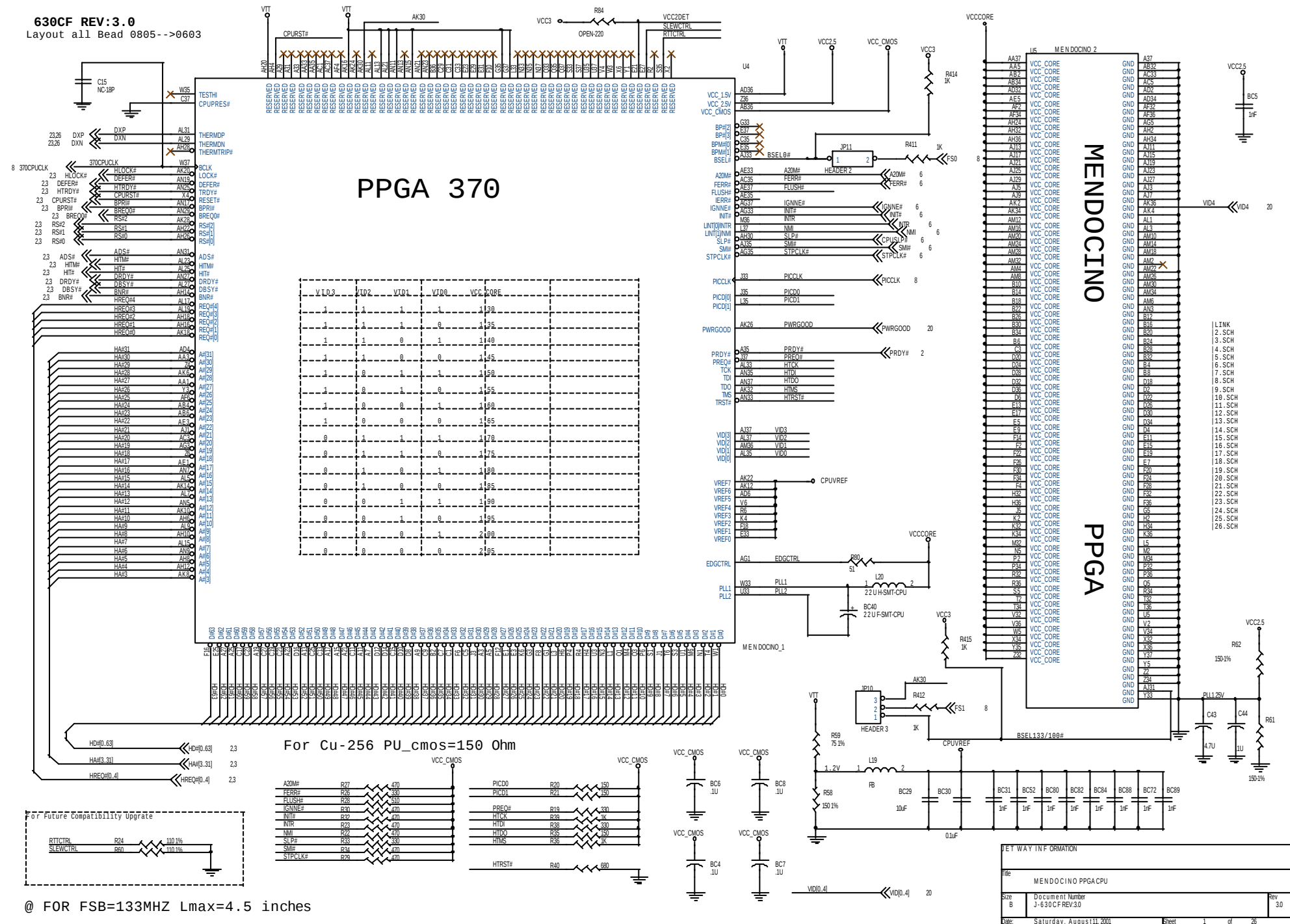
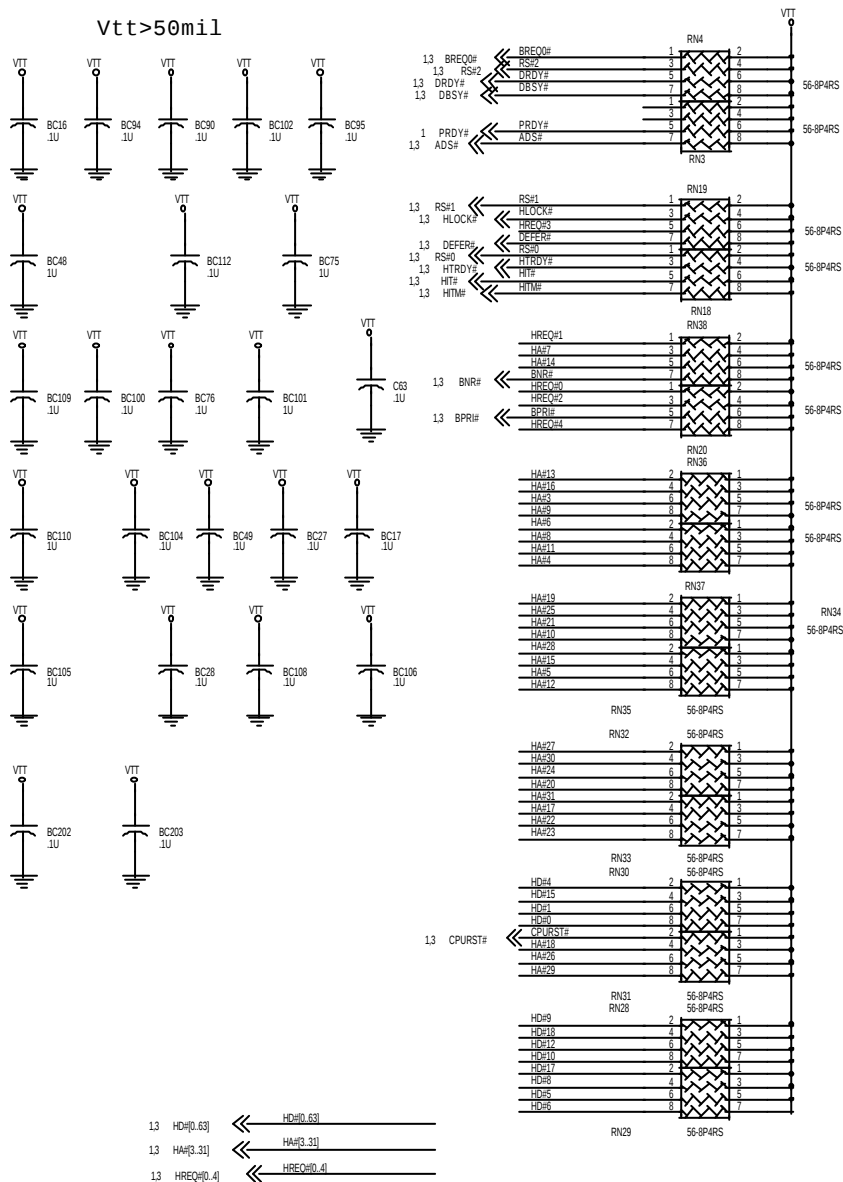
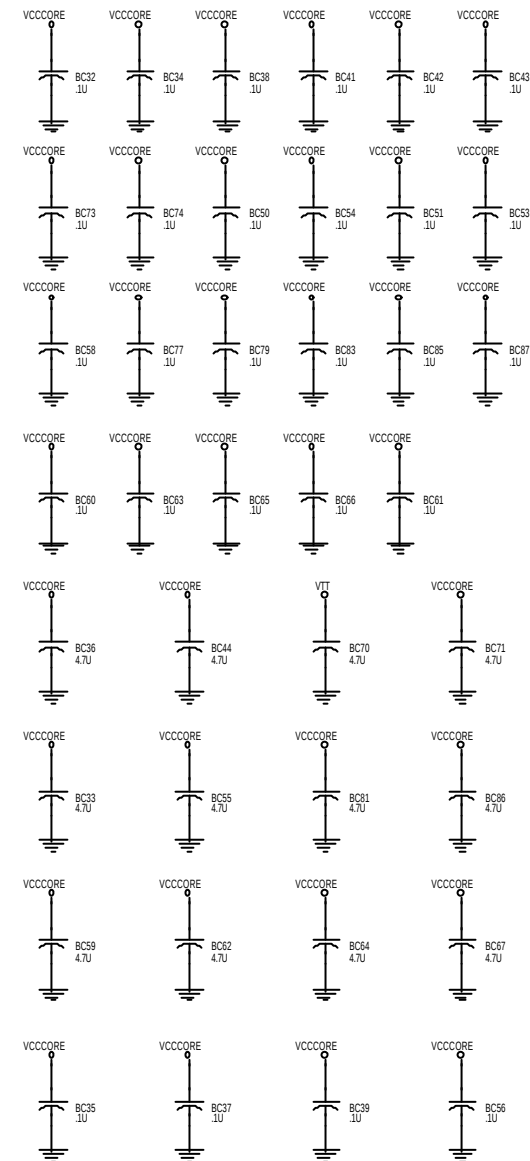
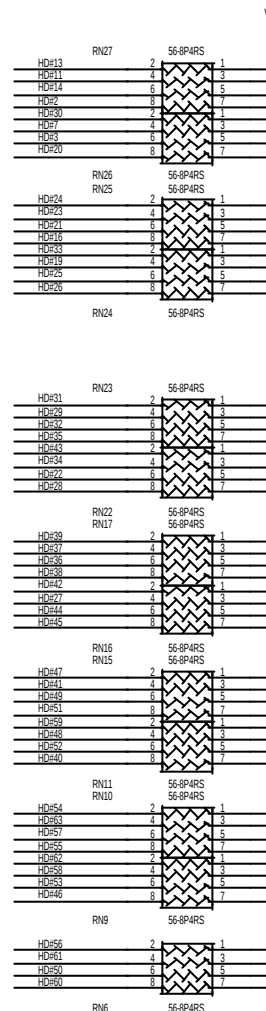




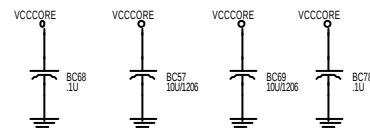
Vtt>50mil



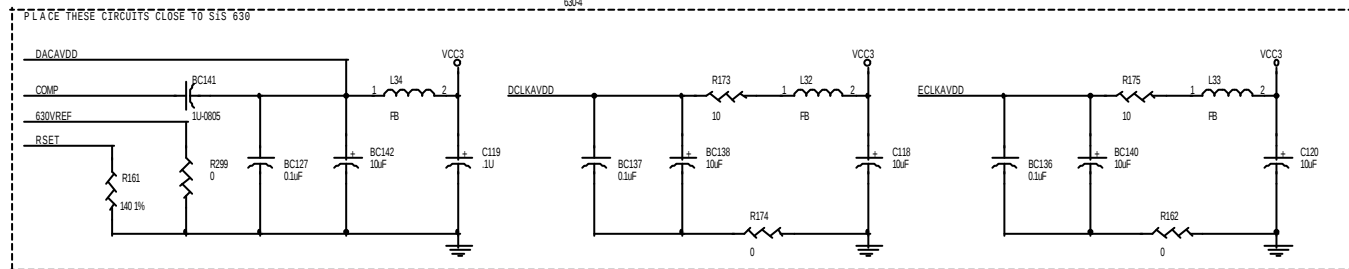
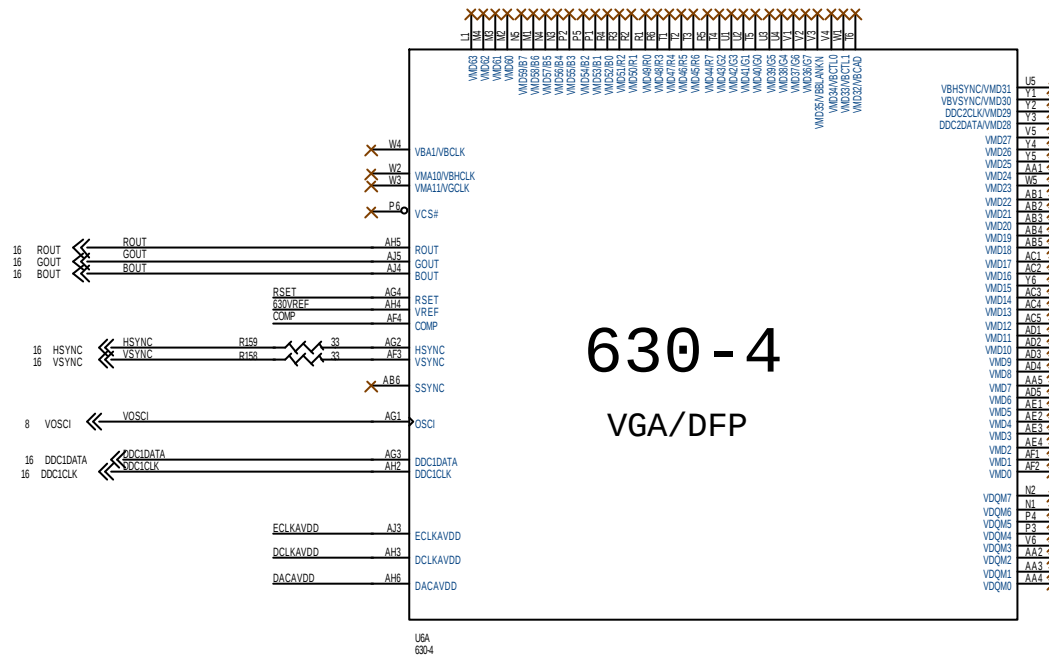
SCOKET 370 RT



NOTE: GTL+ TERMINATION RESISTORS ONLY FOR MENDOCINO PPGA PROCESSOR.

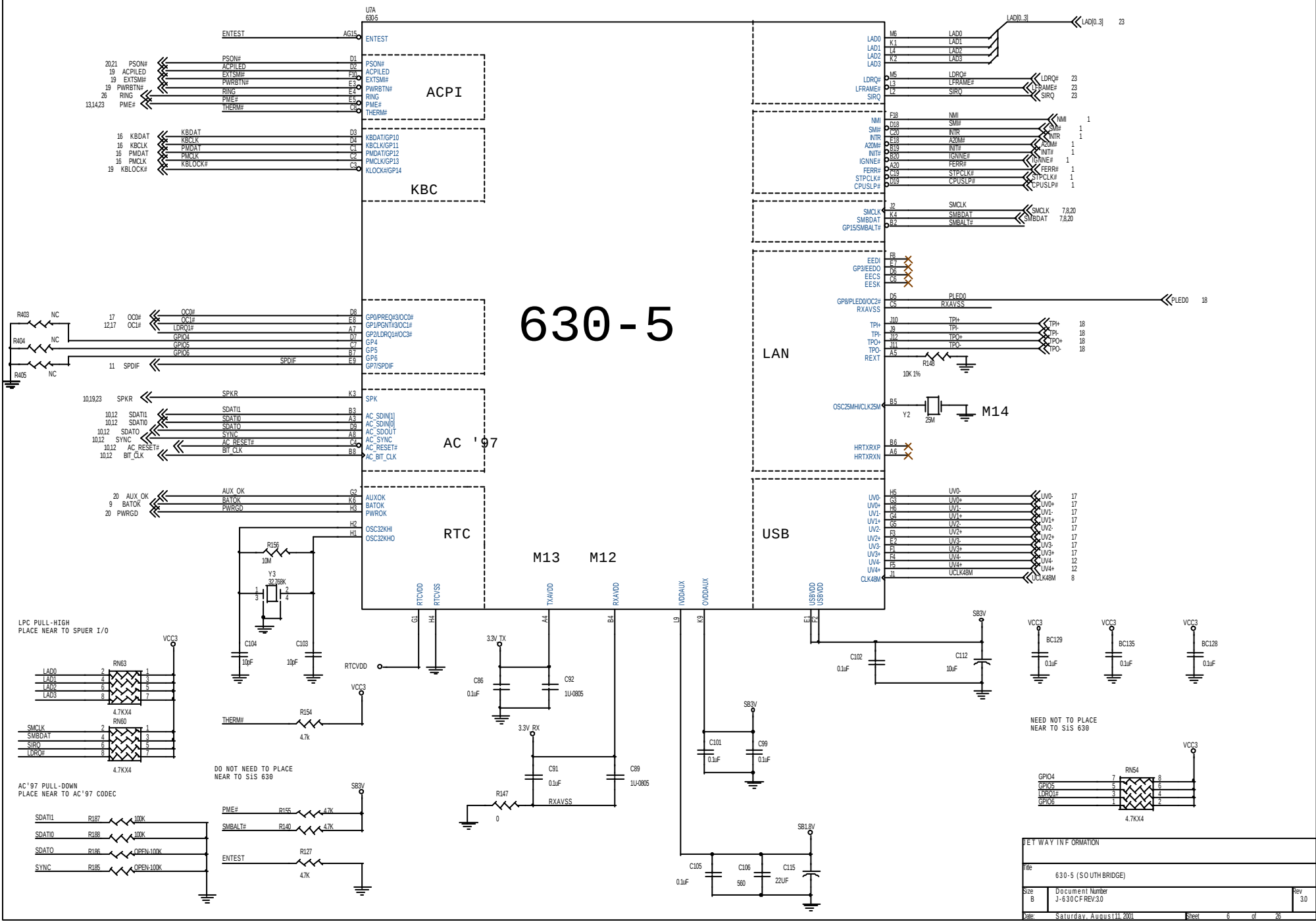


JET WAY INFORMATION			
File: GTL+ TERMINATION R RESISTORS			
Sub B	Document Number J-630CF-REV.3.0	Rev 3.0	
Date: Saturday, August 11, 2001	Sheet 2	of 26	

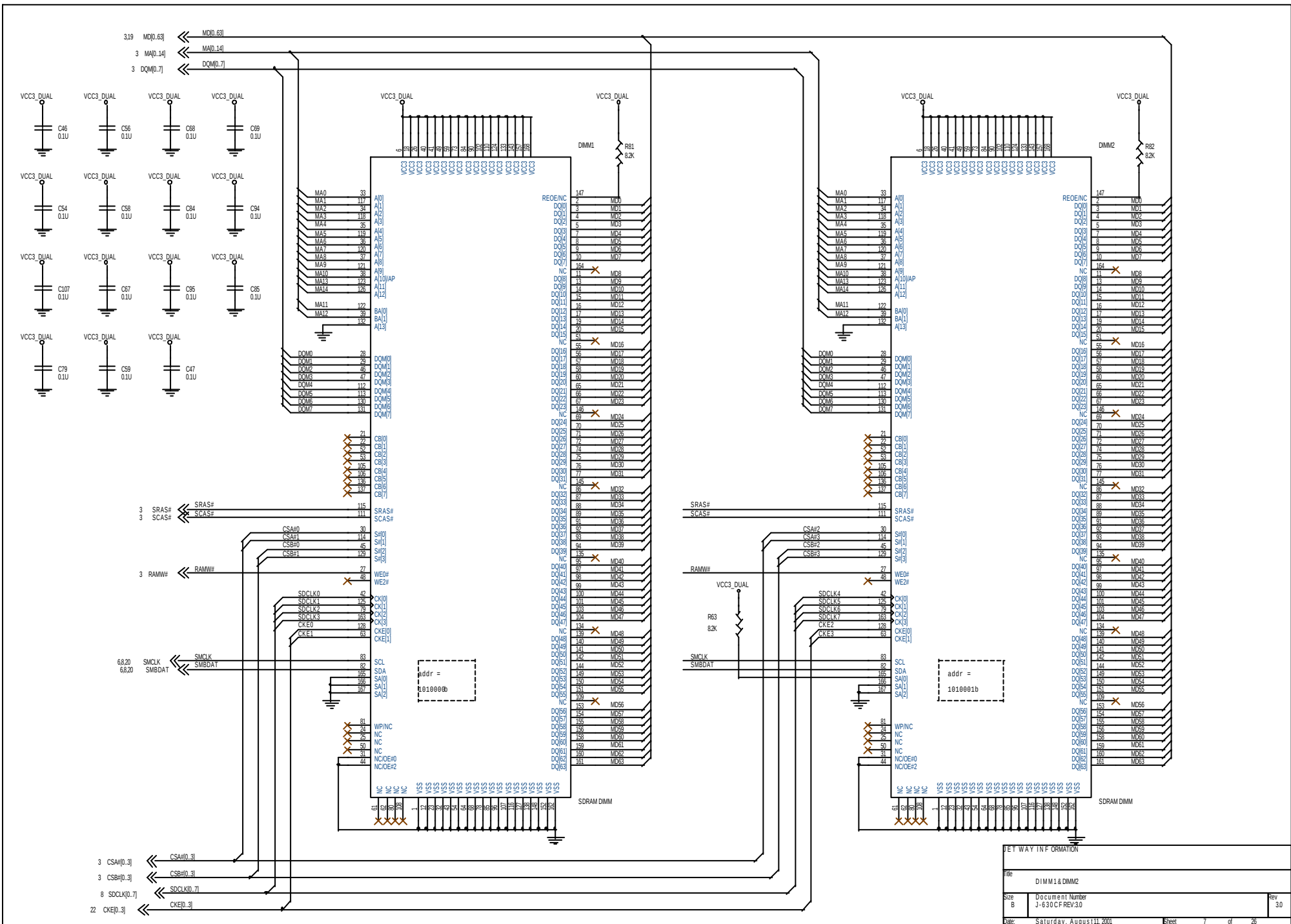


JET WAY INFORMATION			
File	630-4 (VGA/DFP)		
Size	Document Number	Rev	
B	J-630CF-REV.3.0	3.0	
Date	Saturday, August 11, 2001	Sheet	5 of 26

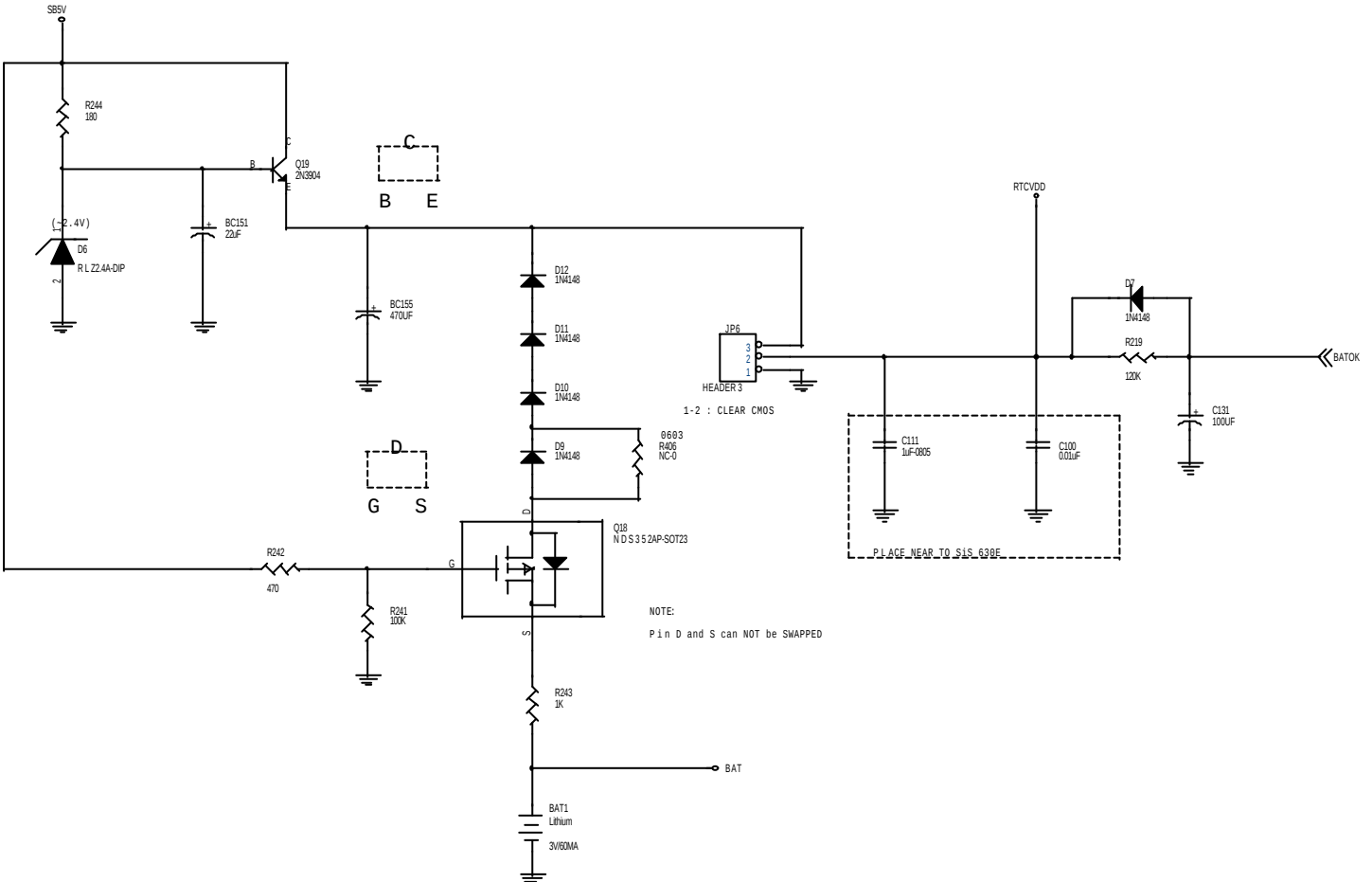
630-5



JET WAY INFORMATION			
File	630-5 (SOUTH BRIDGE)		
Size	Document Number		
B	J-630CF REV.3.0		
Date	Saturday, August 11, 2001	Sheet	6 of 26



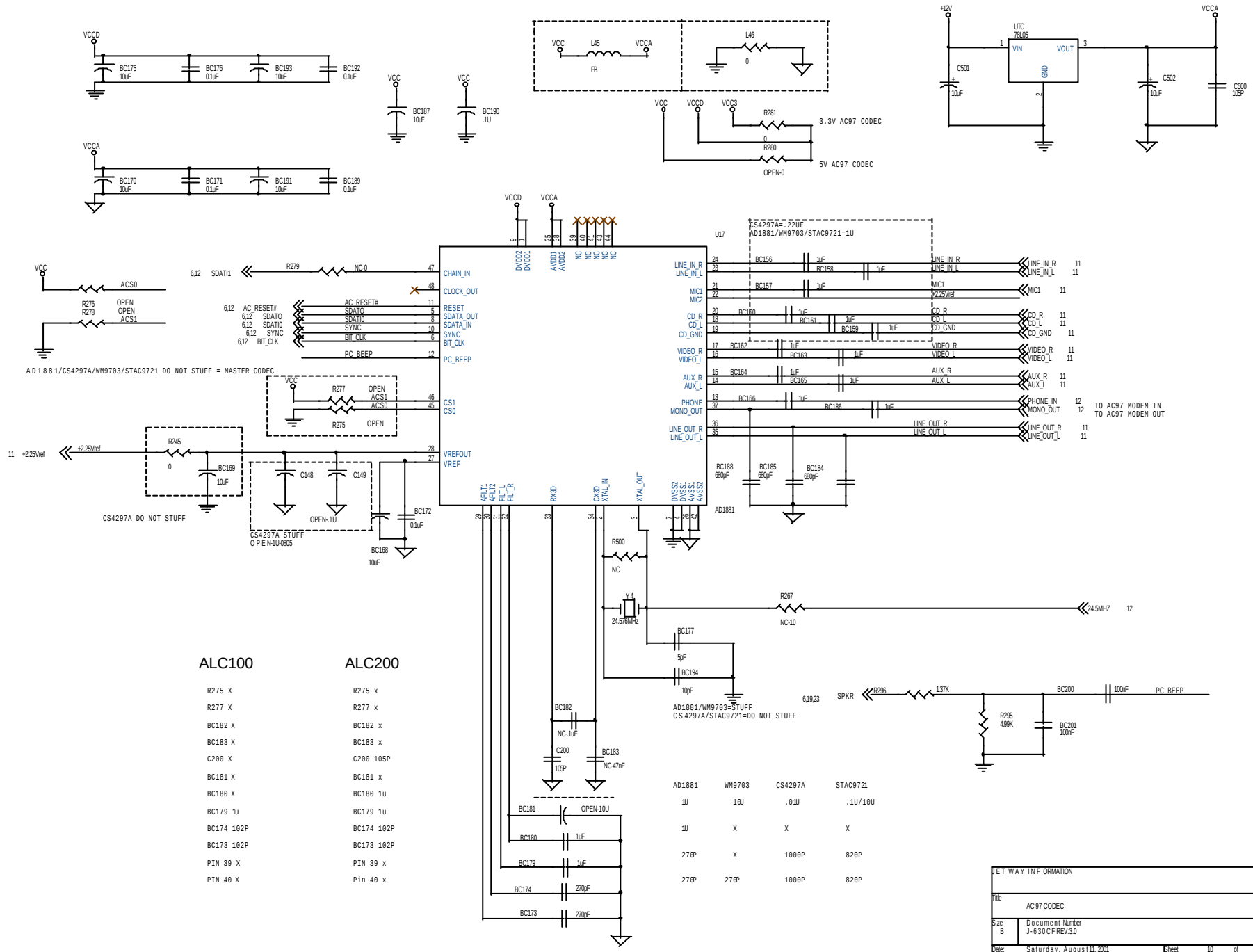
INT. RTC
MUST CLOSE TO CHIP RTCVDD PIN

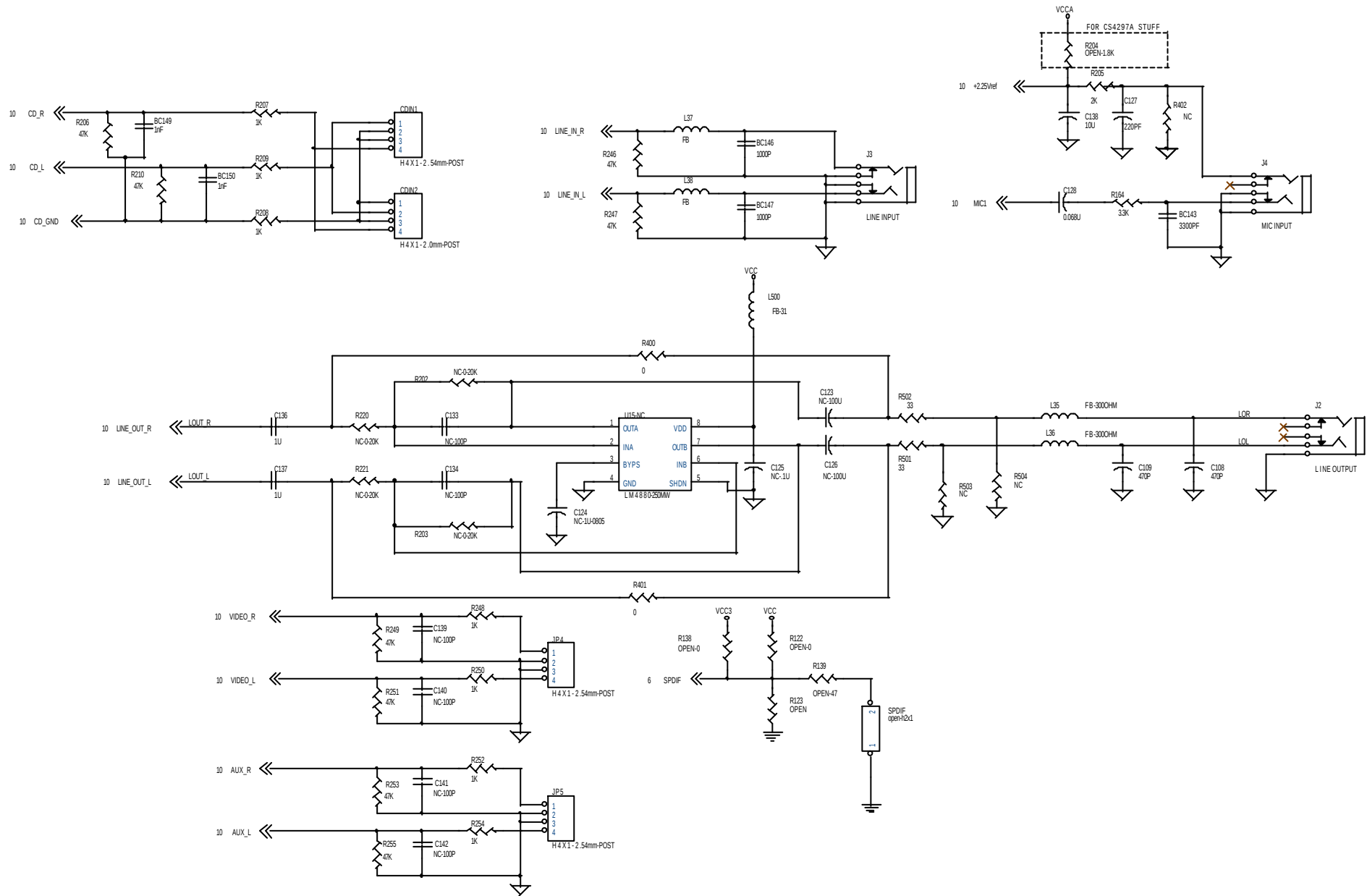


NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

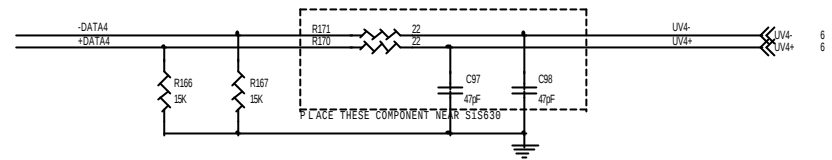
DETAILED WAY INFORMATION				
File RTIC				
Size B	Document Number J-630CF REV3.0			Rev 3.0
Notes	Revisions	Approved	By	Date

ALC100/ALC200/AD1881/WM9703/CS4297A/STAC9721

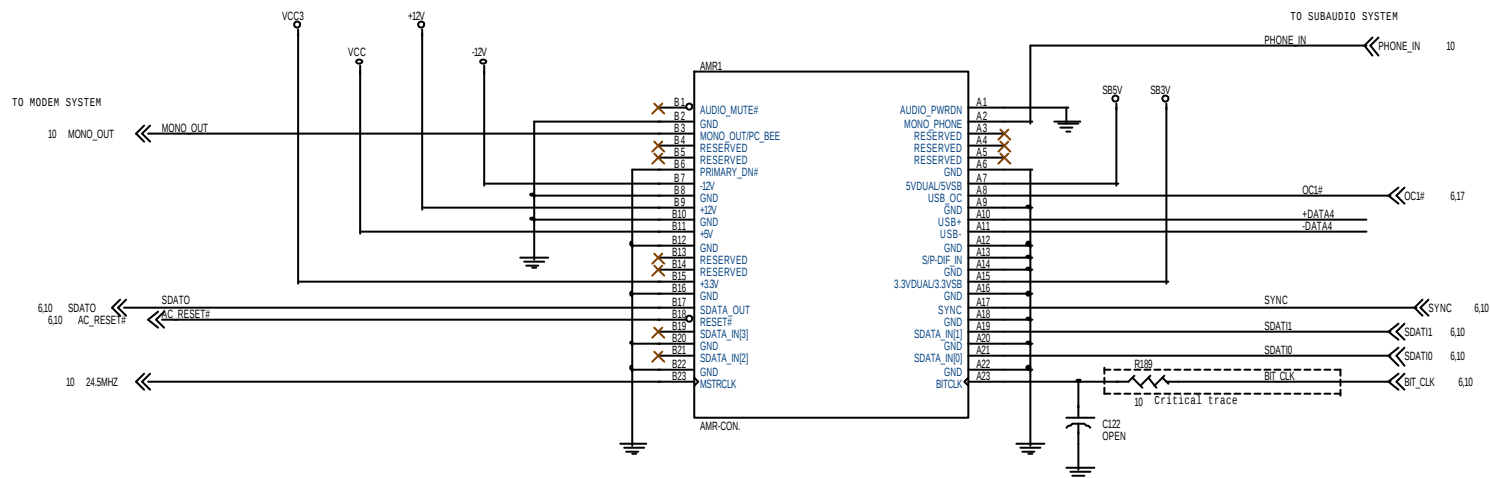




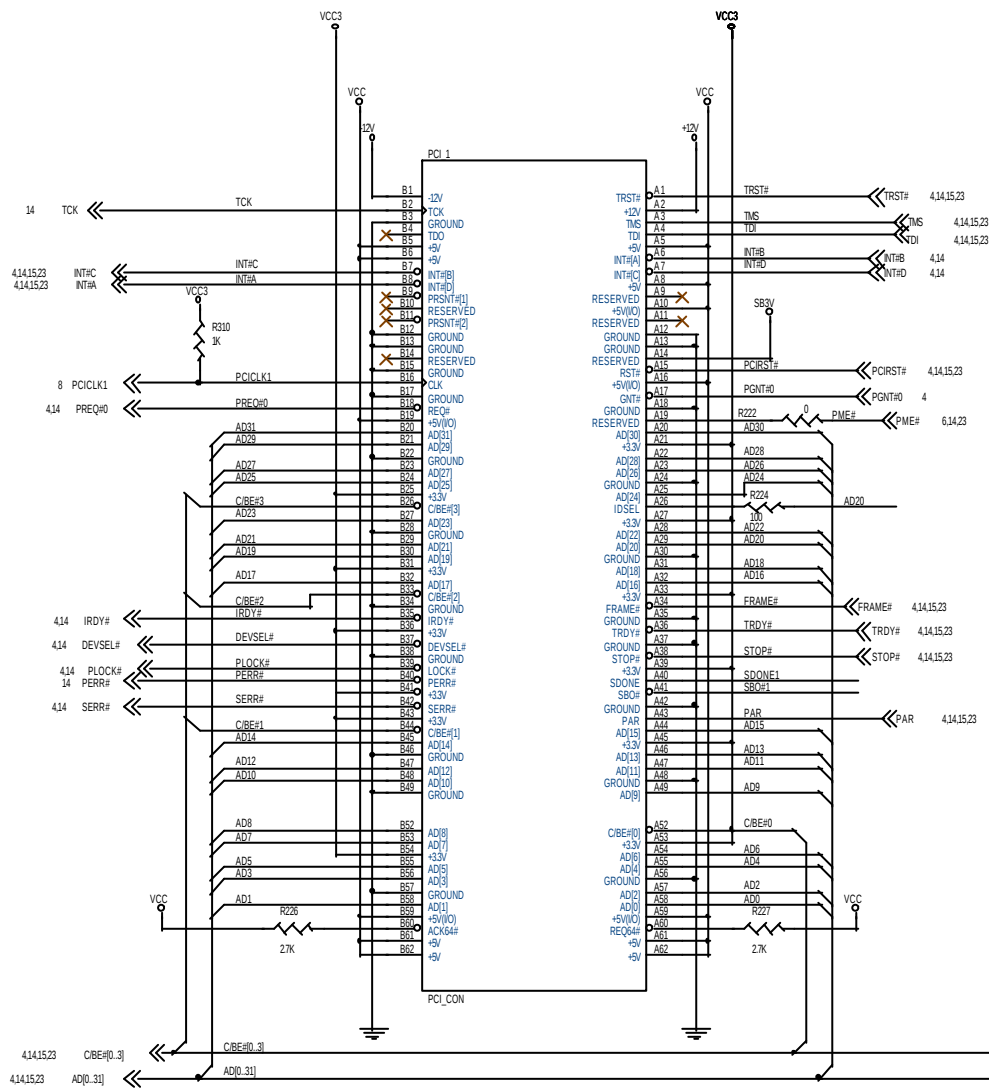
JET WAY INFORMATION			
File	AUDIO ANALOGUE IN/OUT		
Size	Document Number	Rev	
B	J-630CF-REV.3.0	3.0	
Date	Saturday, August 11, 2001	Sheet	11 of 26

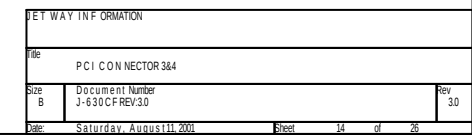
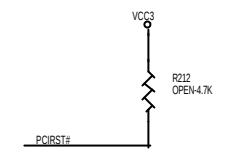


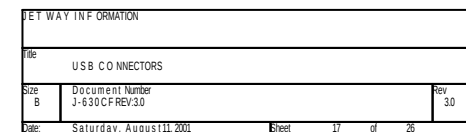
AMR

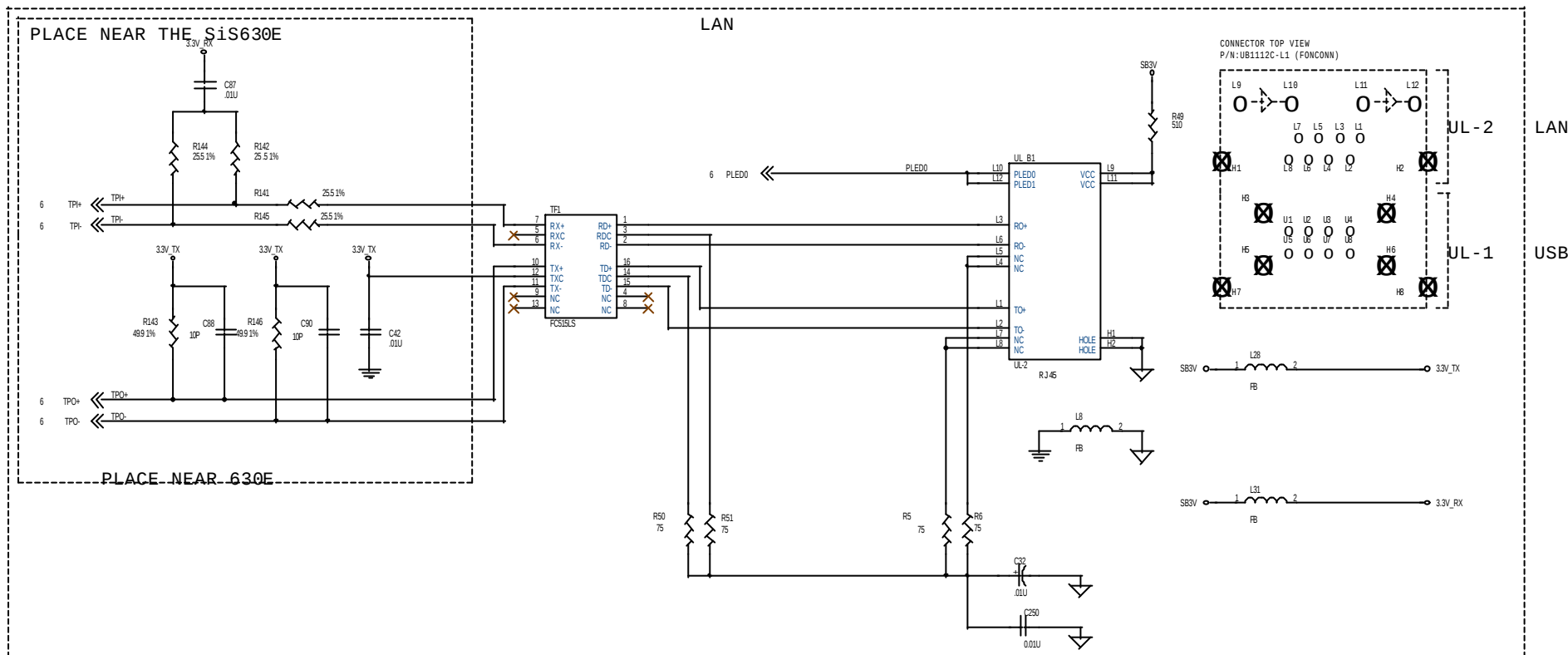


JET WAY INFORMATION			
File	AUDIO/MODEM RISER		
Size	Document Number		Rev
B	J-630CF-REV.3.0		3.0
Date	Saturday, August 11, 2001	Sheet	12 of 26





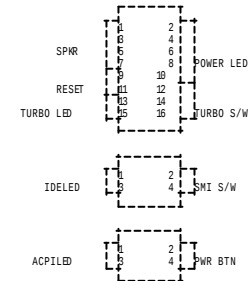
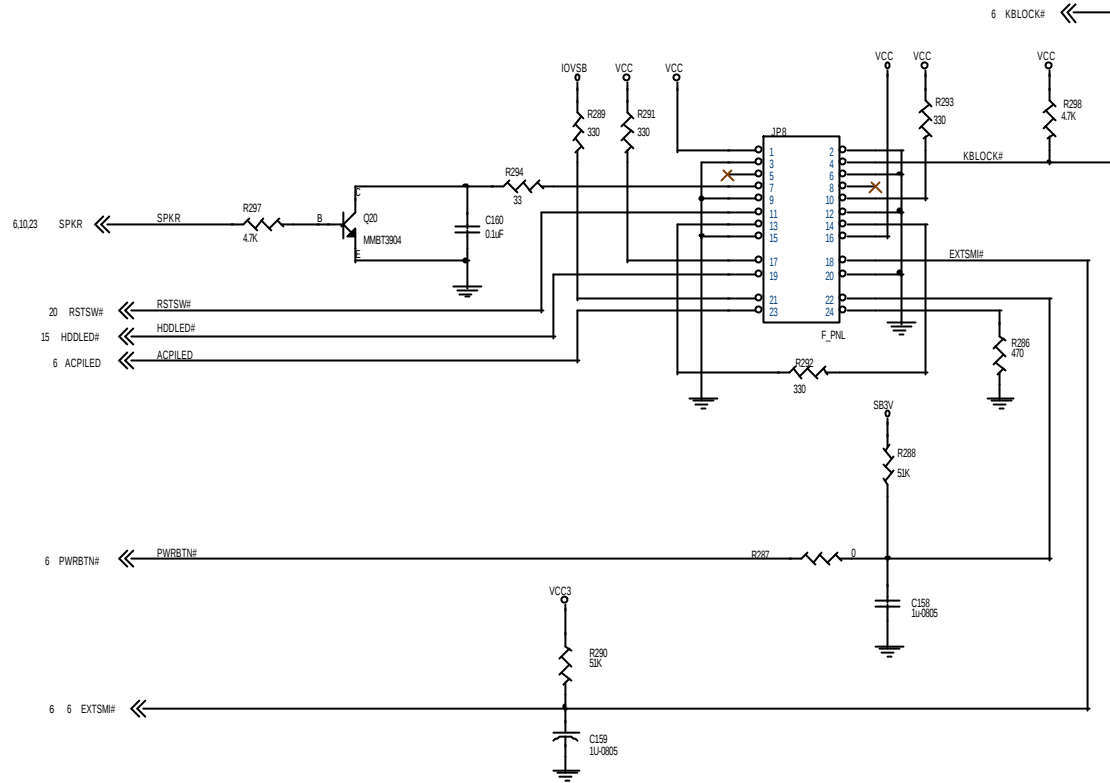
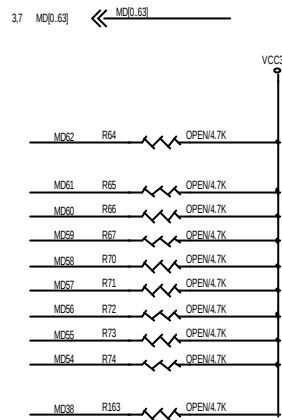




NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION			
File:	NETWORK OPTION1		
Size:	Document Number		Rev
B	J-630CFREV.30		3.0
Date:	Saturday, August11, 2001	Sheet	18 of 26

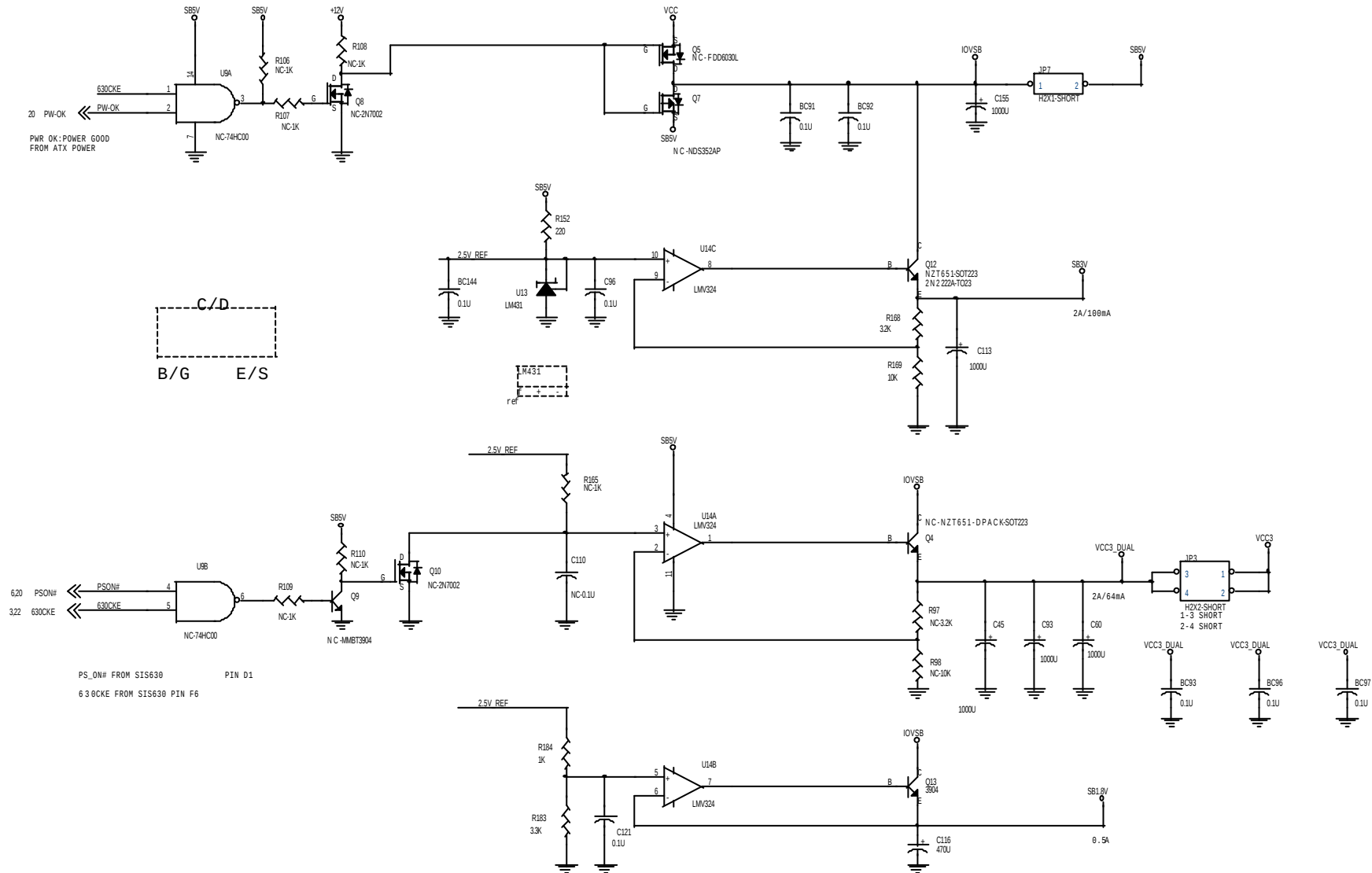
MD62: PCI Clock PLL Enable
MD61: SDRAM Clock DLL Enable
MD60: CPU Clock DLL Enable
MD[59..58]: SDRAM Clock DLL'S DRC[1..0]
(Default 00)
MD[57..56]: CPU Clock DLL'S DRC[1..0]
(Default 00)
MD[55..54]: PCI Clock DLL'S DRC[1..0]
(Default 00)
MD32: 0=NTSC / 1=PAL
MD38: Enable INTERRUPT



NOTE:
SIS IS NOT RESPONSIBLE FOR
ANY ERRORS OR OMISSIONS IN
THESE SCHEMATICS. THIS IS
AN EXAMPLE ONLY.

JET WAY INFORMATION			
File: DATA ACQUISITION & JUMPERS			
Size: B	Document Number: J-630CF-REV.3.0	Rev: 3.0	
Date: Saturday, August 11, 2001	Sheet: 19	of 26	

RC5058+Discrete ACPI FOR SIS630 - PAGE 2

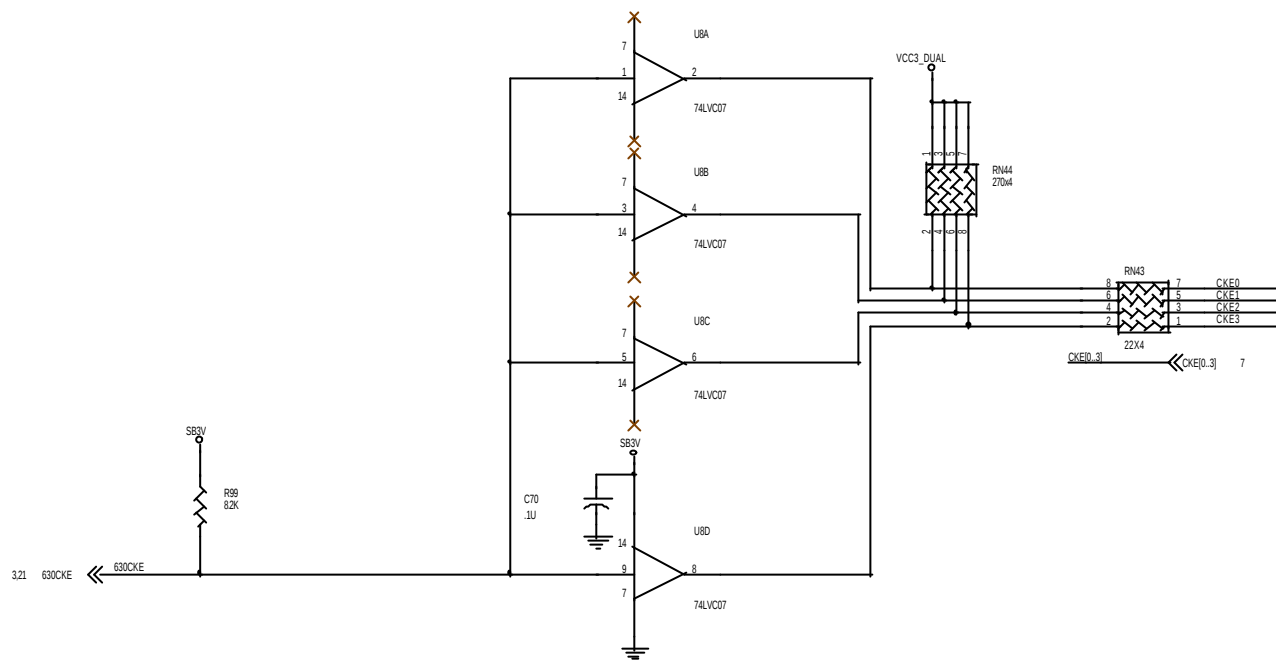


JET WAY INFORMATION			
Title DISCRETE ACPI FOR S IS630-2			
Size B	Document Number J-630CFREV3.0	Rev 3.0	
Date	Saturday, August 11, 2001	Sheet 21	of 26

VCC and VCC3 Standby VOLTAGE SWITCH

WHEN IN S0,S1
THIS CIRCUIT PASSES THE NORMAL POWER

WHEN IN S3,S4,S5
THIS CIRCUIT PASSES THE STANDBY POWER



JET WAY IN FORMION				
Title VOLTAGE SWITCH FOR SUSPEN DTODRAM				
Size B	Document Number J-630CFREV.30			Rev 3
Date	Saturday, August 11, 2001	Sheet	22	of 26

W83697HF

(To monitor battery voltage, this input should be connected directly to battery)

Temperature Sensing

Voltage SENSING

Fan Speed Seneing & Speed Control

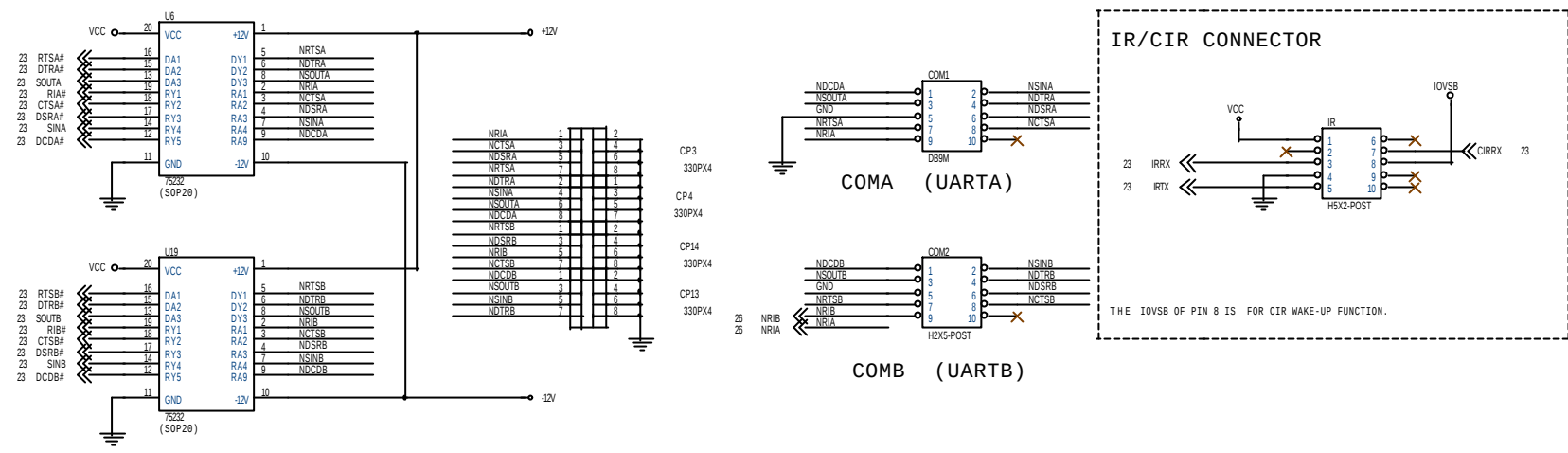
MIDI PORT

GAME PORT

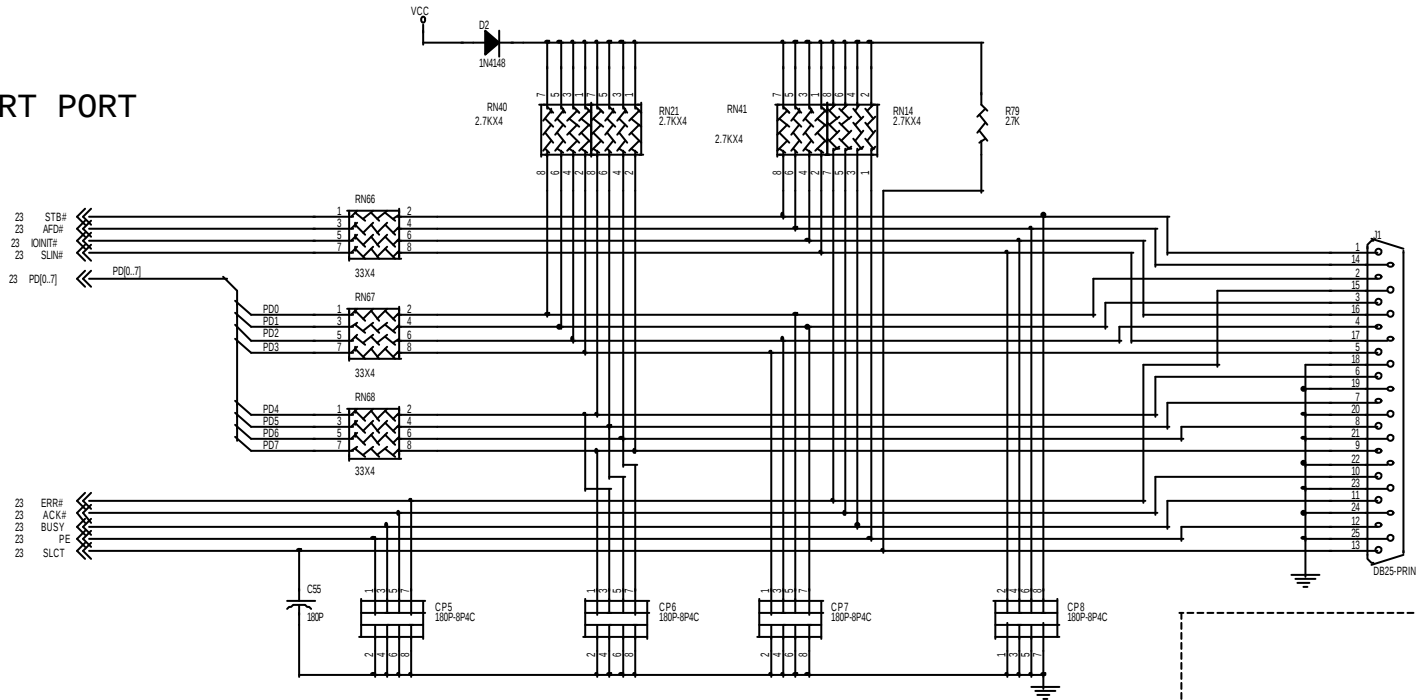
FDC

JET WAY INFORMATION		
File	W83697HF (LPC I/O + H/W + FLASH ROM)	
Size	Document Number	Rev
B	J-630CFREV.30	3.0
Date	Saturday, August 11, 2001	Sheet 23 of 26

COM PORT



PRT PORT



GET WAY INFORMATION		
File	W83697HF (LPC I/O + H/W + FLASH RO MIP)	
Size	Document Number	Rev
B	J-630CF-REV.3.0	3.0
Date	Saturday, August 11, 2001	Sheet 24 of 26

FLASH ROM

SELECT 2M-FLASH ROM, UNINSTALL R258
SELECT 4M-FLASH ROM, INSTALL R258

VCC

R274
0

R258
OPEN

XA18

XA0..18

VCC

R261 4.7K

R260 4.7K

R259 4.7K

R262 4.7K

MEMW#

MEMR#

ROMCS#

U18

NC/A18

A17

A16

A15

A14

A13

A12

A11

A10

A9

A8

A7

A6

A5

A4

A3

A2

A1

A0

WE#

OE#

CE#

W 29C02040

DQ0

DQ1

DQ2

DQ3

DQ4

DQ5

DQ6

DQ7

XD0

XD1

XD2

XD3

XD4

XD5

XD6

XD7

XD0..7

23

4.7KX4

RN77

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

4.7KX4

RN78

1

2

3

4

5

6

7

8

9

10

11

12

13

14

15

16

17

18

19

20

21

22

23

4.7KX4

C154

1U

VCC

GND

IOVSB CIRCUIT

On Now or Wake_up function power

RX1 OFF, D1,D2 ON: Wake_up fuction

RX1 ON, D1,D2 OFF:NO Wake_up function

IOVSB

VCC

OPEN-5817

SB5V

OPEN-5817

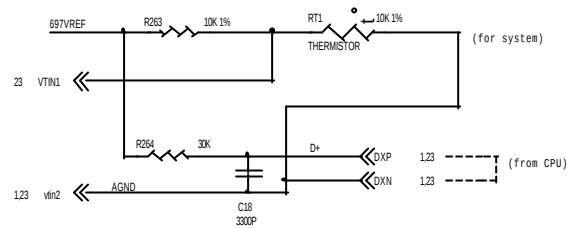
C156
NC10U

REFERENCE TO PAGE 21

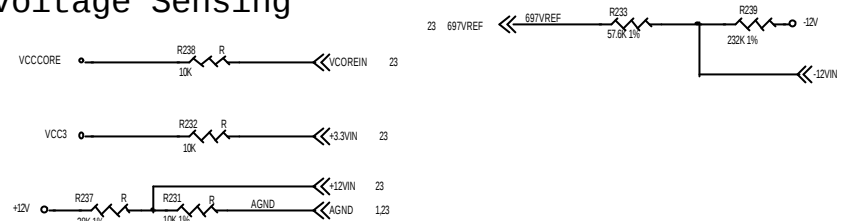
JET WAY INFORMATION			
Title W83697HF (LPC I/O + H/W + FLASH ROM) MF			
Size B	Document Number J-630CFREV30	Rev 30	
Date	Saturday, August 11, 2001	Sheet 25	of 26

Hardware Monitor circuits

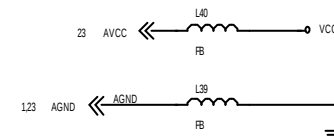
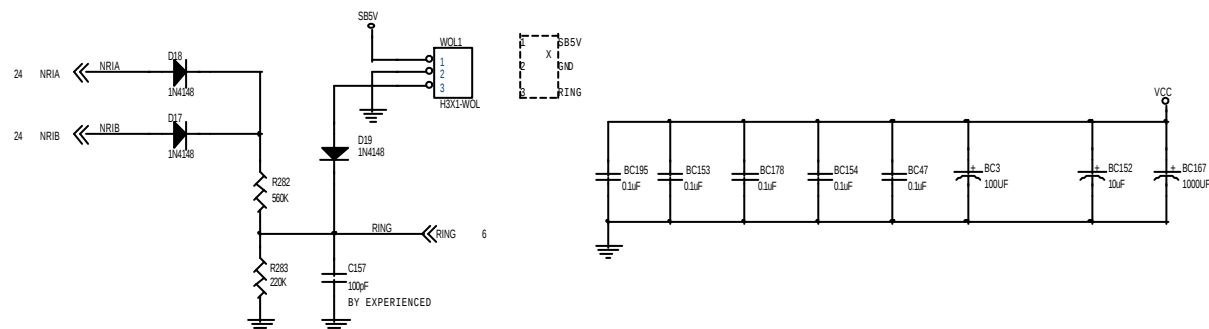
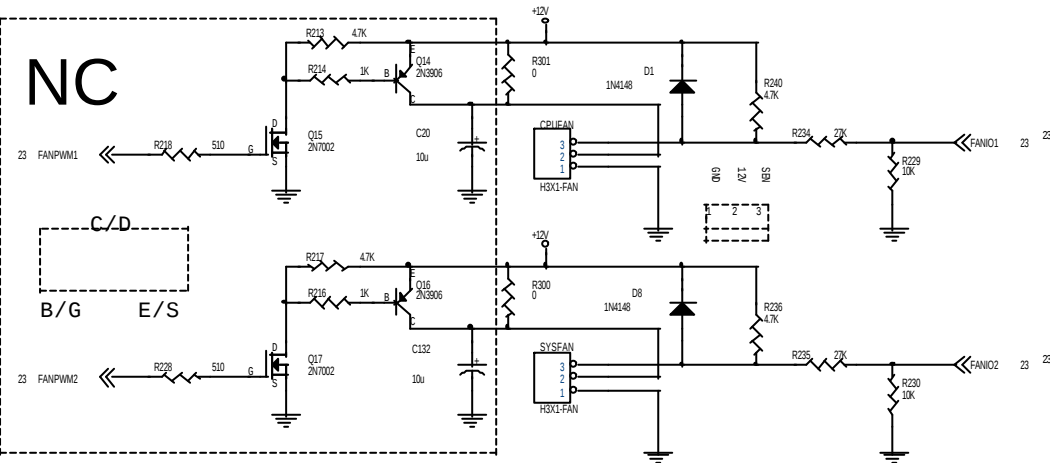
Temperature Sensing



Voltage Sensing



PWM Circuit for FAN speed control



JET WAY INFORMATION			
Title W83697HF (LPC I/O + H/W + FLASH RO MIF)			
Size B	Document Number J-630CF REV.30	Rev 30	
Date	Saturday, August 11, 2001	Sheet	26 of 26