

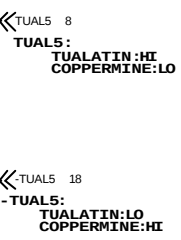
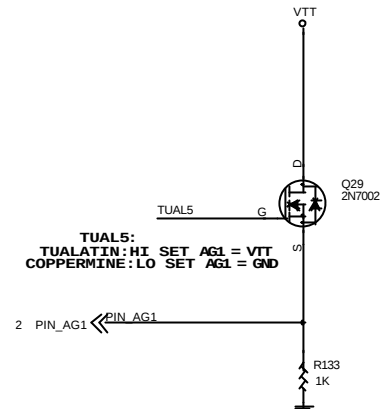
VIA Apollo ProMedia Board Schematics

601TCF

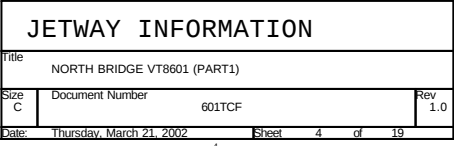
TITLE	SHEET No.
COVER SHEET	1
SOCKET 370 PROCESSOR	2, 3
NORTH BRIDGE VT8601/A	4, 5
SOUTH BRIDGE VT82C686A/B	6, 7
CLOCK SYNTHESIZER	8
AGTL+ BUS AND PULL UP RESISTORS	9
SDRAM DIMM SLOTS 1/2	10
PCI SLOT1 & PCI SLOT2	11
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IDE CONNECTORS & WAKE UP CIRCUITRY	14
FRONT PANEL & BACK PANEL CONNECTOR (USB 0/1)	15
FAN CONTROL CIRCUITRY & VGA CONNECTOR	16
AC'97 AUDIO CODEC & AUDIO PORTS	17
DC-DC CONVERTERS	18
ATX POWER CONNECTORS & BYPASS CAPACITORS	19

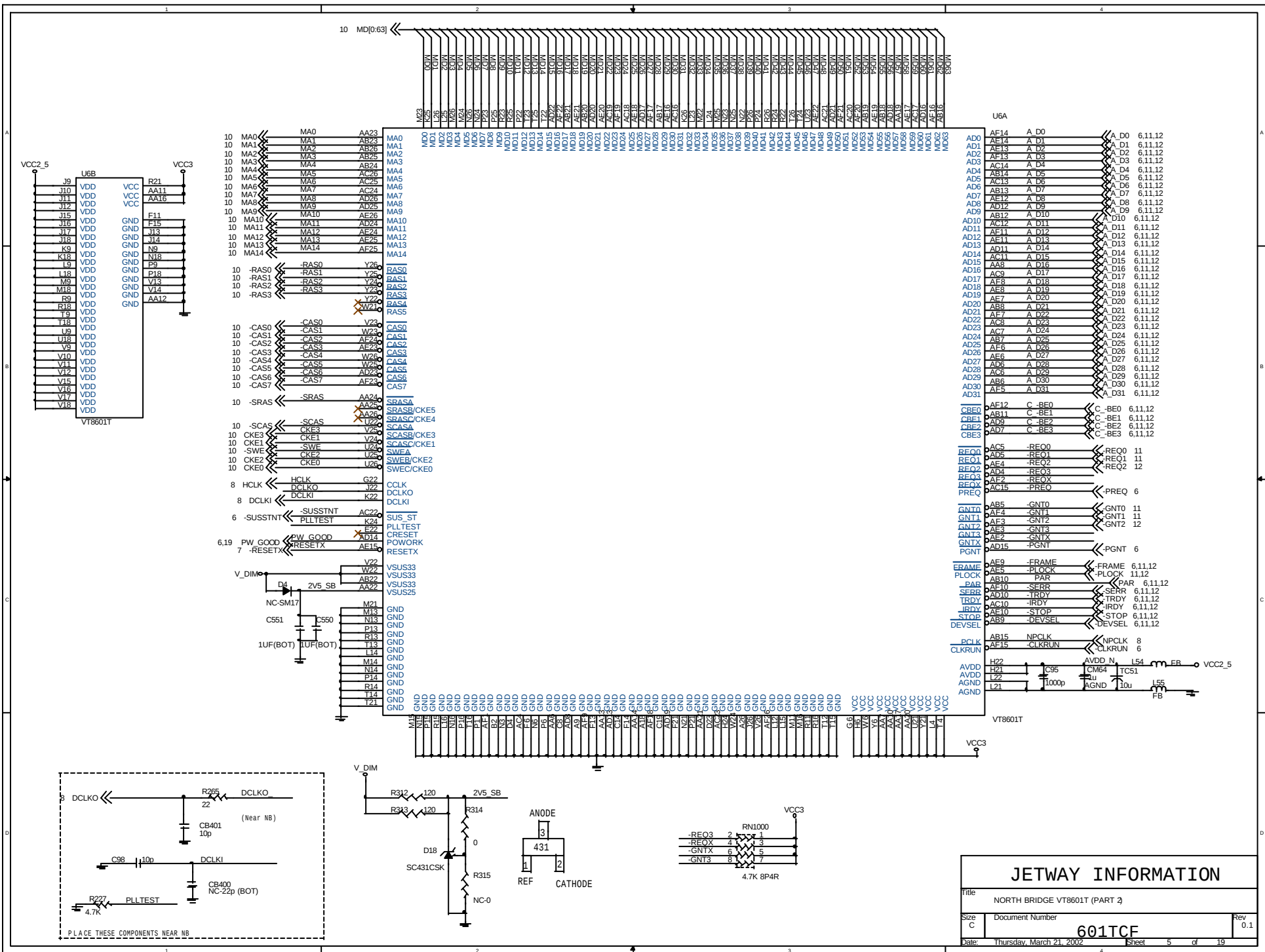
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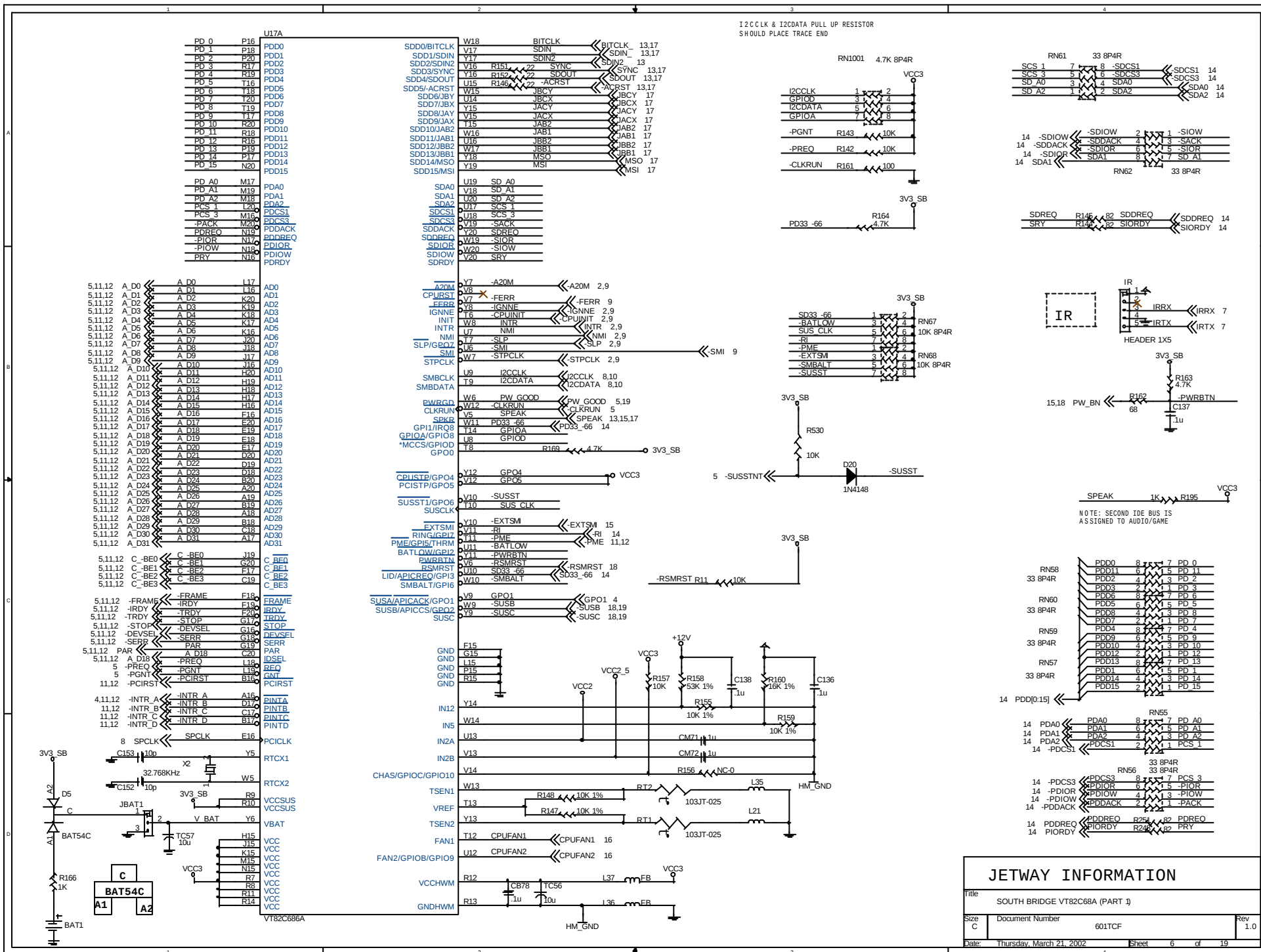
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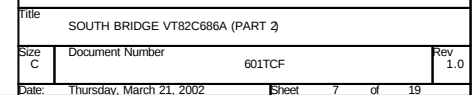


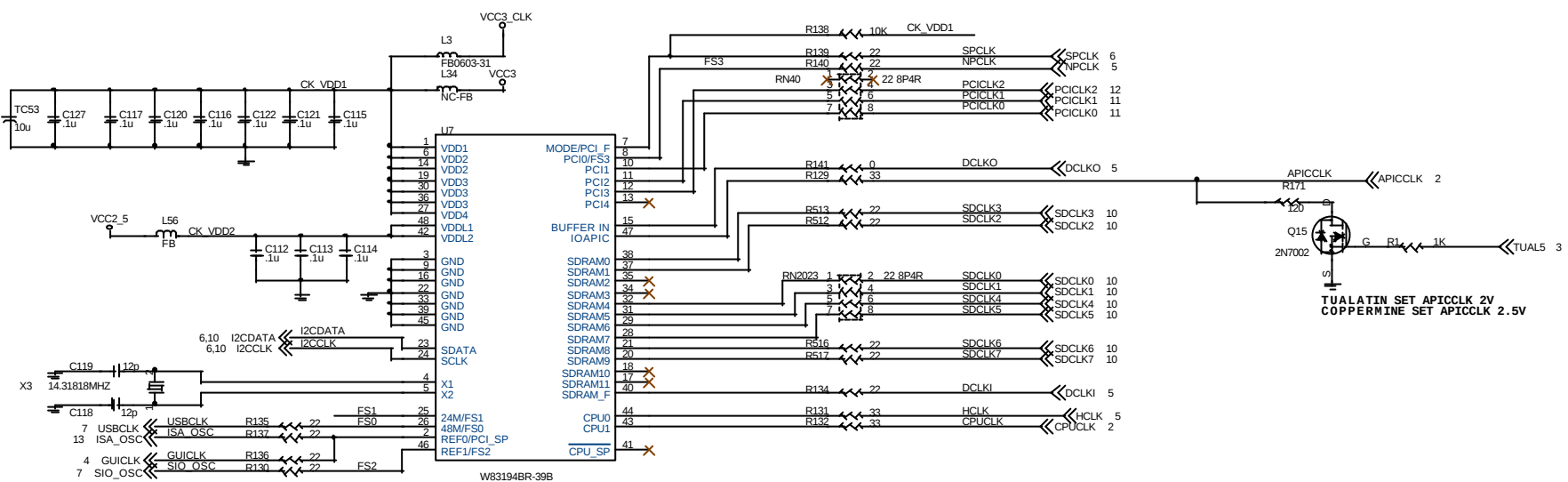
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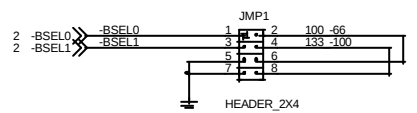
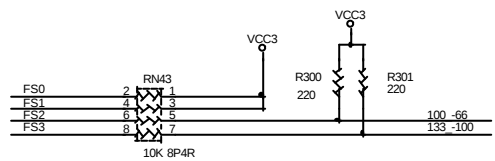






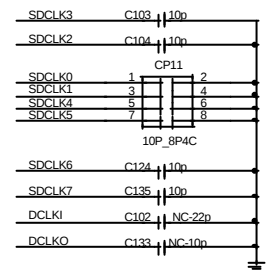
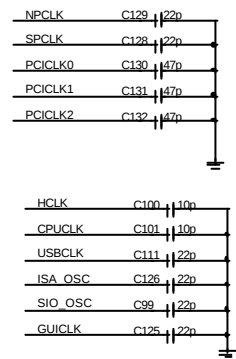


APICCLK
R171
Q15
2N7002
TUALATIN SET APICCLK 2V
COPPERMINE SET APICCLK 2.5V

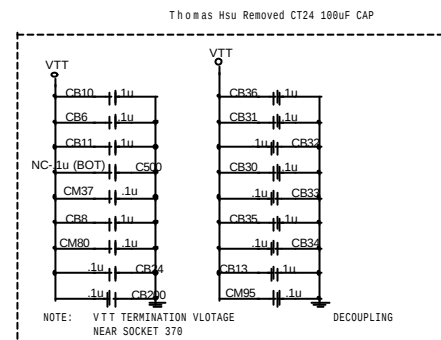
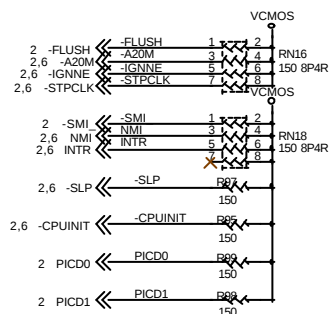
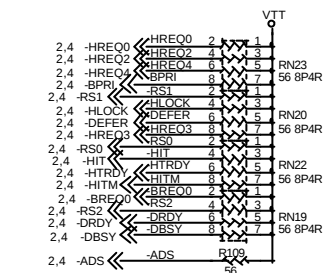
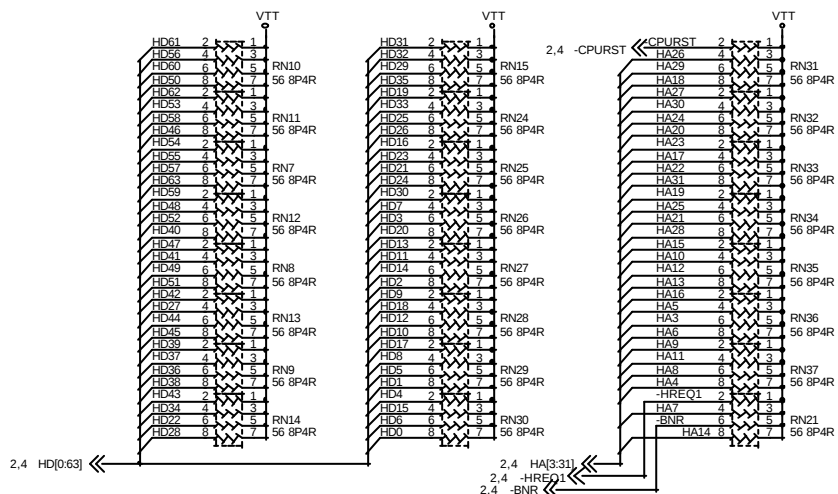
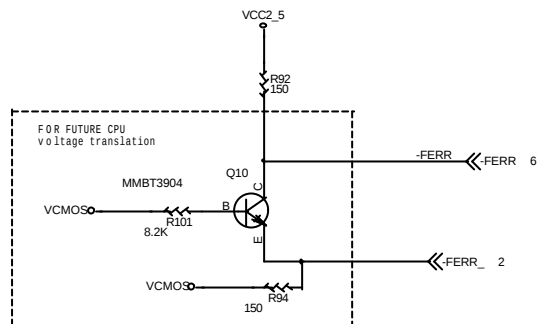


JMP1

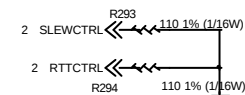
7-8	5-6	3-4	1-2	CPU
OFF	OFF	ON	ON	AUTO
ON	ON	OFF	OFF	66
ON	OFF	OFF	OFF	100
OFF	OFF	OFF	OFF	133



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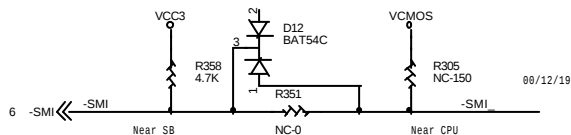
Thomas Removed for detecting ratio by CPU

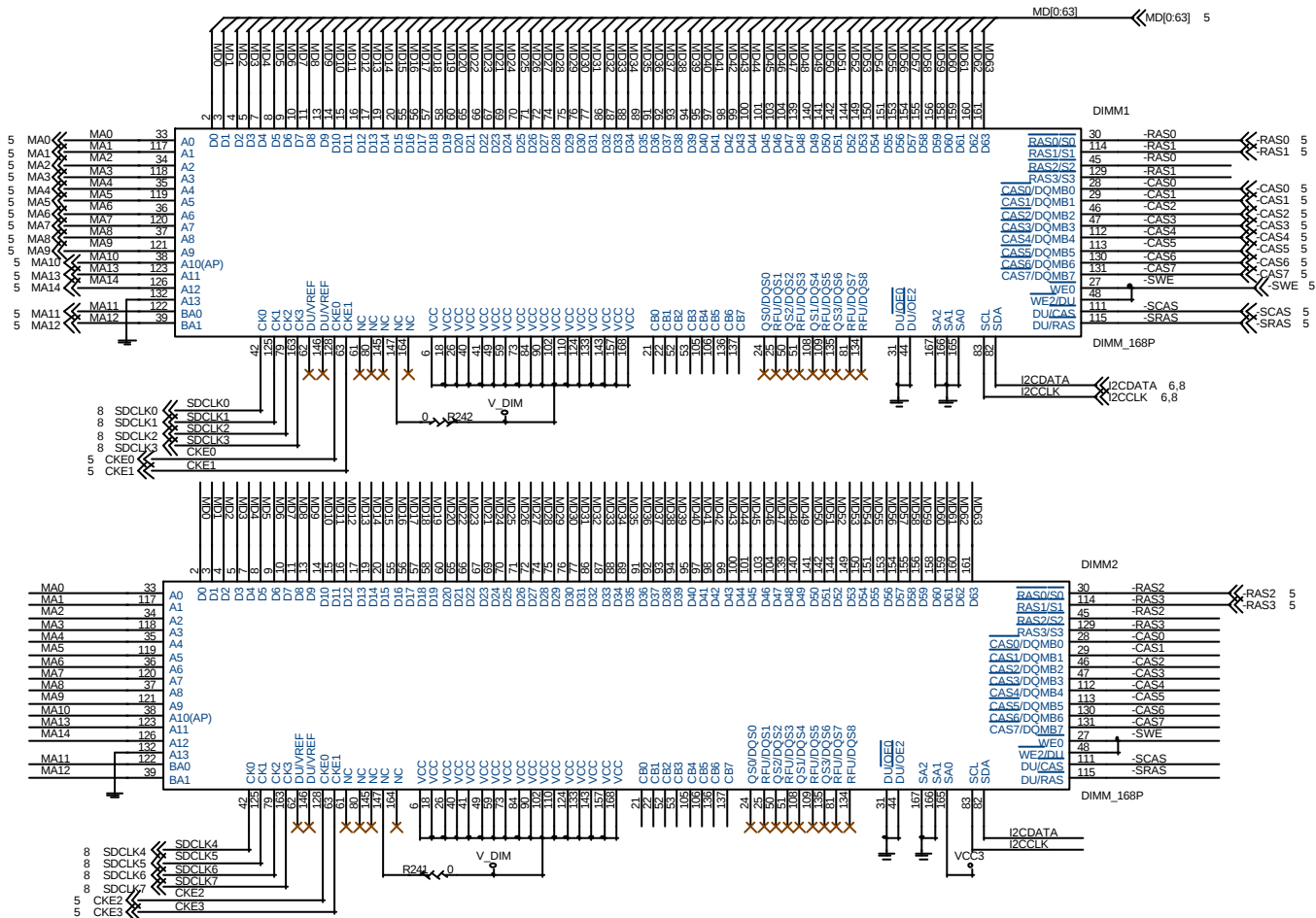


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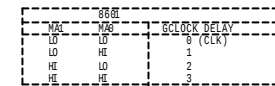
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AGTL+ PULL UP RESISTORS		
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Thomas modified for detecting Ratio by CPU

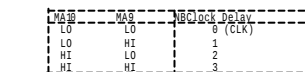
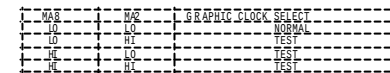
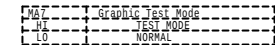




MA POWER-UP STRAPPING OPTIONS



MA6:LO DISABLE LCD FUNCTION

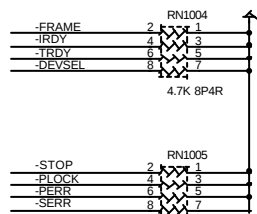
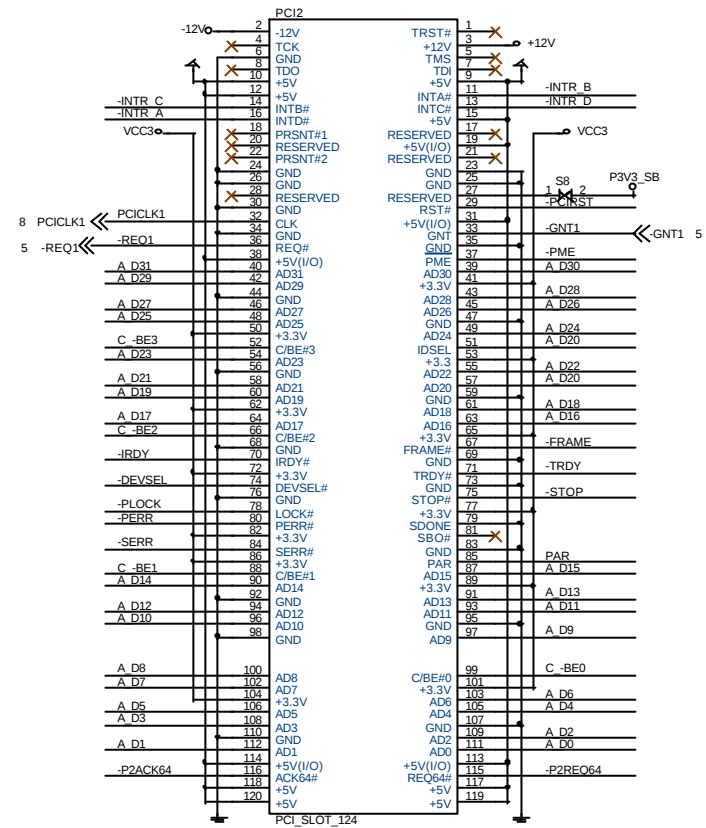
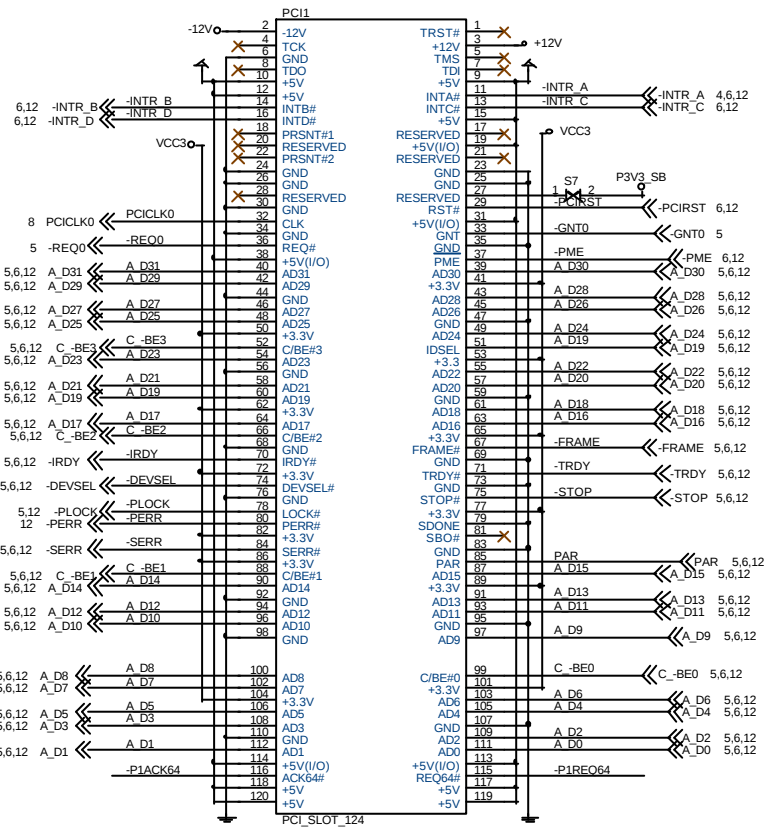


CPU FREQ FIX AUTO MODE

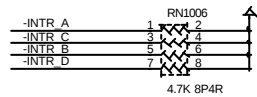
Thomas

JETWAY INFORMATION

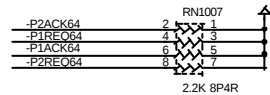
Title			SDRAM DIMM SLOTS
Size	Document Number	601TCF	Rev
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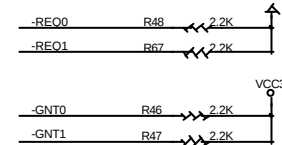
Thomas Hsu



Thomas Hsu

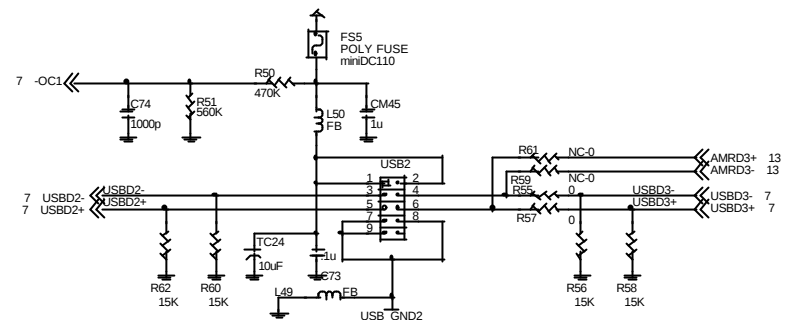


Thomas Hsu



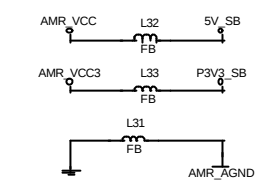
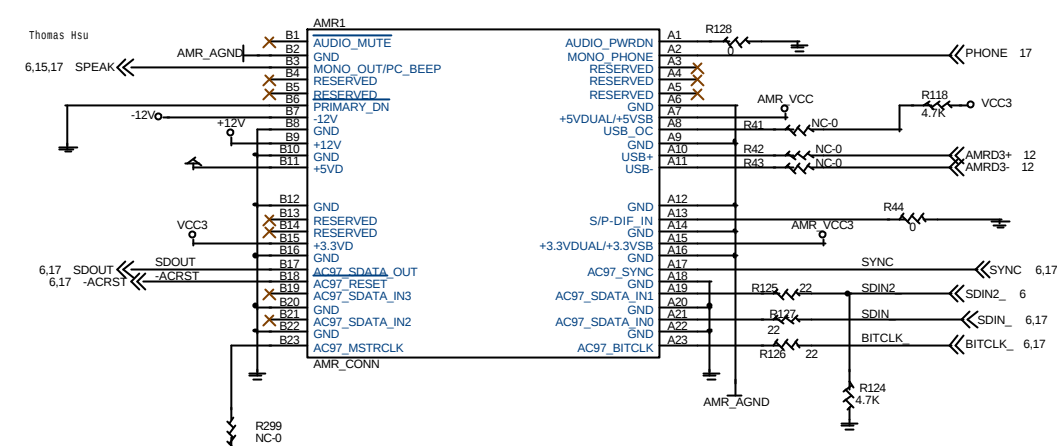
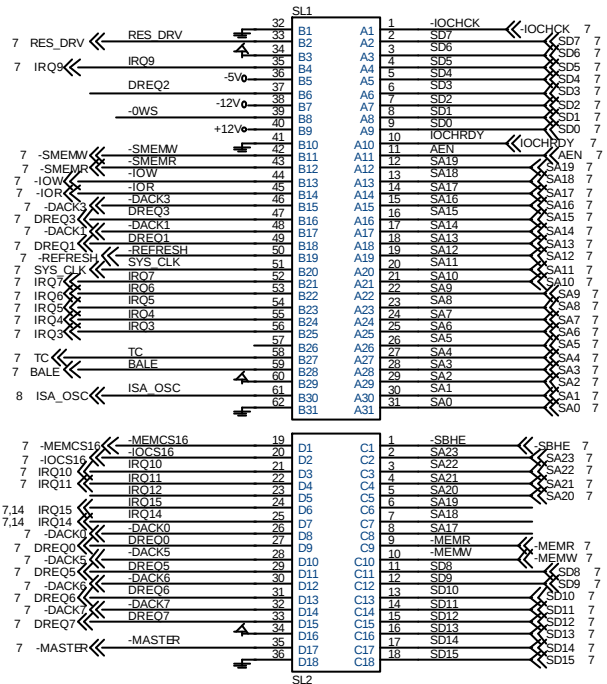
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Title PCI1 & PCI2 SLOTS		
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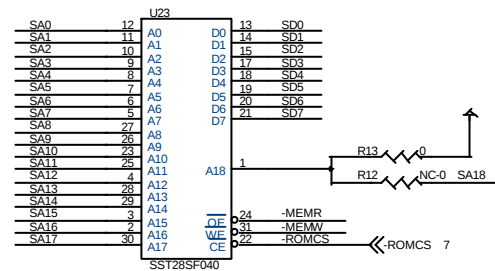


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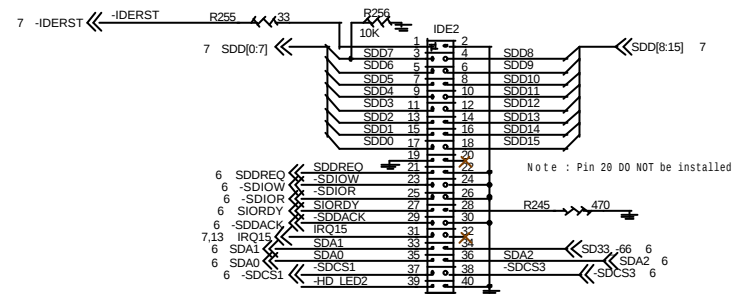
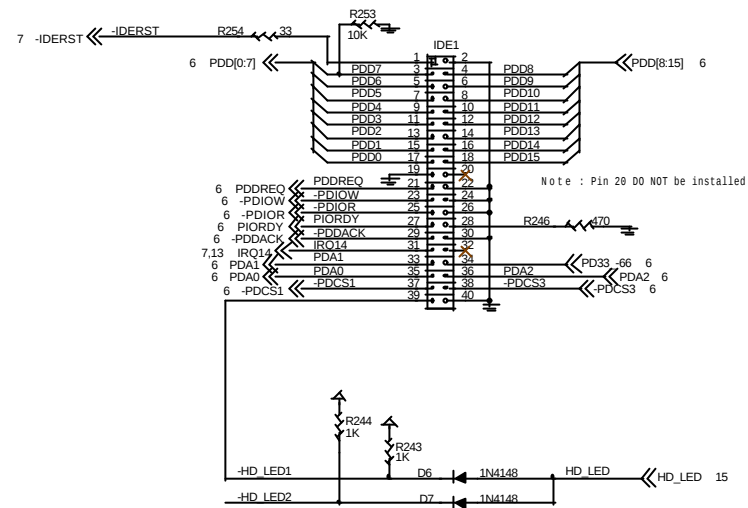
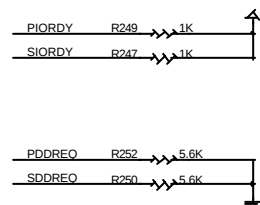
Title			
PCI3 & USB3/USB4			
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Thomas Hsu Removed XD SD connectors

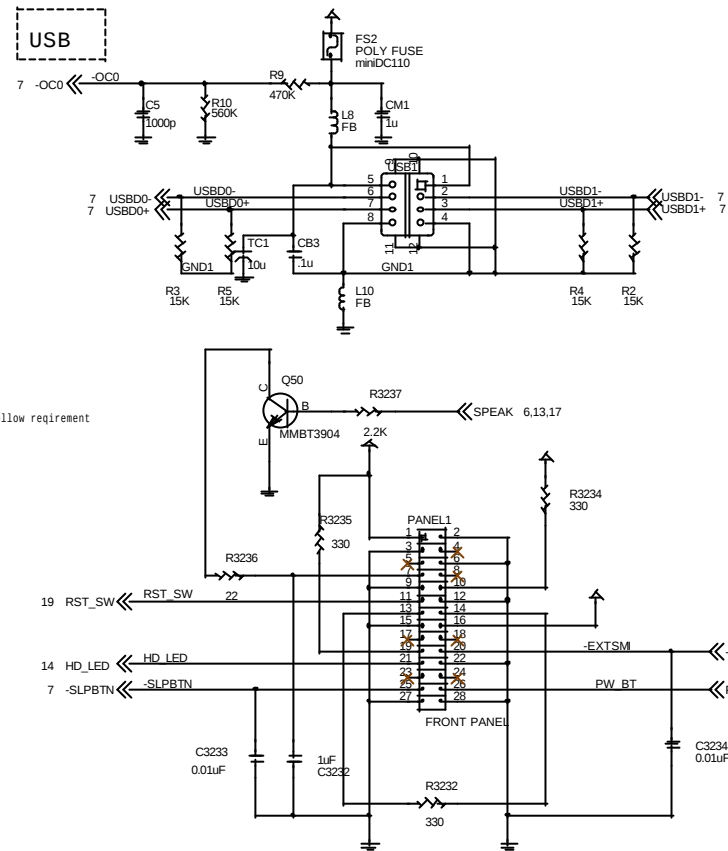
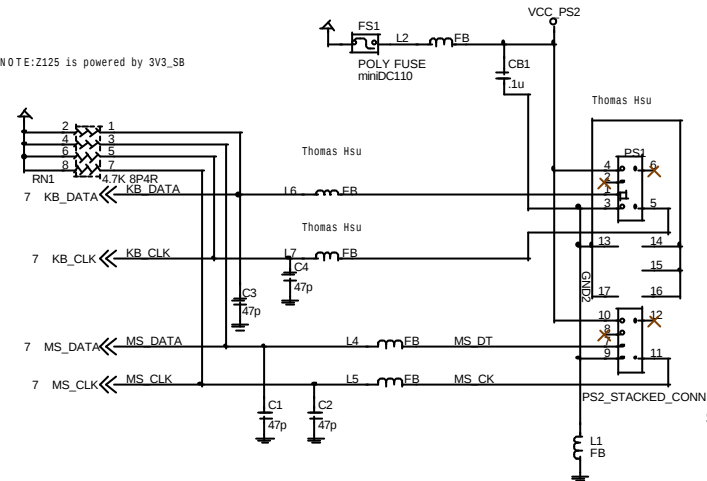


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Title			
ISA SLOT & SYSTEM ROM & AMR SLOT			
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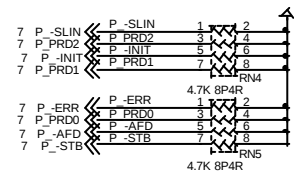
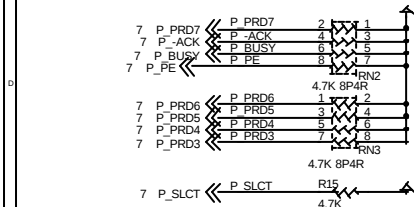
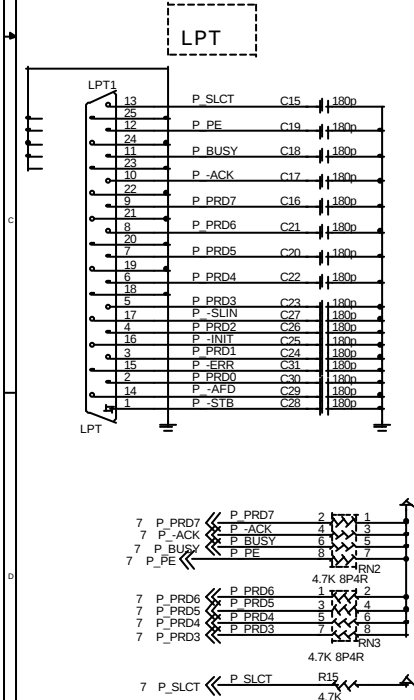
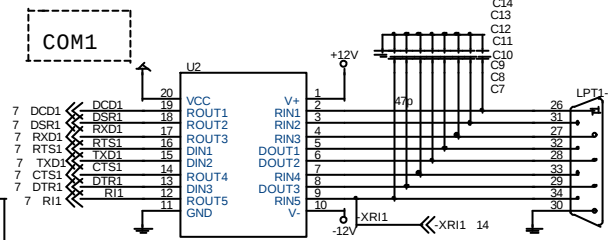
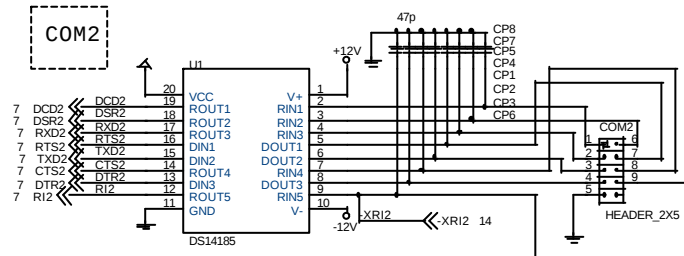
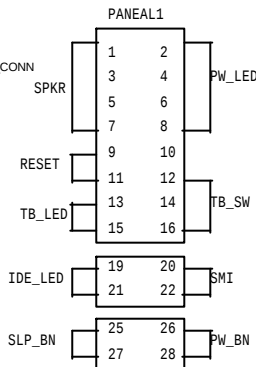


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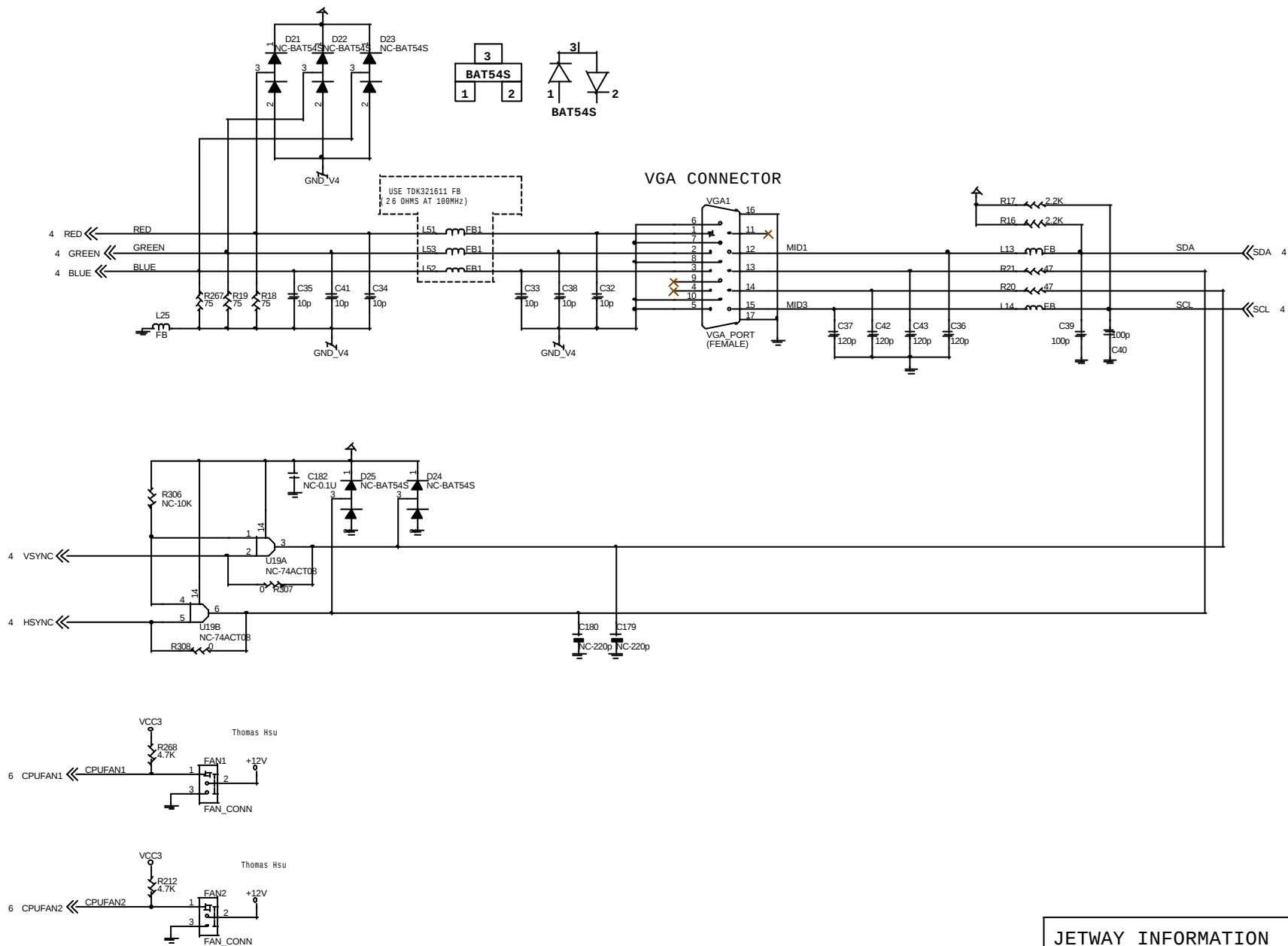
NOTE: Z125 is powered by 3V3_S8



Modification follow requirement of customer



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FAN CONTROL & VGA CONNECTOR		
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